DATA SHEET

BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC3228T5S$

LOW DISTORTION DOWN-CONVERTER + AGC AMPLIFIER + VIDEO AMPLIFIER

DESCRIPTION

NEC

The μ PC3228T5S is a silicon bipolar monolithic IC designed for use as IF down-converter for digital TV, digital CATV. This IC consists of AGC amplifier, mixer and video amplifier.

The package is 32-pin plastic QFN (Quad Flat Non-lead) package suitable for surface mount.

This IC is manufactured using our 30 GHz fmax UHS0 (Ultra High Speed Process) silicon bipolar process.

This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, this IC has excellent performance, uniformly and reliability.

FEATURES

•	Total performance	: Icc = 85 mA TYP. @ Vcc = 5 V
•	AGC AMPLIFIER + MIXER + DRIVER BLOCK	: fre (BW) = 20 to 800 MHz
		: CG = 28 dB TYP.
		: GCR = 70 dB TYP.
		: IM ₃ = 47 dBc MIN./57 dBc TYP. @ Single Ended-OUT = 0.5 $V_{\text{p-p}}/\text{tone}$
•	VIDEO AMPLIFIER BLOCK	: Gv = 59 dB TYP.
		: fif (BW) = 20 to 100 MHz
		: $IM_3 = 45 dBc MIN./55 dBc TYP.$ @ Output = 110 dBu/tone, Differential-out
•	High-density surface mounting	: 32-pin plastic QFN package (5.0 \times 5.0 \times 0.75 mm)

APPLICATION

- Digital CATV
- Cable modem receivers

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPC3228T5S-E2	μPC3228T5S-E2-A	32-pin plastic QFN (Pb-Free)	C3228	 Embossed tape 12 mm wide Pin 8,9 face the perforation side of the tape Qty 2.5 kpcs/reel Dry pack specification

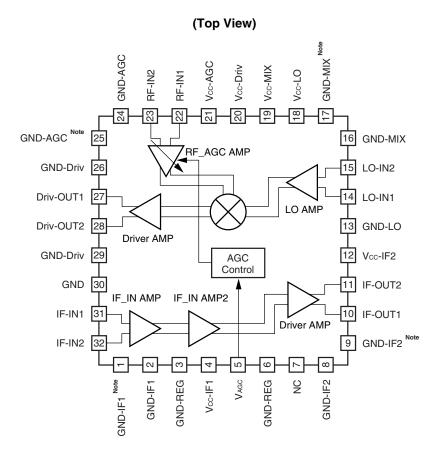
Remark To order evaluation samples, contact your nearby sales office. Part number for sample order: μ PC3228T5S

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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INTERNAL BLOCK DIAGRAM AND PIN CONFIGURATION



Note 1, 9, 17, 25-pin: Connected to the lead frame.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions		Ratings	Unit
Supply Voltage	Vcc	$T_A = +25^{\circ}C$		6.0	V
Power Dissipation	PD	$T_A = +80^{\circ}C$ N	ote	800	mW
Operating Ambient Temperature	TA	N	lote	-20 to +80	°C
Storage Temperature	Tstg			-55 to +150	°C

Note Mounted on double-sided copper-clad $50 \times 50 \times 1.6$ mm epoxy glass PWB

RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc		4.5	5.0	5.5	V
Operating Ambient Temperature	TA	Vcc = 4.5 to 5.5 V	-20	+25	+80	°C
Gain Control Voltage Range	VAGC		0	-	3.3	V
RF Operating Frequency Range	frf (BW)		20	-	800	MHz
IF Operating Frequency Range	fif (BW)		20	-	100	MHz

ELECTRICAL CHARACTERISTICS (TA = +25°C, Vcc = 5 V, unless otherwise specified)

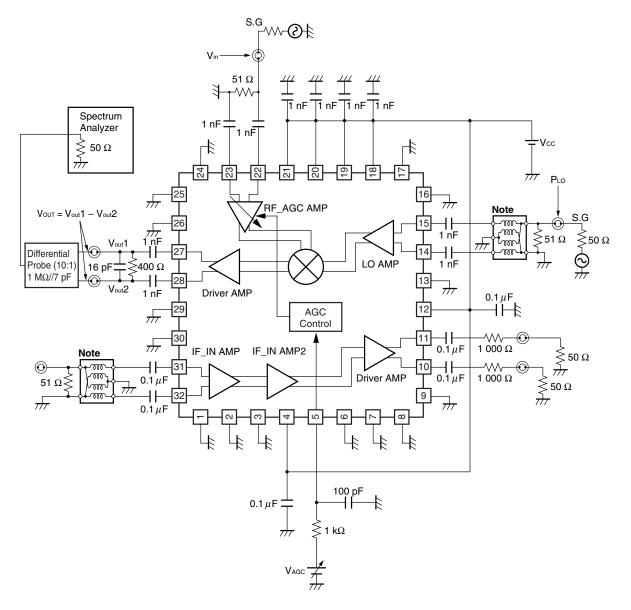
Parameter	Symbol	Test Conditions		MIN.	TYP.	MAX.	Unit
DC Characteristics							
Circuit Current	lcc	No input signal	Note 1	65	85	110	mA
AGC Voltage High Level	VAGC (H)	@ Maximum gain	Note 1	2.5	-	3.5	V
AGC Voltage Low Level	VAGC (L)	@ Minimum gain	Note 1	-	0	-	V
RF Characteristics (RF AGC Amplifier Block + Mixer Block	+ Driver An	nplifier: fr⊧ = 80 MHz, f∟o = 130 M	1Hz, P ∟o =	-10 dBm,	Zs = 50 Ω	, Z∟ = 400 Ω	2/16 pF)
RF Input Frequency Range	fre	fc = -3 dB	Note 1	20	-	800	MHz
RF Gain Control Range	GCR1	V _{AGC} = 0 to 2.5 V	Note 1	62	70	-	dB
Mixer Conversion Gain	CG	V _{AGC} = 2.5 V Differential-IN: V _{in} = +18 dBmV	Note 1	25	28	31	dB
3rd Order Intermodulaion Distortion	IM₃1	f1 = 44 MHz, f2 = 45 MHz, Vin = +30 dBmV/tone, Single Ended-OUT = 0.5 V_{P^-P}/t	tone Note 1	47	57	_	dBc
Noise Figure	NF1	V _{AGC} = 2.5 V, f = 50 MHz, Differential-Output	Note 2	-	8.3	-	dB
IF Characteristics (IF Amplifier Block + Driver Amplifier: f⊮	= 50 MHz,	Zs = 50 Ω, ZL = 2 100 Ω)					
IF Input Frequency Range	fı⊨	fc = −3 dB	Note 5	20	-	100	MHz
IF Amplifier Gain	Gv	V _{in} = -7 dBmV, Differential-IN/OUT	Note 5	56	59	62	dB
3rd Order Intermodulaion Distortion	IM₃2	$\label{eq:states} \begin{array}{l} \text{f1} = 49.5 \text{ MHz}, \text{ f2} = 50.5 \text{ MHz}, \\ V_{\text{out}} = 110 \text{ dBu/tone}, \\ \text{Differential-IN/OUT} \end{array}$	Note 5	45	55	_	dBc
IF Output Voltage	Vout	Single Ended-Output	Note 5	_	1.0	-	V _{p-p}
Noise Figure	NF2	V _{AGC} = 0 V, f = 50 MHz, Single Ended-Output	Note 2	_	3.0	-	dB
Total Block (RF AGC Amplifier + Mixer SAW Filter : EPCOS X6889M (fiF = 49				-			1
LO-RF Leakage	LOrf	V_{AGC} = 2.5 V, 22-pin 75 Ω Terr fLO = 110 to 180 MHz	mination Note 3	-	-54	-44	dBmV
LO-IF Leakage	LOIF	$\label{eq:VAGC} \begin{array}{l} V_{AGC} = 2.5 \ V, \\ V_{out} = 0.7 \ V_{P\text{-}P} \ Single \ Ended - O \\ f_{\text{RF}} = 130 \ \text{MHz}, \ f_{\text{LO}} = 179 \ \text{MHz} \end{array}$	•	_	-40	-25	dBc

Notes 1. By measurement circuit 1

- 2. By measurement circuit 2
- **3.** By measurement circuit 3
- 4. By measurement circuit 4
- 5. By measurement circuit 5

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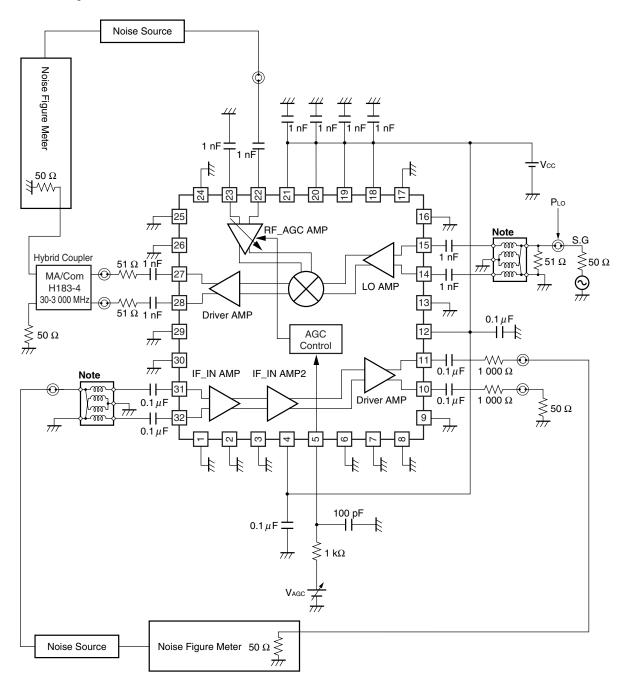
MEASUREMENT CIRCUIT 1



Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

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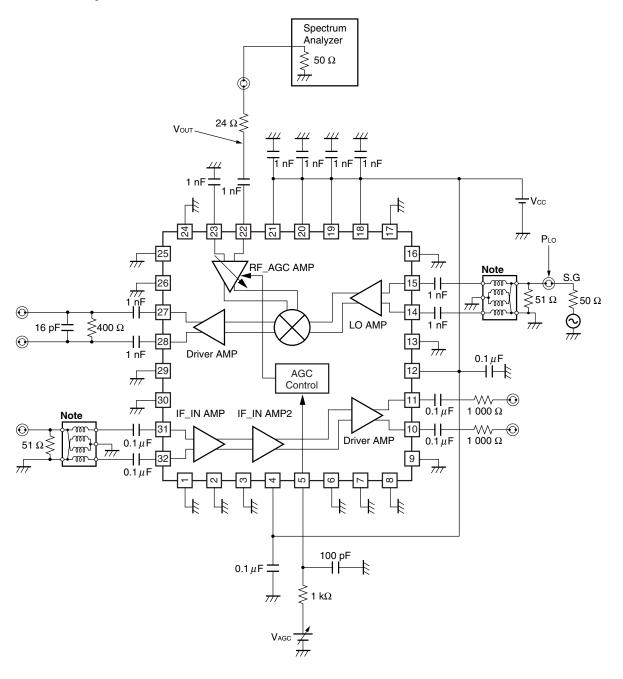
Noise Figure



Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

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Lo-RF Leakage



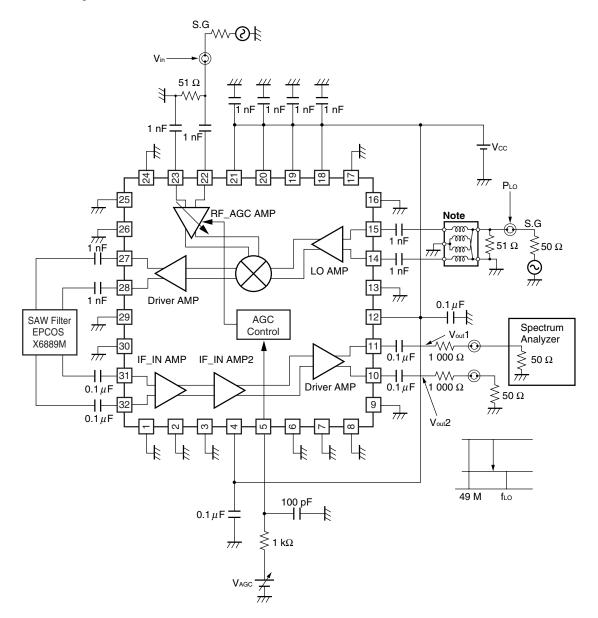
Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

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(fRF = 70 to 130 MHz (fIF = 49.1 MHz \pm 0.6 MHz), fLO = 119 to 179 MHz, PLO = -10 dBm, V_{out} = 0.7 V_{P-P} (Single Ended))

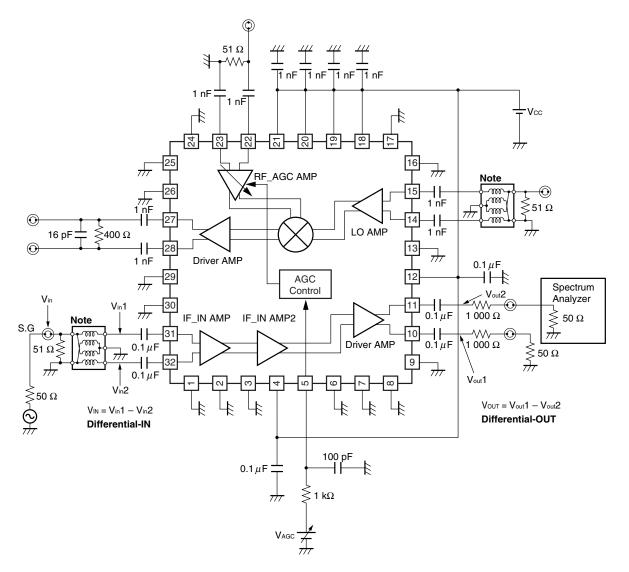
LO-IF Leakage



Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

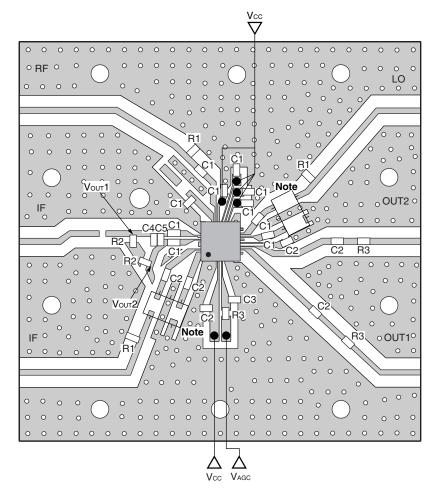
IF Block

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Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

ILLUSTRATION OF THE EVALUATION BOARD



Note Balun Transformer : TOKO 617DB-1674 B4F (Double balanced type)

Remarks

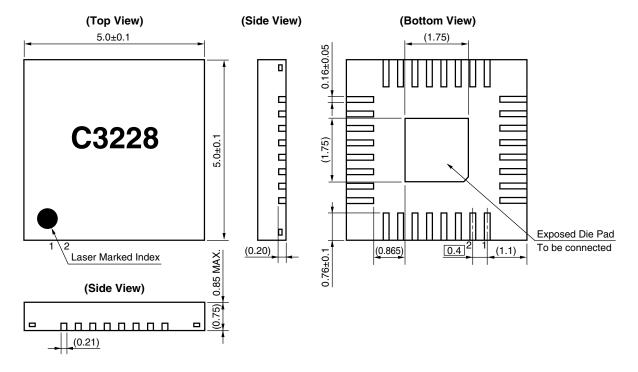
- 1. Back side: GND pattern
- 2. Solder plated on pattern
- 3. $_{o}$ () : Through hole

USING THE NEC EVALUATION BOARD

Symbol	Values	Maker	Part Number	Size
C1	1 nF	Murata	GRM39CH	1608
C2	0.1 <i>µ</i> F	Murata	GRM39B	1608
C3	100 pF	Murata	GRM39CH	1608
C4	10 pF	Murata	GRM36B	1005
C5	6 pF	Murata	GRM36B	1005
R1	51 Ω	Susumu	RR0816 510SSM	1608
R2	200 Ω	Susumu	RR0816 201SSM	1608
R3	1 000 Ω	Susumu	RR0816 102SSM	1608

PACKAGE DIMENSIONS

32-PIN PLASTIC QFN (UNIT: mm)



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NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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