



BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC8232T5N$

SIGe:C LOW NOISE AMPLIFIER FOR GPS

DESCRIPTION

The μ PC8232T5N is a silicon germanium carbon (SiGe:C) monolithic integrated circuit designed as low noise amplifier for GPS. This device exhibits low noise figure and high power gain characteristics, so this IC can improve the sensitivity of GPS receiver. In addition, the μ PC8232T5N which is included output matching circuit contributes to reduce external components and system size.

The package is a 6-pin plastic TSON (<u>Thin Small Out-line Non-leaded</u>) suitable for surface mount. This IC is manufactured using our UHS4 (<u>Ultra High Speed Process</u>) SiGe:C bipolar process.

FEATURES

- Low noise : NF = 0.95 dB TYP. @ fin = 1 575 MHz
- High gain : $G_P = 17 \text{ dB TYP}$. @ fin = 1 575 MHz
- Low current consumption : Icc = 3.0 mA TYP. @ Vcc = 3.0 V
- Built-in power-saving function
- High-density surface mounting : 6-pin plastic TSON package (1.5 \times 1.5 \times 0.37 mm)
- Included output matching circuit
- Included very robust bandgap regulator (Small Vcc and TA dependence)
- Included protection circuits for ESD

APPLICATION

Low noise amplifier for GPS

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
<i>µ</i> РС8232Т5N-Е2	μΡC8232T5N-E2-A	6-pin plastic TSON (Pb-Free)	6L	 8 mm wide embossed taping Pin 1, 6 face the perforation side of the tape Qty 3 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office. Part number for sample order: *µ*PC8232T5N

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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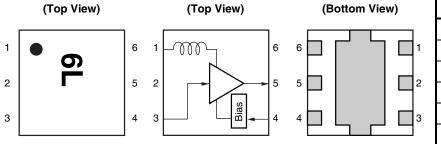
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The mark <R> shows major revised points.

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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name		
1	Vcc		
2	GND		
3	INPUT		
4	Power Save		
5	OUTPUT		
6	Vcc		

Remark Exposed pad : GND

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	Vcc	TA = +25°C	4.0	V
Power-Saving Voltage	VPS	TA = +25°C	4.0	V
Total Power Dissipation	Ptot		150	mW
Operating Ambient Temperature	TA		-40 to +85	°C
Storage Temperature	Tstg		–55 to +150	°C
Input Power	Pin		+10	dBm

RECOMMENDED OPERATING RANGE

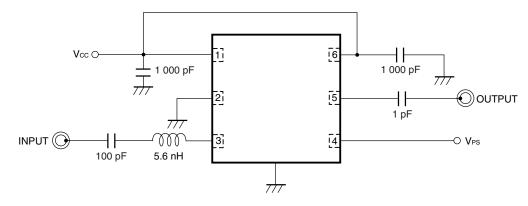
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc	2.5	3.0	3.3	V
Operating Ambient Temperature	TA	-40	+25	+85	°C
Power Save Turn-on Voltage	VPSon	1.6	-	Vcc	V
Power Save Turn-off Voltage	VPSoff	0	1	0.4	V

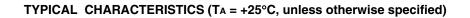
ELECTRICAL CHARACTERISTICS

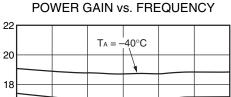
(TA = +25°C, Vcc = VPs = 3.0 V, fin = 1 575 MHz, unless otherwise specified)

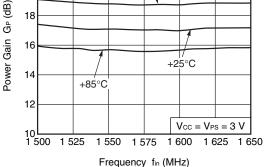
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	lcc	No Signal (VPs = 3.0 V)	2.3	3.0	4.1	mA
		At Power-Saving Mode (VPS = 0 V)	-	-	1	μA
Power Gain	G₽	Pin = -35 dBm	15	17	19	dB
Noise Figure	NF		-	0.95	1.25	dB
Input 3rd Order Intercept Point	IIP₃	fin1 = 1 574 MHz, fin2 = 1 575 MHz	-	-8	-	dBm
Input Return Loss	RLin		7	10	-	dB
Output Return Loss	RLout		10	20	-	dB
Isolation	ISL		-	40	-	dB
Gain 1 dB Compression Input Power	Pin (1 dB)		-	-21	-	dBm

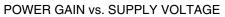
TEST CIRCUIT

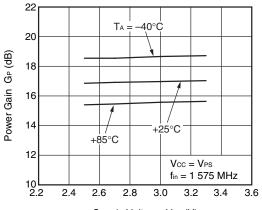


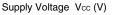




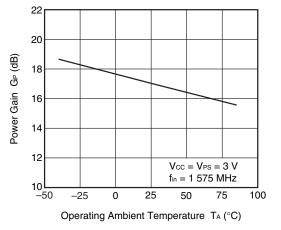




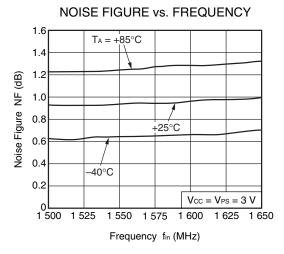




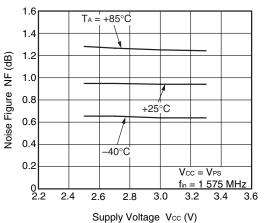




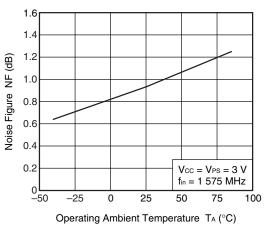




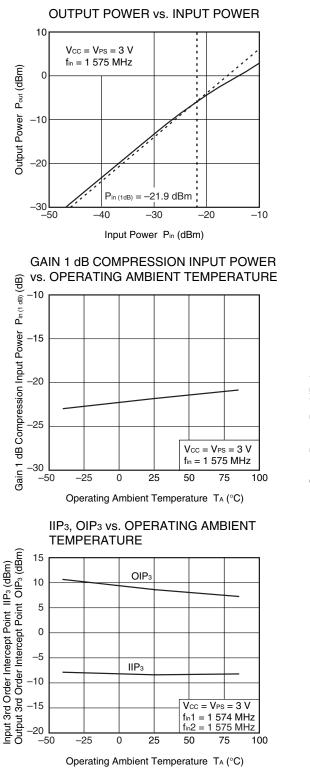
NOISE FIGURE vs. SUPPLY VOLTAGE



NOISE FIGURE vs. OPERATING AMBIENT TEMPERATURE

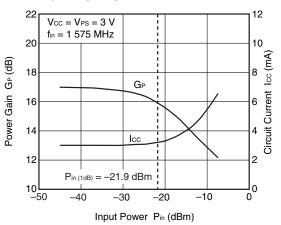


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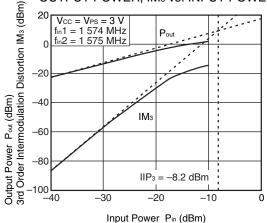


Remark The graphs indicate nominal characteristics.

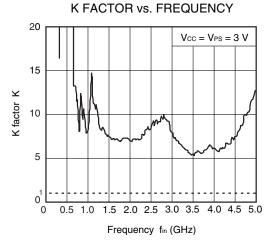
POWER GAIN, CIRCUIT CURRENT vs. INPUT POWER



OUTPUT POWER, IM3 vs. INPUT POWER







CIRCUIT CURRENT vs. SUPPLY VOLTAGE .TA = +85°C 4 Circuit Current loc (mA) 3 2 25°C -40°C 1 Vcc = Vps RF = off0∟ 2.0 2.5 3.0 3.5 4.0 Supply Voltage Vcc (V) CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE 5 Λ Circuit Current Icc (mA) 3 2 $V_{CC} = V_{PS} = 3 V$ RF = off

> 25 Operating Ambient Temperature T_A (°C)

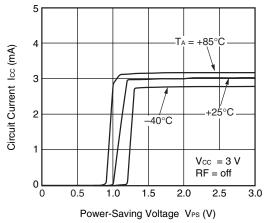
50

75

100

Remark The graphs indicate nominal characteristics.

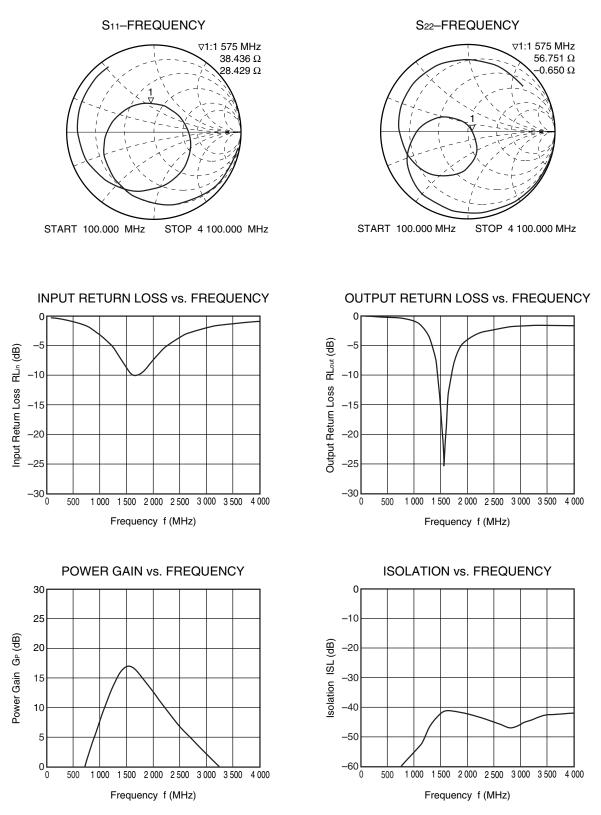




ol -50

-25

0



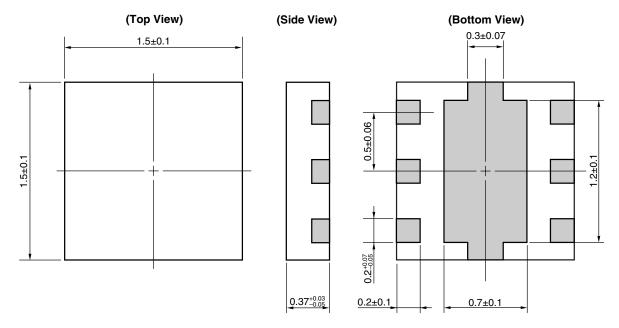
S-PARAMETERS (T_A = +25°C, Vcc = V_{PS} = 3.0 V, monitored at connector on board)

Remark The graphs indicate nominal characteristics.

Data Sheet PU10672EJ02V0DS

<R> PACKAGE DIMENSIONS

6-PIN PLASTIC TSON (UNIT: mm)



NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) Do not supply DC voltage to INPUT pin.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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