

MOS INTEGRATED CIRCUIT Bipolar Analog Integrated Circuit $\mu PC1555$

TIMER CIRCUIT

The μ PC1555 is a powerful integrated circuit. Adding a few external parts to it can turn it into various types of timing signal generators, such as monostable and astable multivibrators. It has trigger, threshold, and control pins. Inputting a signal to the reset pin can stop the circuit operation easily. In addition, the output can sink current as high as 200 mA (maximum). So, it can be used to drive relays and lamps.

TYPICAL CHARACTERISTICS

- Supply voltage : 4.5 to 16 V
- Circuit current (Vcc = 5 V) : 3 mA
- Output current capacity : 200 mA
- Temperature stability : 0.005%/°C
- Rising and falling time : 100 ns

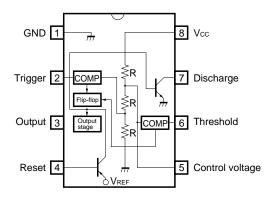
ORDERING INFORMATION

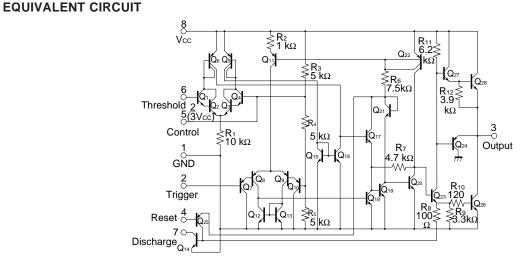
Part number	Package		
μPC1555C	8-pin plastic DIP (300 mil)		
μPC1555G2	8-pin plastic SOP (225 mil)		

FEATURES

- · Monostable and astable oscillation
- · Interfacing directly with TTL-level signals
- · Variable duty cycle

PIN CONFIGURATION (TOP VIEW)





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ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Devenueion	Cumhal	Rated	11.2	
Parameter	Symbol	μPC1555C	μPC1555G	Unit
Supply voltage	Vcc	-0.3 to +18	-0.3 to +18	V
Input voltage (trigger, threshold, reset, control)	Vin	-0.3 to Vcc + 0.3	-0.3 to Vcc + 0.3	V
Applicable output voltageNote 4 (output and discharge)	Vo	-0.3 to Vcc + 0.3	-0.3 to Vcc + 0.3	V
Output current	lo	200Note 1	200Note 1	mA
Power dissipation	Рт	600Note 2	440Note 3	mW
Operating temperature	TA	-20 to +80	-20 to +80	°C
Storage temperature	Tstg	-55 to +125	-55 to +125	°C

Notes 1. Be sure to use the product within the Power dissipation.

- 2. For $T_A \ge 25^\circ C,$ the total loss is derated at T_J $_{MAX}$ = 125°C and -6 mW/°C. (See the PT-TA characteristic curve.)
- **3.** For $T_A \ge 25^{\circ}C$, the total loss is derated at $T_J \text{ MAX} = 125^{\circ}C$ and $-4.4 \text{ mW/}^{\circ}C$. (See the PT-TA characteristic curve.)
- **4.** This is an external voltage that can be applied to the output pin without deteriorating the quality of the product or causing damage to the product.

Be sure to use the product within the rated value under any conditions where coils are inserted or power is turned on or off. The output voltage that can be obtained during normal operation is within the output saturation voltage range.

Parameter	Symbol	Conditions	MIN.	MAX.	Unit
Supply voltage	Vcc		4.5	16	V
Oscillation frequency	f	Vcc = 5 to 15 V	0.1	100 k	Hz
Output pulse width	tw (out)	Vcc = 5 to 15 V	10 <i>µ</i>	10	Sec
Input voltage (trigger, threshold)	Vin		0	Vcc	V
Input voltageNote 5 (control)	Vin		3.0	Vcc • 1.5	V
Reset voltage (high level)	Vreset H	Vcc = 5 to 15 V	1.0	Vcc	V
Reset voltage (low level)	Vreset L	Vcc = 5 to 15 V	0	0.4	V

RECOMMENDED OPERATING CONDITIONS (TA = 25°C)

Note 5. This parameter defines the voltage that can be applied when a PWM mode application circuit is configured by applying an external voltage to the control pin. Usually, a capacitance of 0.01 μ F is connected as shown in the application circuit.

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ELECTRICAL CHARACTERISTICS (TA = 25°C, Vcc = 5 to 15 V)

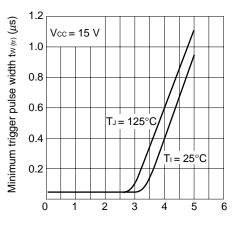
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Supply voltage	Vcc		4.5		16	V
Supply current	lcc	$V_{CC} = 5 \text{ V}, \text{ R}_{L} = \infty, \text{ V}_{O} = \text{``L''Note 6}$	0	3	6	mA
		Vcc = 15 V, RL = ∞, Vo = "L"Note 6	0	10	15	mA
Threshold voltage	Vth			2/3 Vcc		V
Threshold current	Ith	Note 7	0	0.1	0.25	μA
Trigger voltage	Vtr	Vcc = 15 V		5		V
		Vcc = 5 V		1.67		V
Trigger current	Itr			0.5		μΑ
Reset voltage	Vreset	Note 8	0.4	0.7	1.0	V
Reset current	Ireset			0.1		mA
Control voltage	Vcont	Vcc = 15 V	9.0	10	11	V
		Vcc = 5 V	2.6	3.33	4	V
Output saturation voltage "L"	Vol	Vcc = 15 V, Isınк = 10 mA	0	0.1	0.25	V
		Vcc = 15 V, Isınк = 50 mA	0	0.4	0.75	V
		Vcc = 15 V, Isınк = 100 mA	0	2.0	2.5	V
		Vcc = 15 V, Isınк = 200 mA		2.5		V
		Vcc = 5 V, Isinк = 5 mA	0	0.1	0.35	V
Output saturation voltage "H"	Vон	Vcc = 15 V, Isource = 200 mA		12.5		V
		Vcc = 15 V, Isource = 100 mA	12.75	13.3	15.0	V
		Vcc = 5 V, Isource = 100 mA	2.75	3.3	5.0	V
Propagation delay (L \rightarrow H)	tрын			200		ns
Propagation delay (H \rightarrow L)	t PHL			200		ns
Minimum trigger pulse width	tw (tr)	$V_{CC} = 15 \text{ V}, \text{ V}_{tr min.} = 2.5 \text{ V}$		25		ns
Minimum output pulse width	tw (оит)	Vcc = 15 V, Vtr min. = 2.5 V tw (tr) = 3 μ s		6		μs
Minimum reset pulse width	tw (reset)	$V_{CC} = 15 \text{ V}, \text{ V}_{tr min.} = 0 \text{ V}$		900		ns
Timing error		Astable multivibrator				
Initial accuracy		R_A , $R_B = 1$ to 100 k Ω		1		%
Temperature drift		C = 0.1 μF		50		ppm/°C
Supply voltage drift				0.01		%/V

Notes 6. When the output is "H", the circuit current decreases by approximately 1 mA (when Vcc = 5 V).

- 7. The maximum allowable value for $R_A + R_B$ is determined for a supply voltage of 15 V. The maximum value is 20 M Ω .
- **8.** When the reset pin is driven to a low level, discharge T_rQ₁₄ is turned on, stopping oscillation (the output state is undefined).

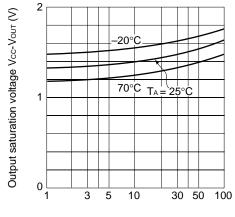
CHARACTERISTIC CURVES (TA = 25°C, TYP.)

Minimum trigger pulse width characteristic



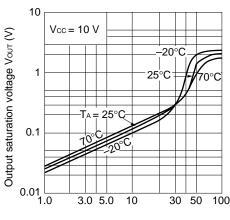
Minimum trigger pulse voltage Vtr min. (V)

ISOURCE-(VCC-VOUT) characteristic



Output source current ISOURCE (mA)

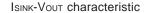
ISINK-VOUT characteristic

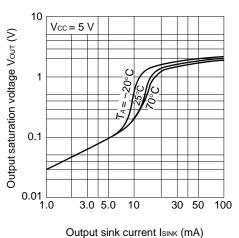


Output sink current ISINK (mA)

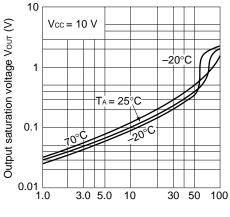
Icc-Vcc characteristic

Supply voltage Vcc (V)





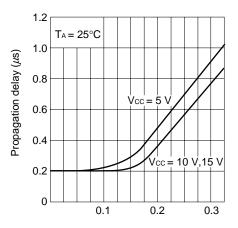
ISINK-VOUT characteristic



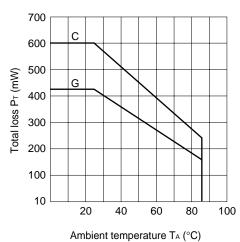
Output sink current ISINK (mA)

4



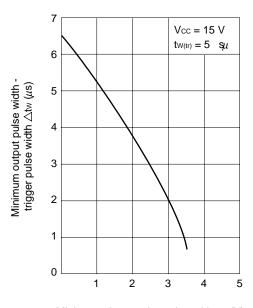


Minimum trigger pulse voltage (×Vcc)



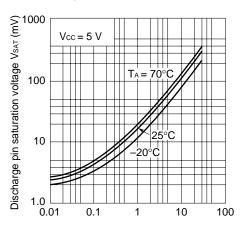






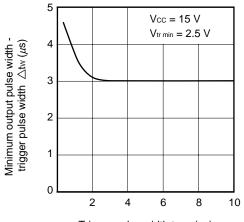
Minimum trigger pulse voltage Vtr min. (V)

Discharge pin ISINK-VSAT characteristic



Discharge pin (pin 7) sink current ISINK (mA)

△tw-tw (tr) characteristic



Trigger pulse width tw (tr) (μ s)

NEC

PIN FUNCTIONS	
1. Trigger pin (pin 2) :	Supplying one-third of Vcc to the trigger pin triggers the circuit, changing the output voltage from low to high.
2. Output pin (pin 3) :	The maximum output current is 200 mA. Be careful not to exceed the total loss (see the PT-TA characteristic curve).
3. Reset pin (pin 4) :	Supplying 0.4 V or less to the reset pin stops the circuit operation (such as monostable or astable multivibrator operation).
4. Control voltage (pin 5) :	When not used, the reset pin should be clamped at 1 V to Vcc. This voltage determines the threshold level of the comparator. It is set to two-thirds of Vcc. It is possible to configure a PWM (pulse width modulation) or PPM (pulse position modulation) mode application circuit by supplying a control voltage from the outside. When this pin is not in use, it should be bypassed using a capacitor of approximately 0.01 μ F for more table circuit operation.
5. Threshold pin (pin 6) :	The values of an external capacitor (C) and resistor (R) connected to this pin determine the width of the output pulse.
6. Discharge pin (pin 7) :	This pin is used to discharge an external capacitor (if connected). It operates, when the internal flip-flop circuit is turned on, or a reset signal is applied.

APPLICATION CIRCUITS

(1) Monostable multivibrator

Vcc = 5 to 15 V \bigcirc Note 10 R∟≷ ŚR₁ 8 4 TRIGGER μPC1555 C_1 6 OUTPUT 5 π Control voltage 0.01 μF π \overline{T} 11.

When the μ PC1555 is configured as shown in Fig. a, it functions as a monostable multivibrator. Applying a voltage one-third as high as Vcc or less (trigger pulseNote 9) to pin 2 (trigger pin) drives the output to a high level. Under this condition, capacitor C1 starts charging through resistor R1. When C1 is charged up to two-thirds as high as Vcc, pin 6 (threshold pin) is turned on and inverted to a low level. At this point, C1 starts discharging through pin 7. When a trigger pulse is applied to pin 2 again, the same operation is repeated. Fig. b shows this operation. A capacitor connected to pin 5 functions as a nose filter for the control voltage. If pin 4 (reset pin) is connected to 1 V or higher (for example, by being connected to Vcc), the circuit operation can be stopped by switching it from 2 V or higher to a GND level.

The output pulse width (delay) is determined theoretically by (see Fig. c):

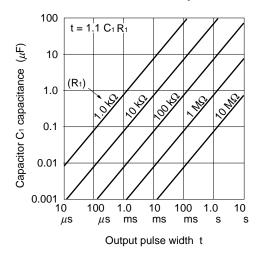
 $t=1.1\boldsymbol{\cdot} C_1\boldsymbol{\cdot} R_1$

t = 0.1 ms/DIV Trigger input voltage: 5 V/DIV Output voltage: 5 V/DIV "H" "H" "L" "L" Capacitor (C1) voltage: 2 V/DIV

Fig. b Monostable Response Waveform

 $(R_1 = 9.1 \text{ k}\Omega, C_1 = 0.01 \mu\text{F}, R_L = 1 \text{ k}\Omega)$

Fig. c Interrelationships among Output Pulse Width, R₁, and C₁ (approximate value obtained by calculation)



The value obtained by this equation is only an approximate value, however. If it is necessary to obtain an accurate output pulse width, determine R₁ and C₁ through actual measurement and confirmation; a trimmer should be used as required. Moreover, R₁ should be 300 Ω or higher.

Notes 9. Keep the trigger pulse width smaller than the output pulse width.

10. If the load is connected across the output and GND pins, a "staircase" occurs in the output waveform.

The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

Fig. a Monostable Multivibrator Example

(2) Astable multivibrator example

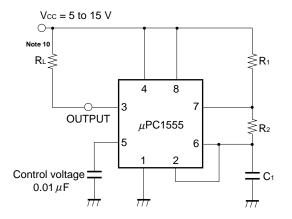


Fig. d Astable Multivibrator Example

When the μ PC1555 is used in a circuit configuration shown in Fig. d, the circuit is triggered by itself to operate as an astable multivibrator, because pin 2 (trigger pin) and pin 6 (threshold pin) are connected to each other. When the output voltage is high, capacitor C1 is charged through R1 and R2. When C1 is charged up to a voltage two-thirds as high as Vcc, the threshold pin is turned on, and the output pin becomes low. At this point C1 starts discharging through R2. When C1 discharges, and the voltage across C1 decreases to a voltage one-third as high as Vcc, the trigger pin is turned on, and the output voltage becomes high, causing the charge current to flow into C1 through R1 and R2 again. This operation is shown in Fig. e. Because C1 repeats charging and discharging between one-third as high as Vcc and two-thirds as high as Vcc, the oscillation frequency is not affected by the supply voltage.

Oscillation is represented theoretically using the following expressions.

When the output voltage is high, the charge time is : When the output voltage is low, the discharge time is : Adding expressions (1) and (2) determines period T : Therefore, the oscillation frequency is (see Fig. f for reference)

The duty cycle is determined by the equation (5)

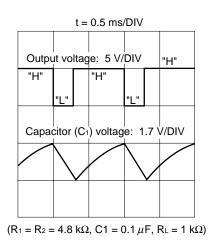
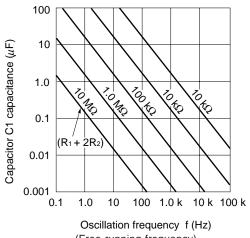
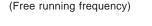


Fig. e Astable Multivibrator Response Waveform

Fig. f Interrelationships among Oscillation Frequency, R1, R2, and C1 (approximate value obtained by calculation)





$t_1 = 0.693 (R_1 + R_2)$) C1	(1)
$t_2 = 0.693 \cdot R_2 \cdot C_1$		(2)

: $T = t_1 + t_2 = 0.693 (R_1 + 2R_2) C_1$ (3)

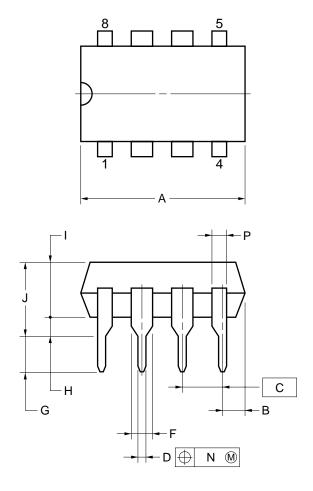
$$f = \frac{1}{T} = \frac{1.44}{(R_1 + 2R_2) C_1}$$
(4)

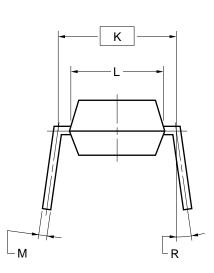
$$D = \frac{R_2}{R_1 + 2R_2}$$
 (5)

The values obtained this way are approximate values, however. If it is necessary to obtain an accurate oscillation frequency, determine R₁, R₂, and C₁ through actual measurement and confirmation; a trimmer should be used as required. Moreover, R₁ and R₂ should be 300 Ω or higher.

Note 10. If the load is connected across the output and GND pins, a "staircase" occurs in the output waveform.

8PIN PLASTIC DIP (300 mil)





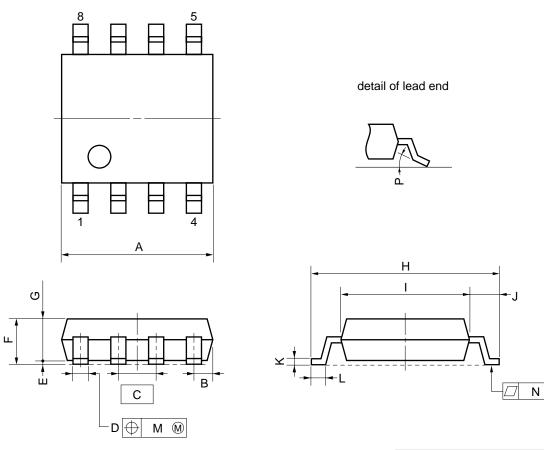
NOTES

- 1) Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS	INCHES
А	10.16 MAX.	0.400 MAX.
В	1.27 MAX.	0.050 MAX.
С	2.54 (T.P.)	0.100 (T.P.)
D	0.50±0.10	$0.020^{+0.004}_{-0.005}$
F	1.4 MIN.	0.055 MIN.
G	3.2±0.3	0.126±0.012
Н	0.51 MIN.	0.020 MIN.
I	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
К	7.62 (T.P.)	0.300 (T.P.)
L	6.4	0.252
М	$0.25^{+0.10}_{-0.05}$	$0.010^{+0.004}_{-0.003}$
N	0.25	0.01
Р	0.9 MIN.	0.035 MIN.
R	0~15°	0~15°
		20C 400 200B C 4

P8C-100-300B,C-1

8 PIN PLASTIC SOP (225 mil)



NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
А	5.37 MAX.	0.212 MAX.
В	0.78 MAX.	0.031 MAX.
С	1.27 (T.P.)	0.050 (T.P.)
D	$0.40^{+0.10}_{-0.05}$	$0.016\substack{+0.004\\-0.003}$
Е	0.1±0.1	0.004±0.004
F	1.8 MAX.	0.071 MAX.
G	1.49	0.059
Н	6.5±0.3	0.256±0.012
I	4.4	0.173
J	1.1	0.043
к	$0.15^{+0.10}_{-0.05}$	$0.006\substack{+0.004\\-0.002}$
L	0.6±0.2	$0.024^{+0.008}_{-0.009}$
М	0.12	0.005
N	0.10	0.004
Р	3° ^{+7°} -3°	3° ^{+7°} 3°

S8GM-50-225B-4

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RECOMMENDED SOLDERING CONDITIONS

The conditions listed below shall be met when soldering the μ PC1555.

Please consult with our sales offices in case any other soldering process is used, or in case soldering is done under different conditions.

Surface-Mount Devices

For details of the recommended soldering conditions, refer to our document *SMD Surface Mount Technology Manual* (IEI-1207).

µPC1555G2

Soldering process	Soldering conditions	Symbol
Infrared reflow	Peak package's surface temperature: 230°C Reflow time: 30 seconds or less (at 210°C or more) Maximum allowable number of reflow processes: 1 Exposure limit: None ^{Note}	IR30-00
VPS	Peak package's surface temperature: 215°C Reflow time: 40 seconds or less (at 200°C or more) Maximum allowable number of reflow processes: 1 Exposure limit: None ^{Note}	VP15-00
Wave soldering	Temperature in the soldering vessel: 260°C or less Soldering time: 10 seconds or less Maximum allowable number of reflow processes: 1 Exposure limit: None ^{Note}	WS60-00
Partial heating method	Pin temperature: 300°C or less Flow time: 10 seconds or less Exposure limit: None ^{Note}	

Note Exposure limit before soldering after dry-pack package is opened.

Storage conditions: Temperature of 25°C or less and maximum relative humidity of 65% or less

Caution Do not apply more than a single process at once, except for "Partial heating method."

Through-Hole Mount Devices

μPC1555C

Soldering process	Soldering conditions	
Wave soldering	Temperature in the soldering vessel: 260°C or less Soldering time: 10 seconds or less	

REFERENCE

Document name	Document No.
NEC Semiconductor Device Reliability/Quality Control System	IEI-1212
Quality Grade on NEC Semiconductor Devices	IEI-1209
Semiconductor Device Mounting Technology Manual	IEI-1207
Semiconductor Device Package Manual	IEI-1213
Guide to Quality Assurance for Semiconductor Devices	MEI-1202
Semiconductor Selection Guide	MF-1134

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- Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
- Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
- Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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Anti-radioactive design is not implemented in this product.

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