

BIPOLAR ANALOG INTEGRATED CIRCUIT μ PC2766GR/GS

WIDE BAND IQ DEMODULATOR FOR DIGITAL VIDEO/DATA RECEIVER

DESCRIPTION

The μ PC2766GR/GS is a Silicon monolithic IC designed for use as IQ demodulator in wide dynamic range compressed video or spread spectrum receivers. This IC consists of a wide band RF amplifier, gain control amplifier, dual balanced mixers (DBM), Lo buffers, and I & Q output buffer amplifiers.

The package is 20 pin SSOP (shrink small outline package: μ PC2766GR) or 20 pin SOP (μ PC2766GS) suitable for high-density surface mount.

FEATURES

Broadband operation
 RF & LO up to 1 000 MHz

IF (IQ) up to 200 MHz

• Wideband IQ phase and amplitude balance Amplitude balance ±0.3 dB (TYP.)

Phase balance ± 0.3 degree (TYP.)

AGC dynamic range 45 dB
 Low distortion; IM₃ 30 dBc
 Supply Voltage 5 V

· Packaged in 20 pin SSOP or 20 pin SOP suitable for high-density surface mount

ORDERING INFORMATION

PART NUMBER	PACKAGE	PACKAGE STYLE
μPC2766GR-E1	20 pin plastic SSOP (225 mil)	Embossed tape 12 mm wide. 2.5 k/REEL
		Pin 1 indicates pull-out direction of tape
μPC2766GS-E1	20 pin plastic SOP (300 mil)	Embossed tape 24 mm wide. 2.5 k/REEL
		Pin 1 indicates pull-out direction of tape

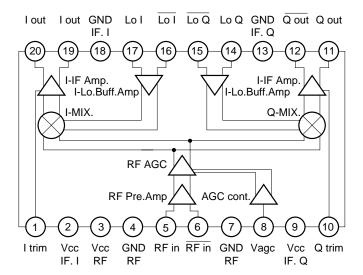
Caution electro-static sensitive device

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.



INTERNAL BLOCK DIAGRAM





PIN FUNCTIONS

PIN No.	PIN NAME	PIN VOLTAGE TYP. (V)	FUNCTION AND EXPLANATION	EQUIVALENT CIRCUIT
1	I Trim	4.2	Trimming pin for I-IF output.	Vcc
2	VccIF I	5.0	Power supply pin for I-MIXER.	
3	VccRF	5.0	Power supply pin for RF and AGC block.	
4	GND RF	0.0	Ground pin of RF and AGC block.	
5	RFin	2.6	RF input pin. In case of single input, 6 pin should be grounded through capacitor.	To next block
6	RFin	2.6		5 7 6
7	GND RF	0.0	Ground pin of RF and AGC block.	
8	Vagc	0 to 5	Gain control pin. • VAGC = 0 V: Full gain • VAGC = 5 V: Maximum reduction	8-W
9	VccIF Q	5.0	Power supply pin for Q-MIXER.	
10	Q Trim	4.2	Trimming pin for Q-IF output.	10 Vcc



PIN No.	PIN NAME	PIN VOLTAGE TYP. (V)	FUNCTION AND EXPLANATION	EQUIVALENT CIRCUIT
11	Qout	3.3	Q-IF output pin. 11 pin and 12 pin are balance outputs.	Vcc Vcc 12 Trom before
12	Qout	3.3		Delock
13	GNDIF Q	0.0	Ground pin of Q-IF block.	
14	Lo Q	2.2	Oscillator signal input pin of Q-MIXER. In case of single input, 15 pin should be grounded through capacitor.	Vcc
15	Lo Q	2.2		
16	Lo I	2.2	Oscillator signal input pin of I-MIXER. In case of single input, 16 pin should be grounded through capacitor.	Vcc
17	Lo I	2.2		
18	GNDIF I	0.0	Ground pin of I-IF block.	
19	lout	3.3	I-IF output pin. 19 pin and 20 pin are balance outputs.	Vcc Vcc Prom before
20	lout	3.3		e block



ABSOLUTE MAXIMUM RATINGS (TA = 25 $^{\circ}$ C)

μ PC2766GR

PARAMETER	SYMBOL	RATING	UNIT	TEST CONDITIONS
Supply voltage	Vcc	6.0	V	
Power dissipation	Pp	430	mW	T _A = 85 °CNote 1
Operating temperature range	TA	-40 to +85	°C	
Storage temperature range	T _{stg}	-55 to +150	°C	

μ PC2766GS

PARAMETER	SYMBOL	RATING	UNIT	TEST CONDITIONS
Supply voltage	Vcc	6.0	V	
Power dissipation	PD	650	mW	T _A = 85 °CNote 1
Operating temperature range	TA	-40 to +85	°C	
Storage temperature range	Tstg	-55 to +150	°C	

Note 1 Mounted on $50 \times 50 \times 1.6$ mm double epoxy glass board.

RECOMMENDED OPERATING RANGE

μ PC2766GR/GS

PARAMETER	SYMBOL	MAX.	TYP.	MIN.	UNIT
Supply voltage	Vcc	4.5	5.0	5.5	V
Operating temperature range	TA	-40	+25	+85	°C



ELECTRICAL CHARACTERISTICS (Vcc = 5 V, Ta = 25 $^{\circ}$ C,ZL = 250 Ω)

μ PC2766GR/GS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Supply current	Icc	-	60	78	mA	no input signal
RF input bandwidth	RF BW	DC - 750	DC - 1000	_	MHz	$f_{\text{IF}} = 40 \text{ MHz}, f_{\text{RF}} > f_{\text{LO}}$ IQ phase balance $\leq \pm 1.5^{\circ}$
IF output bandwidth	IF BW	DC	200	-	MHz	$f_{RF} = 480 \text{ MHz}, P_{LO} = -10 \text{ dBm}$ $f_{RF} > f_{LO}, -3 \text{ dB down}, Vagc = 0 \text{ V}$
Gain control range	Ggc	40	45	-	dB	$f_{RF} = 480$ MHz, $f_{IF} = 40$ MHz $P_{RF} = -30$ dBm, $P_{LO} = -10$ dBm Vagc = 0 - 5 V
IQ phase balance	$\Delta\phi$	-	±0.3	±1.5	deg	$f_{RF} = 480 \text{ MHz}, f_{IF} = 40 \text{ MHz}$ $P_{RF} = -30 \text{ dBm}, P_{LO} = -10 \text{ dBm}$
IQ amplitude balance	ΔG	_	±0.3	±0.5	dB	$f_{RF}=480$ MHz, $f_{IF}=40$ MHz $P_{RF}=-30$ dBm, $P_{LO}=-10$ dBm Vagc=0 V
Output voltage	Vo	1.2	1.5	-	V _{P-P}	$f_{RF} = 480 \text{ MHz}, f_{IF} = 40 \text{ MHz}$ $P_{LO} = -10 \text{ dBm}, Z_{L} = 250 \Omega$
Conversion gain	CG	15	20	25	dB	f _{RF} = 480 MHz, f _{IF} = 40 MHz Vagc = 0 V

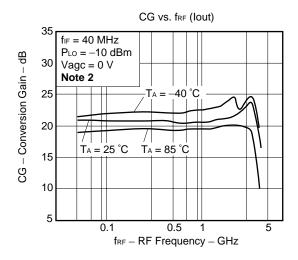
STANDARD CHARACTERISTICS (REFERENCE VALUES) (Vcc = 5 V, TA = 25 $^{\circ}$ C,ZL = 250 Ω)

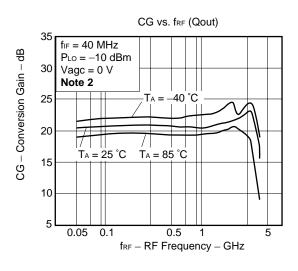
μ PC2766GR/GS

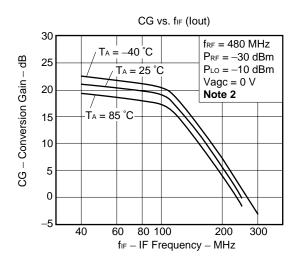
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Noise figure	NF	-	21	-	dB	$f_{RF} = 480 \text{ MHz}, f_{IF} = 40 \text{ MHz}$ $P_{LO} = -10 \text{ dBm}, Vagc = 0 \text{ V}$
LO to RF isolation	LO-RFisol	_	55	-	dB	$f_{LO} = 440 \text{ MHz}, P_{LO} = -10 \text{ dBm}$ $Vagc = 0 \text{ V}$
LO to IF isolation	LO-IFisol	-	10	_	dB	$f_{LO} = 440 \text{ MHz}, P_{LO} = -10 \text{ dBm}$ Vagc = 0 V
3rd order intermodulation distortion	IMз	-	30	-	dBc	f _{RF1} = 480 MHz, f _{RF2} = 490 MHz f _{LO} = 440 MHz, Pin = -20 dBm P _{LO} = -10 dBm, Vagc = 1 V
Saturated output level	Po(SAT)	-	+2	-	dBm	$f_{RF} = 480 \text{ MHz}, f_{IF} = 40 \text{ MHz}$ $P_{RF} = -10 \text{ dBm}$

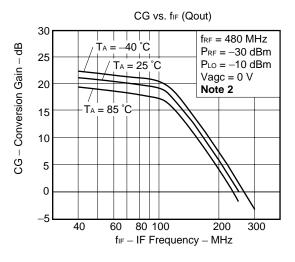


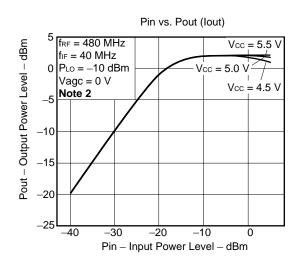
TYPICAL CHARACTERISTICS - on Measurement Circuit - (Note 2 Lower local)

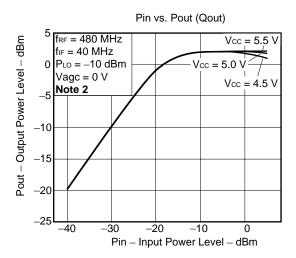


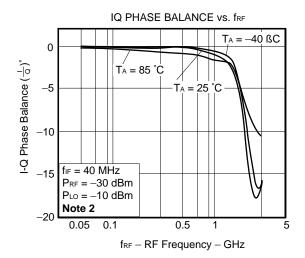


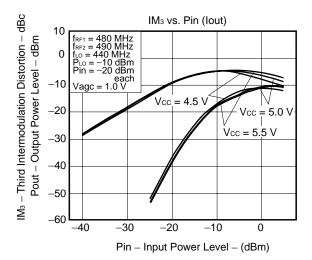


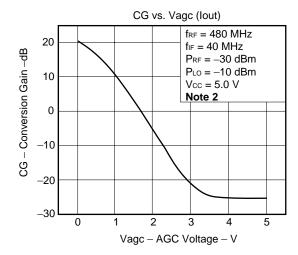


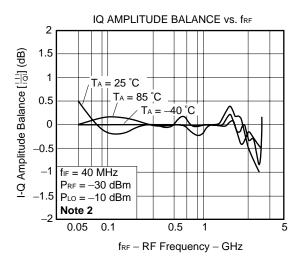


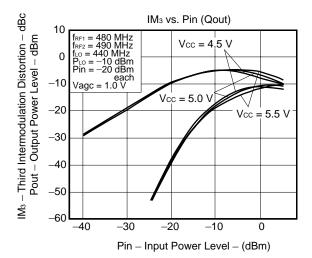


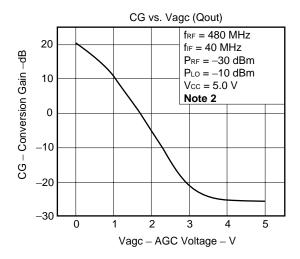


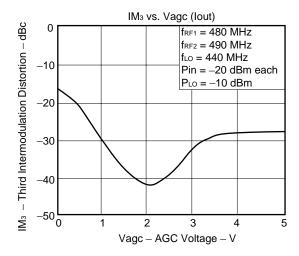


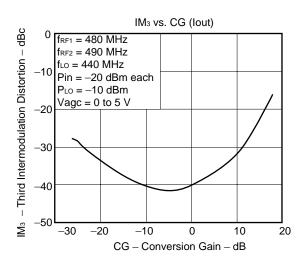


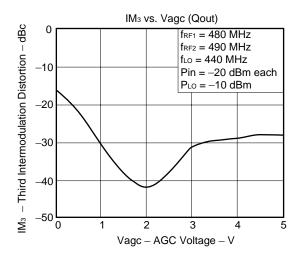


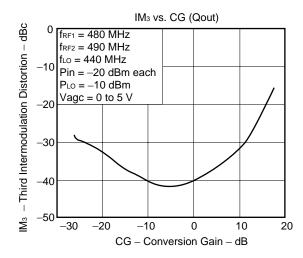








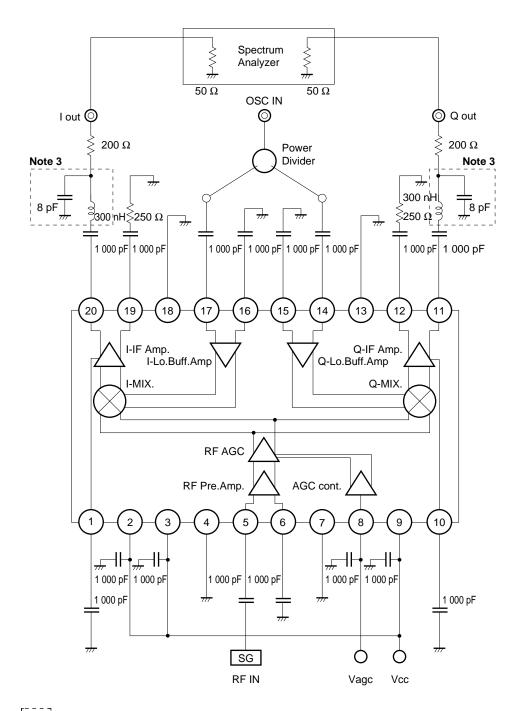






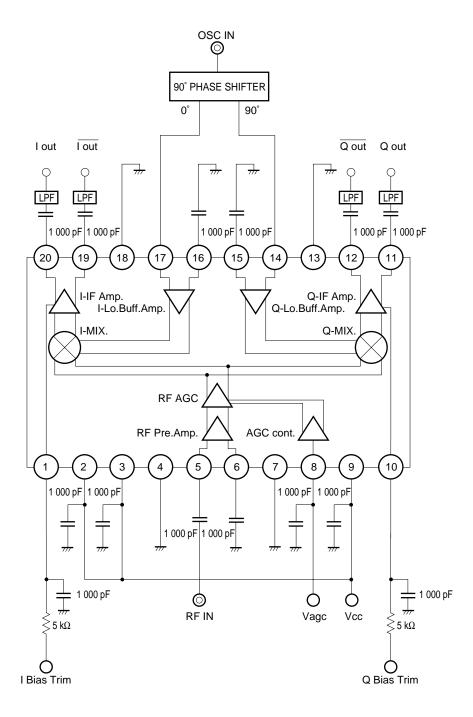
MEASUREMENT CIRCUIT

(@ $Z_L = 250 \Omega$)



Note 3 [___] is Low pass filter in order to eliminate local leak.

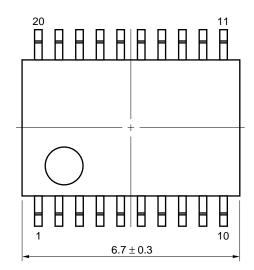
APPLICATION CIRCUIT EXAMPLE





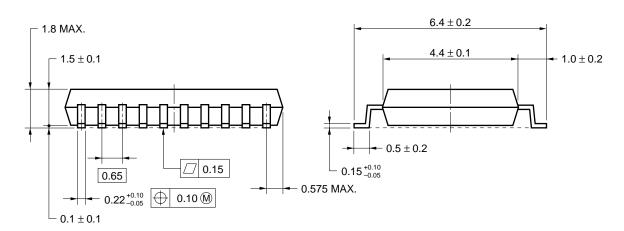
PACKAGE DIMENSIONS

★ 20 PIN PLASTIC SSOP (225 mil) (UNIT: mm)



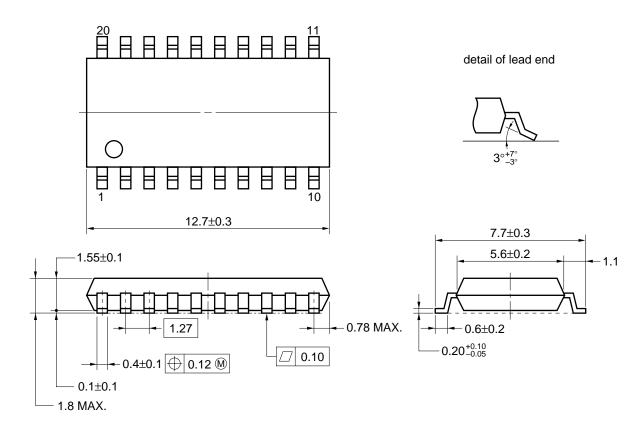
detail of lead end





NOTE Each lead centerline is located within 0.10 mm of its true position (T.P.) at maximum material condition.

★ 20 PIN PLASTIC SOP (300 mil) (UNIT: mm)



NOTE Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.



RECOMMENDED SOLDERING CONDITIONS

The following conditions (see table below) must be met when soldering this product.

Please consult with our sales offices in case other soldering process is used or in case soldering is done under different conditions.

For details of recommended soldering conditions for surface mounting, refer to information document **SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL (C10535E)**.

μ PC2766GR

Soldering process	Soldering conditions	Symbol
Infrared ray reflow	Peak package's surface temperature: 235 °C or below, Reflow time: 30 seconds or below (210 °C or higher), Number of reflow process: 3, Exposure limit Note: None	IR35-00-3
VPS	Peak package's surface temperature: 215 °C or below, Reflow time: 40 seconds or below (200 °C or higher), Number of reflow process: 3, Exposure limit ^{Note} : None	VP15-00-3
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below, Number of flow process: 1, Exposure limit Note: None	WS60-00-1
Partial heating method	Terminal temperature: 300 °C or below, Flow time: 3 seconds or below, Exposure limit Note: None	

Note Exposure limit before soldering after dry-pack package is opened.

Storage conditions: 25 °C and relative humidity at 65 % or less.

Caution Do not apply more than single process at once, except for "Partial heating method".

[MEMO]

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 - "Standard", "Special", and "Specific". The Specific quality grade applies only to devices developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a device depend on its quality grade, as indicated below. Customers must check the quality grade of each device before using it in a particular application.
 - Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
 - Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
 - Specific: Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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