## BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu$ PC2766GR/GS

## DESCRIPTION

The $\mu$ PC2766GR/GS is a Silicon monolithic IC designed for use as IQ demodulator in wide dynamic range compressed video or spread spectrum receivers. This IC consists of a wide band RF amplifier, gain control amplifier, dual balanced mixers (DBM), Lo buffers, and I \& Q output buffer amplifiers.

The package is 20 pin SSOP (shrink small outline package: $\mu \mathrm{PC} 2766 \mathrm{GR}$ ) or 20 pin SOP ( $\mu \mathrm{PC} 2766 \mathrm{GS}$ ) suitable for high-density surface mount.

## FEATURES

- Broadband operation RF \& LO up to 1000 MHz

IF (IQ) up to 200 MHz

- Wideband IQ phase and amplitude balance

| Amplitude balance | $\pm 0.3 \mathrm{~dB}$ (TYP.) |
| :--- | :--- |
| Phase balance | $\pm 0.3$ degree (TYP.) |

- AGC dynamic range

45 dB

- Low distortion; $\mathrm{IM}_{3} \quad 30 \mathrm{dBc}$
- Supply Voltage 5 V
- Packaged in 20 pin SSOP or 20 pin SOP suitable for high-density surface mount

ORDERING INFORMATION

| PART NUMBER | PACKAGE | PACKAGE STYLE |
| :--- | :---: | :--- |
| $\mu$ PC2766GR-E1 | 20 pin plastic SSOP (225 mil) | Embossed tape 12 mm wide. $2.5 \mathrm{k} /$ REEL <br> Pin 1 indicates pull-out direction of tape |
| $\mu$ PC2766GS-E1 | 20 pin plastic SOP ( 300 mil) | Embossed tape 24 mm wide. $2.5 \mathrm{k} / \mathrm{REEL}$ <br> Pin 1 indicates pull-out direction of tape |

## Caution electro-static sensitive device

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## INTERNAL BLOCK DIAGRAM



PIN FUNCTIONS

\begin{tabular}{|c|c|c|c|c|}
\hline PIN No. \& PIN NAME \& PIN VOLTAGE TYP. (V) \& FUNCTION AND EXPLANATION \& EQUIVALENT CIRCUIT <br>
\hline 1 \& I Trim \& 4.2 \& Trimming pin for I-IF output. \&  <br>
\hline 2 \& VcclF I \& 5.0 \& Power supply pin for I-MIXER. \& <br>
\hline 3 \& VccRF \& 5.0 \& Power supply pin for RF and AGC block. \& <br>
\hline 4 \& GND RF \& 0.0 \& Ground pin of RF and AGC block. \& <br>
\hline 5 \& RFin

$\overline{\text { RFin }}$ \& 2.6 \& RF input pin. In case of single input, 6 pin should be grounded through capacitor. \&  <br>
\hline 7 \& GND RF \& 0.0 \& Ground pin of RF and AGC block. \& <br>

\hline 8 \& V ${ }_{\text {AGC }}$ \& 0 to 5 \& | Gain control pin. |
| :--- |
| - $V_{\text {AGC }}=0 \mathrm{~V}$ : Full gain |
| - $V_{A G C}=5 \mathrm{~V}$ : Maximum reduction | \& (8) <br>

\hline 9 \& VcclF Q \& 5.0 \& Power supply pin for Q-MIXER. \& <br>
\hline 10 \& Q Trim \& 4.2 \& Trimming pin for Q-IF output. \&  <br>
\hline
\end{tabular}

\begin{tabular}{|c|c|c|c|c|}
\hline PIN No. \& PIN NAME \& $$
\begin{gathered}
\text { PIN VOLTAGE } \\
\text { TYP. (V) }
\end{gathered}
$$ \& FUNCTION AND EXPLANATION \& EQUIVALENT CIRCUIT <br>
\hline 11

12 \& Qout
$\overline{\text { Qout }}$ \& 3.3

3.3 \& Q-IF output pin. 11 pin and 12 pin are balance outputs. \&  <br>
\hline 13 \& GNDIF Q \& 0.0 \& Ground pin of Q-IF block. \& <br>
\hline 14

15 \& | Lo Q |
| :---: |
|  |
| $\overline{\text { Lo Q }}$ | \& 2.2 \& Oscillator signal input pin of Q-MIXER. In case of single input, 15 pin should be grounded through capacitor. \&  <br>

\hline 16

17 \& Lo I \& 2.2 \& Oscillator signal input pin of I-MIXER. In case of single input, 16 pin should be grounded through capacitor. \&  <br>
\hline 18 \& GNDIF I \& 0.0 \& Ground pin of I-IF block. \& <br>
\hline 19 \& $\overline{\text { lout }}$ \& 3.3 \& I-IF output pin. 19 pin and 20 pin are balance outputs. \&  <br>
\hline 20 \& lout \& 3.3 \& \& $\oplus$ <br>
\hline
\end{tabular}

## ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ) $\mu$ PC2766GR

| PARAMETER | SYMBOL | RATING | UNIT | TEST CONDITIONS |
| :--- | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{cc}}$ | 6.0 | V |  |
| Power dissipation | $\mathrm{P}_{\mathrm{D}}$ | 430 | mW | $\mathrm{~T}_{\mathrm{A}}=85^{\circ} \mathrm{C}^{\text {Note } 1}$ |
| Operating temperature range | $\mathrm{T}_{\mathrm{A}}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |  |
| Storage temperature range | $\mathrm{T}_{\mathrm{stg}}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |  |

$\mu$ PC2766GS

| PARAMETER | SYMBOL | RATING | UNIT | TEST CONDITIONS |
| :--- | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{cc}}$ | 6.0 | V |  |
| Power dissipation | $\mathrm{PD}_{\mathrm{D}}$ | 650 | mW | $\mathrm{~T}_{\mathrm{A}}=85^{\circ} \mathrm{C}^{\text {Note } 1}$ |
| Operating temperature range | $\mathrm{T}_{\mathrm{A}}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |  |
| Storage temperature range | $\mathrm{T}_{\mathrm{stg}}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |  |

Note 1 Mounted on $50 \times 50 \times 1.6 \mathrm{~mm}$ double epoxy glass board.

## RECOMMENDED OPERATING RANGE

$\mu$ PC2766GR/GS

| PARAMETER | SYMBOL | MAX. | TYP. | MIN. | UNIT |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | Vcc | 4.5 | 5.0 | 5.5 | V |
| Operating temperature range | $\mathrm{TA}_{\text {A }}$ | -40 | +25 | +85 | ${ }^{\circ} \mathrm{C}$ |

ELECTRICAL CHARACTERISTICS (Vcc =5 V, $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{\mathrm{L}}=250 \Omega$ )
$\mu$ PC2766GR/GS

| PARAMETER | SYMBOL | MIN. | TYP. | MAX. | UNIT | TEST CONDITIONS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply current | Icc | - | 60 | 78 | mA | no input signal |
| RF input bandwidth | RF BW | $\begin{aligned} & \text { DC - } \\ & 750 \end{aligned}$ | $\begin{aligned} & \text { DC - } \\ & 1000 \end{aligned}$ | - | MHz | $\mathrm{fiF}_{\mathrm{IF}}=40 \mathrm{MHz}, \mathrm{f}_{\text {RF }}>\mathrm{fLO}^{2}$ IQ phase balance $\leq \pm 1.5^{\circ}$ |
| IF output bandwidth | IF BW | DC | 200 | - | MHz | $\begin{aligned} & f_{\text {RF }}=480 \mathrm{MHz}, \mathrm{PLo}=-10 \mathrm{dBm} \\ & f_{\text {RF }}>\mathrm{f}_{\mathrm{LO}},-3 \mathrm{~dB} \text { down, } \mathrm{Vagc}=0 \mathrm{~V} \end{aligned}$ |
| Gain control range | Ggc | 40 | 45 | - | dB | $\begin{aligned} & \mathrm{f}_{\mathrm{RF}}=480 \mathrm{MHz}, \mathrm{f}_{\mathrm{IF}}=40 \mathrm{MHz} \\ & \mathrm{PRF}=-30 \mathrm{dBm}, \mathrm{PLo}=-10 \mathrm{dBm} \\ & \text { Vagc }=0-5 \mathrm{~V} \end{aligned}$ |
| IQ phase balance | $\Delta \phi$ | - | $\pm 0.3$ | $\pm 1.5$ | deg | $\begin{aligned} & f_{\mathrm{RF}}=480 \mathrm{MHz}, \mathrm{fiF}=40 \mathrm{MHz} \\ & \mathrm{P}_{\mathrm{RF}}=-30 \mathrm{dBm}, \mathrm{P}_{\mathrm{LO}}=-10 \mathrm{dBm} \end{aligned}$ |
| IQ amplitude balance | $\Delta \mathrm{G}$ | - | $\pm 0.3$ | $\pm 0.5$ | dB | $\begin{aligned} & f_{\text {RF }}=480 \mathrm{MHz}, \mathrm{fiF}_{\mathrm{IF}}=40 \mathrm{MHz} \\ & \mathrm{P}_{\mathrm{RF}}=-30 \mathrm{dBm}, \text { PLo }=-10 \mathrm{dBm} \\ & \text { Vagc }=0 \mathrm{~V} \end{aligned}$ |
| Output voltage | Vo | 1.2 | 1.5 | - | Vp-P | $\begin{aligned} & f_{R F}=480 \mathrm{MHz}, \mathrm{fiF}=40 \mathrm{MHz} \\ & \mathrm{PLO}=-10 \mathrm{dBm}, \mathrm{Z}_{\mathrm{L}}=250 \Omega \end{aligned}$ |
| Conversion gain | CG | 15 | 20 | 25 | dB | $\begin{aligned} & \mathrm{fRF}_{\mathrm{RF}}=480 \mathrm{MHz}, \mathrm{fiF}=40 \mathrm{MHz} \\ & \mathrm{Vagc}=0 \mathrm{~V} \end{aligned}$ |

STANDARD CHARACTERISTICS (REFERENCE VALUES) (Vcc =5 V, $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{ZL}=250 \Omega$ )
$\mu$ PC2766GR/GS

| PARAMETER | SYMBOL | MIN. | TYP. | MAX. | UNIT | TEST CONDITIONS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Noise figure | NF | - | 21 | - | dB | $\begin{aligned} & \mathrm{f}_{\mathrm{RF}}=480 \mathrm{MHz}, \mathrm{fiF}=40 \mathrm{MHz} \\ & \text { PLo }=-10 \mathrm{dBm}, \mathrm{Vagc}=0 \mathrm{~V} \end{aligned}$ |
| LO to RF isolation | LO-RFisol | - | 55 | - | dB | $\begin{aligned} & \mathrm{f} \mathrm{Lo}=440 \mathrm{MHz}, \mathrm{PLo}=-10 \mathrm{dBm} \\ & \text { Vagc }=0 \mathrm{~V} \end{aligned}$ |
| LO to IF isolation | LO-IFisol | - | 10 | - | dB | $\begin{aligned} & \mathrm{fLO}=440 \mathrm{MHz}, \mathrm{PLo}=-10 \mathrm{dBm} \\ & \text { Vagc }=0 \mathrm{~V} \end{aligned}$ |
| 3rd order intermodulation distortion | $1 \mathrm{M}_{3}$ | - | 30 | - | dBc | $\begin{aligned} & \mathrm{fRF} 1^{=}=480 \mathrm{MHz}, \mathrm{f}_{\mathrm{RF} 2}=490 \mathrm{MHz} \\ & \mathrm{fLO}=440 \mathrm{MHz}, \mathrm{Pin}=-20 \mathrm{dBm} \\ & \mathrm{PLO}=-10 \mathrm{dBm}, \mathrm{Vagc}=1 \mathrm{~V} \end{aligned}$ |
| Saturated output level | Po(sat) | - | +2 | - | dBm | $\begin{aligned} & f_{\text {RF }}=480 \mathrm{MHz}, \mathrm{fiF}_{\mathrm{IF}}=40 \mathrm{MHz} \\ & \mathrm{P}_{\mathrm{RF}}=-10 \mathrm{dBm} \end{aligned}$ |

TYPICAL CHARACTERISTICS - on Measurement Circuit - (Note 2 Lower local)


Pin vs. Pout (lout)


Pin vs. Pout (Qout)









## MEASUREMENT CIRCUIT

( $@ \mathrm{ZL}=250 \Omega$ )


Note $3 \quad \begin{array}{lll}---]^{-}\end{array}$is Low pass filter in order to eliminate local leak.

## APPLICATION CIRCUIT EXAMPLE



## PACKAGE DIMENSIONS

$\star 20$ PIN PLASTIC SSOP (225 mil) (UNIT: mm)


## detail of lead end



NOTE Each lead centerline is located within 0.10 mm of its true position (T.P.) at maximum material condition.

20 PIN PLASTIC SOP (300 mil) (UNIT: mm)


NOTE Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

## RECOMMENDED SOLDERING CONDITIONS

The following conditions (see table below) must be met when soldering this product.
Please consult with our sales offices in case other soldering process is used or in case soldering is done under different conditions.

For details of recommended soldering conditions for surface mounting, refer to information document SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL (C10535E).
$\mu$ PC2766GR

| Soldering process | Soldering conditions | Symbol |
| :--- | :--- | :---: |
| Infrared ray reflow | Peak package's surface temperature: $235^{\circ} \mathrm{C}$ or below, <br> Reflow time: 30 seconds or below ( $210^{\circ} \mathrm{C}$ or higher), <br> Number of reflow process: 3, Exposure limitNote: None | IR35-00-3 |
| VPS | Peak package's surface temperature: $215^{\circ} \mathrm{C}$ or below, <br> Reflow time: 40 seconds or below $\left(200^{\circ} \mathrm{C}\right.$ or higher), <br> Number of reflow process: 3, Exposure limit Note: None | VP15-00-3 |
| Wave soldering | Solder temperature: $260^{\circ} \mathrm{C}$ or below, <br> Flow time: 10 seconds or below, <br> Number of flow process: 1, Exposure limit Note: None | WS60-00-1 |
| Partial heating method | Terminal temperature: $300^{\circ} \mathrm{C}$ or below, <br> Flow time: 3 seconds or below, <br> Exposure limit Note: None |  |

Note Exposure limit before soldering after dry-pack package is opened.
Storage conditions: $25^{\circ} \mathrm{C}$ and relative humidity at $65 \%$ or less.

Caution Do not apply more than single process at once, except for "Partial heating method".
[MEMO]

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