TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8132AN,TA8132AF,TA2012N,TA2012F

3V AM / FM IF + MPX (For Digital Tuning System)

TA8132AN, TA8132AF and TA2012N, TA2012F are the AM / FM IF+ST DET system ICs, which are designed for DTS radios. These are included many functions and these can be used for digital tuning system with IF counter.

Features

- Built-in AM / FM IF and FM stereo PLL multiplex decoder.
- Suitable for combination with digital tuning system which is included IF counter.
 - One terminal type AM / FM IF count output (auto stop signal) for IF counter of digital tuning system.

FM: 10.7MHz or 1.3375MHz (1 / 8 dividing) changeable by external switch

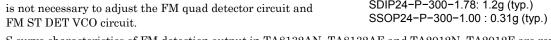
AM: 450kHz

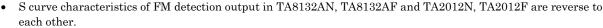
O Built-in mute circuit for IF count output.

It is controlled by the IF request signal from digital tuning system,

Pin(8) level: High \rightarrow come out $Low \rightarrow non output$

- O Adjustable for IF count output sensitivity by external resistance of pin(2).
- For adopting ceramic discriminator and ceramic resonator, it is not necessary to adjust the FM quad detector circuit and

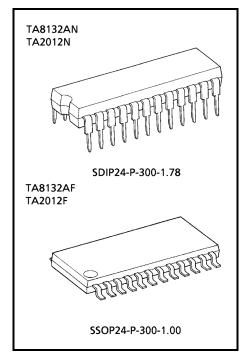




TA8132AN, TA8132AF: Reverse characteristic.

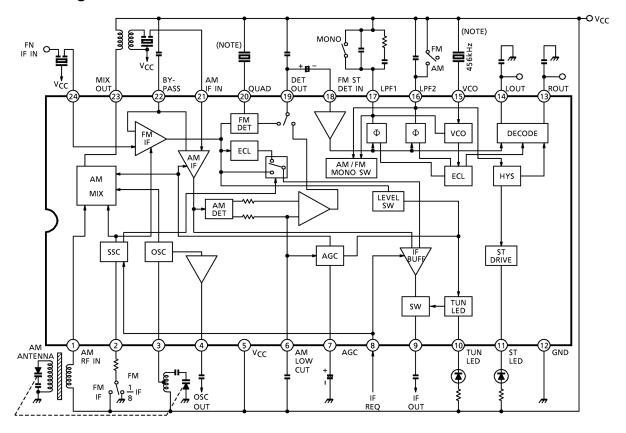
TA2012N, TA2012F: Normal characteristic.

- Built-in one terminal type AM low cut circuit.
- TA2053F is reverse pin type of TA2012F.
- Operating supply voltage range (Ta = 25°C) $V_{CC (opr.)} = 1.8 \sim 8.0 V$



Weight SDIP24-P-300-1.78: 1.2g (typ.)

Block Diagram



(Note)

We recommend

Ceramic resonator: CSB456F18

Ceramic discriminator: CDA10.7MG18 (MURATA MFG CO., LTD)



Explanation Of Terminals

Pin	Item	Internal Circuit	DC Vol	tage (V) Signal)
No.			AM	FM
1	AM RF IN	VCC S	3.0	3.0
2	IF count output sensitivity adjust terminal FM IF divider control terminal	Vcc (5) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1	-	_
3	AM OSC	VCC S BUFF AMP ALC	3.0	3.0
4	AM OSC OUT	VCC S AM OSC 4 GND (2)	2.7	3.0
5	Vcc	_	3.0	3.0
6	AM LOW CUT	VCC (5) (22kΩ) (6) (6)	2.3	2.3

Pin	Item	Internal Circuit	DC Vol	tage (V) Signal)
No.			AM	FM
7	AGC	VCC (5) GND (12) SEARCH MODE: HIGH	0.25	0.35
8	IF OUT SW	8—w- 12	-	_
9	IF OUT	00 D D D D D D D D D D D D D D D D D D	3.0	3.0
10	TUN LED (tuning LED)	VCC S	-	_
11	ST LED (stereo LED)	19kHz ————————————————————————————————————	-	_
12	GND	_	0	0
13 14	R-OUT L-OUT	Vcc (5)	1.0	1.0

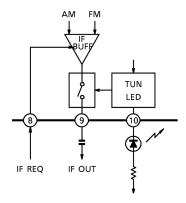
Pin	Item	Internal Circuit	DC Vol	tage (V) Signal)
No.	Item	internal official	AM	FM
15	vco	VCC S GND 12	2.5	2.5 (VCO stop mode)
16	LPF2 • LPF terminal for synchronous detector • Bias terminal for AM / FM switch circuit V ₁₆ = V _{CC} →AM V ₁₆ = open→FM	GND 12	3.0	2.2
17	LPF1 • LPF Terminal for phase detector • VCO stop terminal V ₁₇ = V _{CC} →VCO stop	GND (12)	2.7	2.2
18	FM ST DET IN	(B) (G) (12)	0.7	0.7
19	DET OUT	V _{CC} S AM FM GND 12	1.1	1.1

Pin	Item	Internal Circuit	DC Volt	age (V) Signal)
No.			AM	FM
20	QUAD (FM QUAD. Detector)	V _{CC} (5) (29) (19) (19) (19) (19) (19) (19) (19) (1	2.4	2.1
21	AM IF IN	Vcc (5) C2 (2) m (2) m (3) m (4)	3.0	3.0
22	BY-PASS By-pass for AM/FM IF AMP	GND 12	2.3	2.8
23	AM MIX OUT	V _{CC} (S) MIX MIX GND (12)	3.0	3.0
24	FM IF IN	VCC S BY-PASS 22 GND 12	3.0	3.0



Application Note

1. How to control the IF count output signal (pin(9) output)



		TUN I	_ED
		ON	OFF
V ₈	Н	Come out	Non output
V8	L	Non output	Non output

• Whether or not there is the IF count output signal (pin(9) output) is determined by the and of the pin(8) control voltage: V_8 and tuning LED on / off switching.

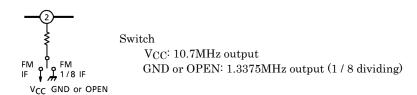
In the condition of

V8: High (active high, $V_{TH} = 0.8V$ (typ.)) TUN LED: ON ($V_{in} \ge V_{L} + 2dB_{\mu}V$ EMF (typ.))

the IF count output signal comes out from the pin(9).

In the case of the tuning LED function is not needed, it doesn't matter the pin(10) is opened.

- The output impedance of pin(9) is $1.5 \mathrm{k}\Omega$ (typ.) (cf.P.4)
- It is possible to reduce the IF count output signal level to add the resistance between the pin(9) and the VCC line.
- $\bullet~$ The signal waveform is the rectangular wave, and the level is $500 mV_{p-p}$ (typ.)
- 2. How to control the divider of FM IF



- 3. How to adjust the IF count output sensitivity
 - The IF count output sensitivity (search sensitivity)

 Can be adjusted by varying the IF AMP gain for FM and varying the MIXER gain for AM.

 This setting is made by changing the value of external resistance R₂ which is connected to pin(2).



• However, this is only possible at the auto-tuning mode. (external voltage supplied to pin(8) is at high level.) The original again returns while receiving a broadcast station (supplied voltage to pin(8) is at low level.)

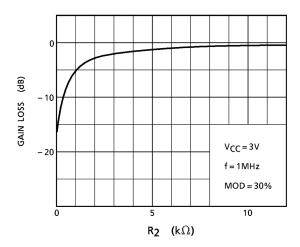
• The gain loss of FM IF AMP

_							
Г			R ₂				
L			Ω0	10KΩ (Note)			
	Mode	IF (10.7MHz)	-20dB	−1dB			
	Mo	1 / 8 IF (1.3375MHz)	-20dB	-1dB			

(Note)

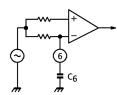
- In the condition of the 1 / 8 IF mode, it is possible to set up R₂ = ∞ (OPEN).
- In the condition of IF mode, it is necessary to set up the value of R_2 under $10k\Omega$. When the R_2 is over $10k\Omega$ it is feared that the mode is change to the 1 / 8 IF mode.
- The gain loss of AM MIXER

R	2
Ω0	10ΚΩ
-16dB	−1dB



4. AM low-cut circuit

• The AM low-cut action is carried out by the bypass of the high frequency component of the positive-feedback signal at the AF AMP stage. The external capacitor: C6 by-passes this component.



 \bullet The cut–off frequency fL is determine by the internal resistance 22k Ω (typ.) and the external capacitor C6 as following;

$$f_L = \frac{1}{2 \times \pi \times 22 \times 10^3 \times C_6} (Hz)$$

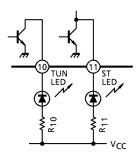
• In the case of the AM low–cut function is not needed, set up the value of C_6 over $0.47\mu F$. In the condition of $C_6 \ge 0.47\mu F$, the frequency characteristic has flat response at the low frequency.

5. AM local oscillator buffer output

- The output impedance of AM local oscillator buffer output pin (pin(4)) is 750Ω (typ.) (cf.P.3)
- It is possible to reduce the output level to add the resistance between the pin(4) and VCC line. The signal waveform is the rectangular wave, and the level is 500mV_{p-p} (fosc = 1.45MHz, typ.)
- The higher local oscillation frequency (fosc) to be, the lower buff output level to be owing to the load capacity. So, in the case that it is connected to other circuits, take care of the input capacity of these circuits and stray capacity of wire.

6. Tuning LED driver and stereo LED driver

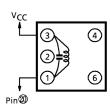
- The tuning LED driver and stereo LED driver don't have current limit resistance shown in the right figure. So, it is necessary to add the current limit resistance: R₁₀, R₁₁.
- Set up the values of R₁₀, R₁₁ to keep the drive currents ID10, ID11 under 10mA.



7.FM detection circuit

For the FM detection circuit, detection coil is able to use instead of ceramic discriminator. Recommended circuit and recommended coil are as follows. In this case, please take care that V_{in} (lim.) falls a little.





Test	Co	0-		Tu	rns		Wire	REF	
Frequency	(pF)	Q ₀	1–2	2–3	1–3	4–6	(mmφ)	KEF	
10.7MHz	100	100			12	_		SUMIDA ELECTRIC CO., LTD 2153–4095–189 or equivalent	

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8. FM / AM switch and forced monaural switch

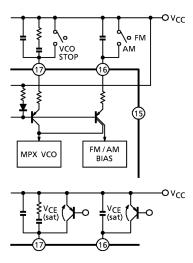
FM / AM switch over and stere / forced monaural switch over are done by internal PNP transistors ON / OFF which are connected to pin(16) and pin(17) respectively.

The threshold voltages of these PNP transistors are V_{th} = V_{CC} , and for switching, we recommend to use mechanical switch.

(Direct short to VCC line.)

In the case of the electrical switch over by transistor, set up $V_{\rm CE}$ (saturation voltage between collector and emitter) 50mV or less, otherwise there are some cases that it does not become the AM mode and force monaural mode.

When these external switches are ON, the currents which flow into pin(16) and pin(17) are $100\mu A$ and $20\mu A$ respectively. (Typical value at VCC = 3V)



Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Supply voltage		V _{CC}	8	V	
LED current		I _{LED}	10	mA	
LED voltage		V_{LED}	8	٧	
Power dissipation	TA8132AN	PD (Note)	1200	mW	
i owei dissipation	TA8132AF	TD (Note)	400	11100	
Operating Temperatu	ire	T _{opr}	-25~75	°C	
Storage temperature		T _{stg}	-55~150	°C	

(Note): Derated above 25°C in the proportion of 9.6mW / °C for TA8132AN, TA2012N and of 3.2mW / °C for TA8132AF, TA2012F.



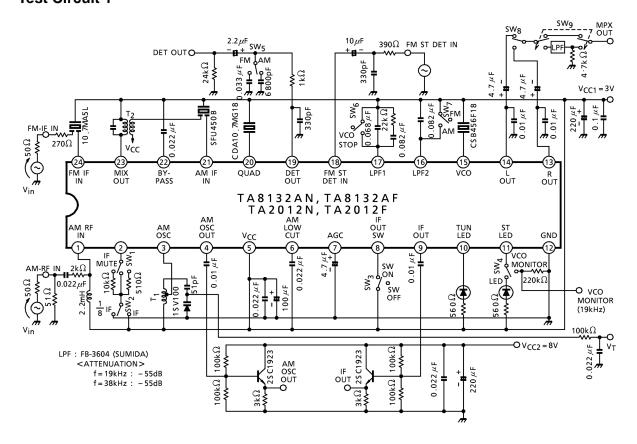
Electrical characteristics Unless Otherwise Specified, Ta = 25°C, V_{CC1} = 3V, $SW_1 \rightarrow 10k\Omega$, $SW_3 \rightarrow OFF$ FM IF: f = 10.7MHz, Δf = $\pm 22.5kHz$, f_m = 1kHz AM: f = 1MHz, MOD = 30%, f_m = 1kHz MPX: f_m = 1kHz

	Characteris	stic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Supply current		I _{CC} (FM)	1	FM mode, V _{in} = 0	_	11.0	14.0	mA	
Suppi	Supply current		I _{CC} (AM)	1	AM mode, V _{in} = 0	_	10.5	13.5	IIIA
	Input limiting voltage	9	V _{in (lim.)}	1	-3dB limiting point	41	46	51	dBµV EMF
	Recovered o	output	V _{OD}	1	V _{in} = 80dBμV EMF	50	75	100	mV _{rms}
	Signal to no ratio	ise	S/N	1	V _{in} = 80dBμV EMF	_	65	_	dB
	Total harmo distortion	nic	THD	1	V _{in} = 80dBμV EMF	_	0.2	_	%
	AM rejection ratio		AMR	1	V _{in} = 80dBμV EMF	_	38	_	dB
	LED on sensitivity		VL	1	I _L = 1mA	48	53	58	dBµV EMF
FM	IF count output frequency	IF	f _{IF} (FM)	1	V_{in} = 80dB μ V EMF, SW $_2$ \rightarrow V _{CC} , SW $_3$ \rightarrow ON	_	10.7	_	MHz
IF		1 / 8 IF	f _{1 / 8 IF} (FM)	1	V _{in} = 80dBμV EMF, SW ₂ →GND, SW ₃ →ON	1.3374	1.3375	1.3376	IVIITZ
	IF count output	IF	V _{IF} (FM)	1	V_{in} = 61dB μ V EMF, SW $_2$ \rightarrow V _{CC} , SW $_3$ \rightarrow ON	350	500	_	m\/
	voltage	1 / 8 IF	V _{1 / 8 IF} (FM)	1	V _{in} = 61dBµV EMF, SW ₂ →GND _, SW ₃ →ON	350	500	_	∙ mV _{p−p}
					$SW_1 \rightarrow 0$, $SW_2 \rightarrow GND$, $SW_3 \rightarrow ON$	_	76	_	
	IF count out	put	IE		$SW_1 \rightarrow 510\Omega$, $SW_2 \rightarrow GND$, $SW_3 \rightarrow ON$	_	68	_	dΒμV
	sensitivity		1	$SW_1 \rightarrow 0$, $SW_2 \rightarrow V_{CC}$, $SW_3 \rightarrow ON$	_	77	_	EMF	
					$SW_1 \rightarrow 510\Omega$, $SW_2 \rightarrow V_{CC}$, $SW_3 \rightarrow ON$	_	69	_	

	Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
	Gain	G _V	1	V _{in} = 26dBμV EMF	28	57	85	
	Recovered output voltage	V _{OD}	1	V _{in} = 60dBμV EMF	50	75	100	mV _{rms}
	Signal to noise ratio	S/N	1	V _{in} = 60dBμV EMF	_	41	_	dB
	Total harmonic distortion	THD	1	V _{in} = 60dBμV EMF	_	1.0	_	%
	LED on sensitivity	VL	1	I _L = 1mA	21	26	31	dBµV EMF
	Local OSC buff. output voltage	V _{OSC} (AM)	1	f _{OSC} = 1.45MHz	350	500	_	m\/-
AM		VOSC (AIVI)	2	f _{OSC} = 27MHz	_	500	_	mV _{p-p}
	IF count output voltage	V _{IF} (AM)	1	V _{in} = 39dBμV EMF, SW ₃ →ON	350	500	_	mV_{p-p}
				$SW_1 \rightarrow 0$, $SW_2 \rightarrow GND$, $SW_3 \rightarrow ON$	_	49	_	
	IF count output	le.	4	$SW_1 \rightarrow 510\Omega$, $SW_2 \rightarrow GND$, $SW_3 \rightarrow ON$	_	42	_	dΒμV
	sensitivity	IF _{sens.} (AM)	1	$\begin{array}{c} SW_1 \rightarrow 0, SW_2 \rightarrow, V_{CC}, \\ SW_3 \rightarrow on \end{array}$	_	49	_	EMF
				$SW_1 \rightarrow 510\Omega$, $SW_2 \rightarrow$, V_{CC} , $SW_3 \rightarrow ON$	_	42	_	
Din/10	9) output resistance	R ₁₉	1	FM mode	_	0.6	_	kΩ
1 111(18	output resistance	K19	7	AM mode	_	12	_	V77

	Characteristic		Symbol	Test Cir– cuit	Test Condi	ition	Min.	Тур.	Max.	Unit
	Input resistance		R _{IN}	1	_		_	25	_	kΩ
	Output resistance		R _{OUT}	1	_		_	5	_	K12
	Max. composite signal input voltage		V _{in MAX} (stereo)	1	L + R = 90%, P = 10% THD = 3%, SW ₉ →LPF	: ON	_	350	_	mV _{rms}
					L + R = 135mV _{rms}	f _m = 100kHz	_	42	_	
	Separation		Sep	_	$P = 15 \text{mV}_{\text{rms}}$	f _m = 1kHz	35	42	_	dB
				SW ₉ →LPF: ON	f _m = 10kHz	_	42	_]	
	Total	Monaural	THD (monaural)	= 150 r		0)	_	0.2	_	
MPX	harmonic distortion	Stereo	THD (stereo)	1	$\label{eq:local_local_local} \begin{split} L + R &= 135 \text{mV}_{rms}, \\ P &= 15 \text{mV}_{rms} \\ \text{SW}_9 &\rightarrow \text{LPF: ON} \end{split}$		_	0.2	_	%
	Voltage gai	in	G _V (MPX)	1	V _{in} = 150mV _{rms} (mono)	-5	-3	-1	dB
	Channel ba	lance	C.B.	1	V _{in} = 150mV _{rms} (mono)	-2	0	2	dB
	Stereo LED	ON	V _L (ON)	1	Pilot input		_	8	15	m\/
	sensitivity	OFF	V _L (OFF)	'	i not input		2	6	I	mV _{rms}
	Stereo LED hysteresis		V_{H}	1	To LED turn off from LED turn on		_	2	_	mV _{rms}
	Capture rai	nge	C.R.	1	P = 15mV _{rms}		_	±1.3	_	%
	Signal to no	oise ratio	S/N	1	V _{in} = 150mV _{rms} (mono)	_	78	_	dB

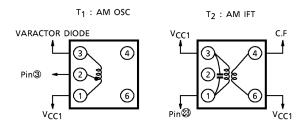
Test Circuit 1



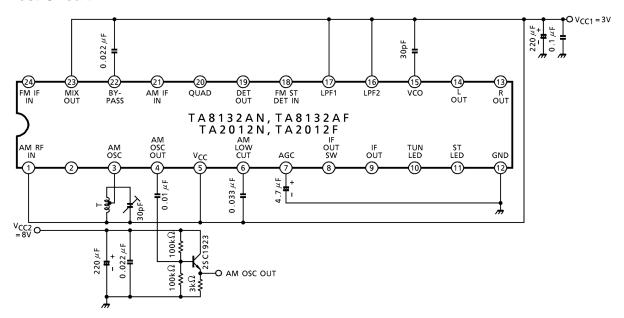
Coil Data (test circuit 1)

Coil No.	f	L (µH)	C _o (pF)	Qo	Turn				Wire	DED (O-HN-)
					1–2	2–3	1–3	4–6	(mm)	RED. (Coil No.)
T ₁ AM OSC	796kHz	288	_	115	13	73	_	_	0.08 UEW	4147-1356-038 (S)
T ₂ AM IFT	455KHz	_	180	120		_	180	15	0.06 UEW	2150-2162-165 (S)

(S): SUMIDA ELECTRIC Co., Ltd.



Test Circuit 2

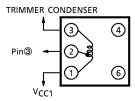


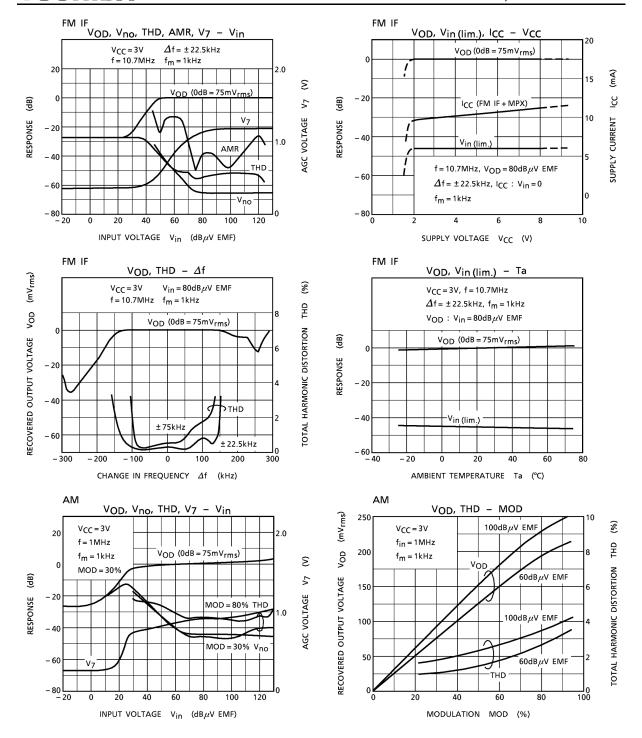
Coil Data (test circuit 2)

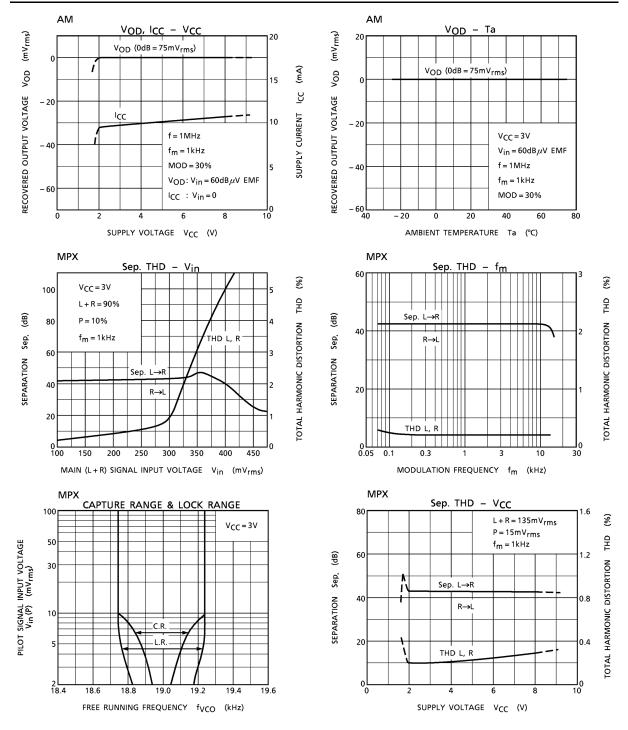
Coil No.	f	L	C _o (pF) Q _o		Turn				Wire	REF. (Coil No.)
		(µH)		1–2	2–3	1–3	4–6	(mm)	REF. (COII NO.)	
T AM OSC	7.96MHz	1.4	_	84	1	6	7	-	0.08 UEW	(T) 7PL-1344Y

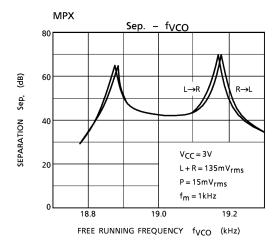
(T): TOKO Co., Ltd.

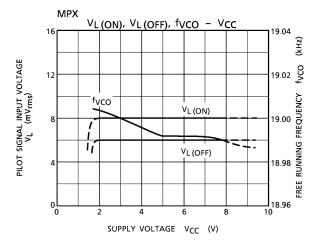
T: AM OSC







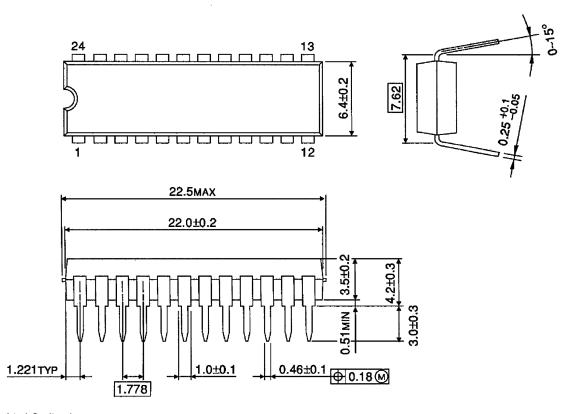




Package Dimensions

SDIP24-P-300-1.78

Unit: mm

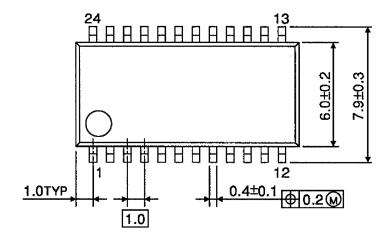


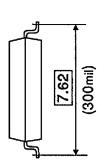
Weight: 1.2g (typ.)

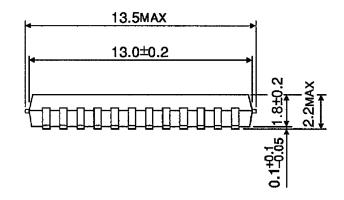
Unit: mm

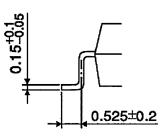
Package Dimensions

SSOP24-P-300-1.00









Weight: 0.31g (typ.)

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