Sept. 1995 Edition 3.0a

MB501SL

DATA SHEET

# SUPER LOW POWER TWO MODULUS PRESCALER

### SUPER LOW POWER TWO MODULUS PRESCALER

The Fujitsu MB501SL is a super low power version of the MB501 two modulus prescaler used with a frequency synthesizer to make a Phase Locked Loop (PLL). It divides the input frequency by the modulus of 64/65 or 128/129, respectively. The MB501SL achieves extremely small stray capacitance by the use of Fujitsu's Advanced Process Technology. High speed operation is achieved with low power supply current of 5mA which is about half of the current value of the MB501L.

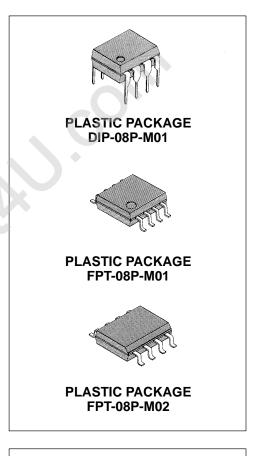
- High Frequency Operation: fmax = 1.1GH<sub>Z</sub> max.(P<sub>IN</sub> = -14bBm)
- Pulse Swallow Function: 64/65, 128/129
- Low Power Supply Current: 5.0mA typ.
- Stable Output Amplitude: V<sub>O</sub> = 1.6Vp-p typ.
- Complete PLL synthesizer circuit with the Fujitsu MB87001A, PLL synthesizer IC
- Plastic 8-pin Dual-In-Line Package
- Plastic 8-pin Mini Flat Package
- · Built-in Termination Resistor
- Stable output amplitude is obtained up to output load capacitance of 8pF.

#### **ABSOLUTE MAXIMUM RATINGS (see NOTE)**

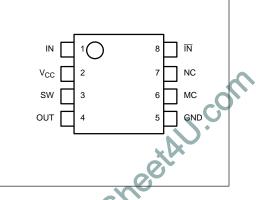
Rating	Symbol	Value	Unit
Power Supply Voltage	V <sub>CC</sub>	-0.5 to +7.0	V
Input Voltage	V <sub>IN</sub>	-0.5 to + V <sub>CC</sub>	V
Output Voltage	Ιo	10	mA
Storage Temperature	T <sub>STG</sub>	– 55 to +125	°C

Note: Permanent device damage may occur if the above **Absolute Maximum Ratings** are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Copyright © 1995 by FUJITSU LIMITED and FUJITSU MICROELECTRONICS



**SU** 



This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.

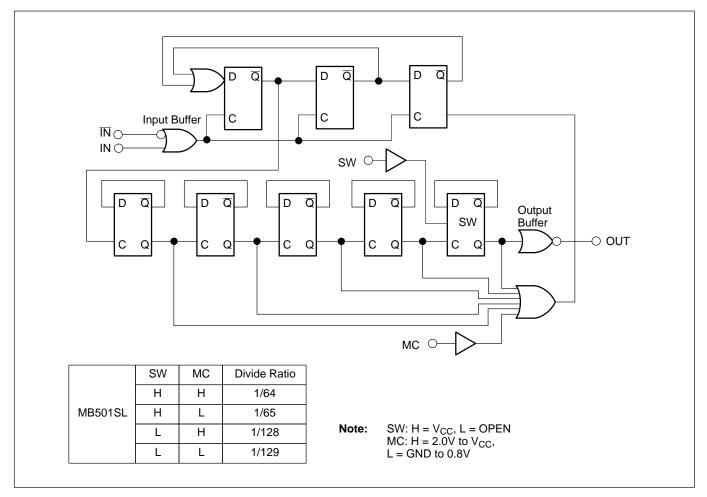


Figure 1. MB501SL Block Diagram

# **PIN DESCRIPTION**

Pin Number	Symbol	Description	
1	IN	Input	
2	V <sub>CC</sub>	Power Supply, +5V	
3	SW	Divide Ratio Control Input (See Divide Ratio Table)	
4	OUT	Output	
5	GND	Ground	
6	MC	Modulus Control Input (See Divide Ratio Table)	
7	NC	Non Connection	
8	ĪN	Complementary Input	

# **RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Values			11	
		Min.	Тур.	Max.	Unit	
Power Supply Voltage	V <sub>CC</sub>	4.5	5.0	5.5	V	
Operating Temperature	T <sub>A</sub>	-40	_	+85	°C	
Load Capacitance	CL	_	_	8	pF	

# **ELECTRICAL CHARACTERISTICS**

(Recommended Operating Conditions unless otherwise noted)

Damaratan	0 mil st	Condition	Values			
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Current	I <sub>CC</sub>	_	_	5.0	7.0	mA
Output Amplitude	Vo	Built-in a termination resistor. Load capacitance = 8pF	1.0	1.6	_	V <sub>P-P</sub>
Input Frequency	fin	With input coupling capacitor 1000pF	10	_	1100	MNz
Input Signal Amplitude	P <sub>IN</sub>	_	-14		0	dBm
High Level Input Voltage for MC	V <sub>IHM</sub>	_	2.0		_	V
Low Level Input Voltage for MC	V <sub>ILM</sub>	_	_	_	0.8	v
High Level Input Voltage for SW	V <sub>IHS</sub> *	_	V <sub>CC</sub> -0.1	V <sub>CC</sub>	V <sub>CC</sub> +0.1	V
Low Level Input Voltage for SW	V <sub>ILS</sub>	_		OPEN		V
High Level Input Current for MC	I <sub>IHM</sub>	V <sub>IH</sub> = 2.0V	_	_	0.4	mA
Low Level Input Current for MC	I <sub>ILM</sub>	V <sub>IL</sub> = 0.8V	-0.2	_	_	mA
Modulus Set-up Time MC to Output	t <sub>SET</sub>	_	_	16	26	ns

Note: \* Design Guarantee

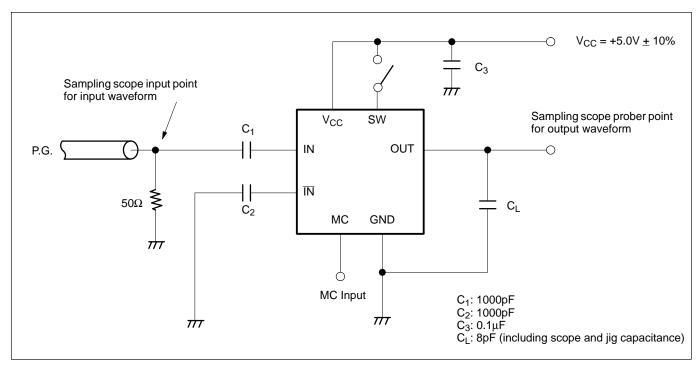
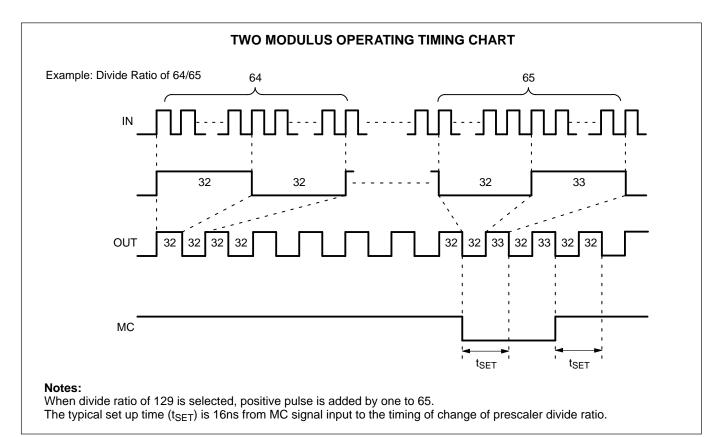


Figure 2. Test Circuit



## **TYPICAL CHARACTERISTICS CURVES**

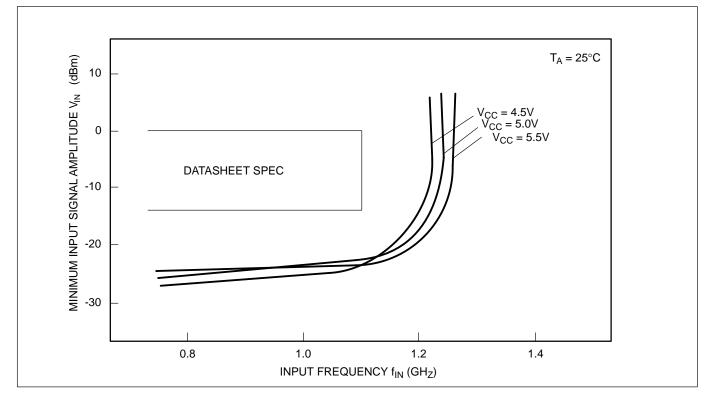


Figure 3. Input Signal Amplitude vs. Input Frequency

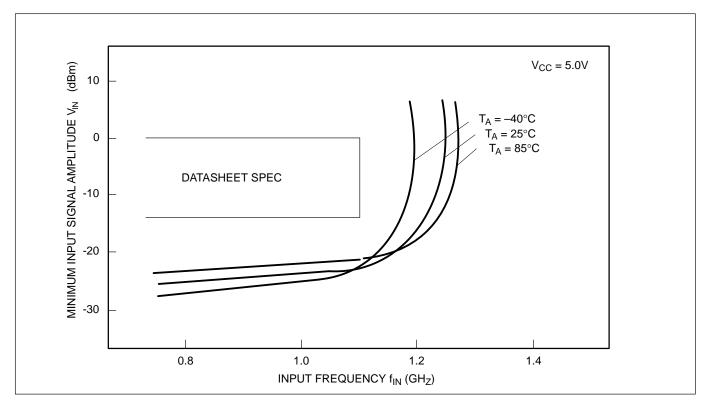


Figure 4. Input Signal Amplitude vs. Input Frequency

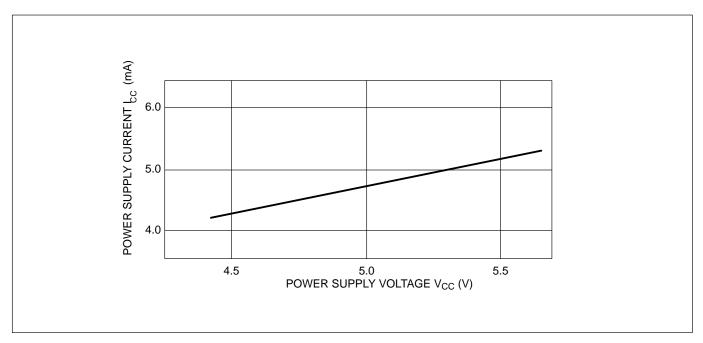


Figure 5. Power Supply Current vs. Power Supply Voltage

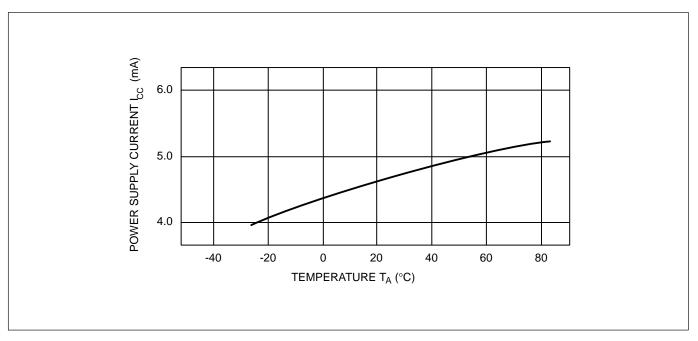


Figure 6. Power Supply Current vs. Temperature

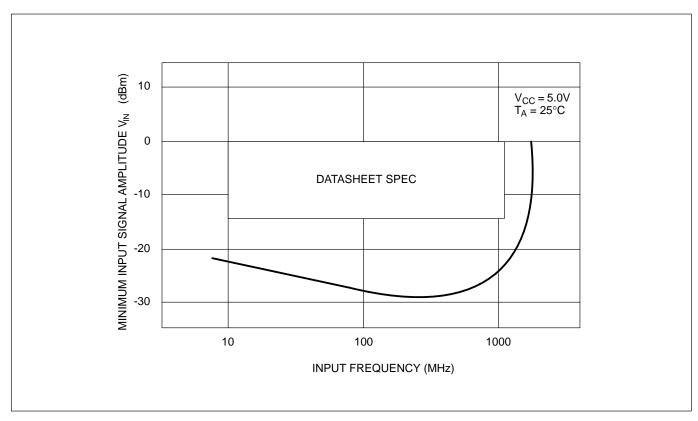


Figure 7. Input Signal vs. Input Frequency

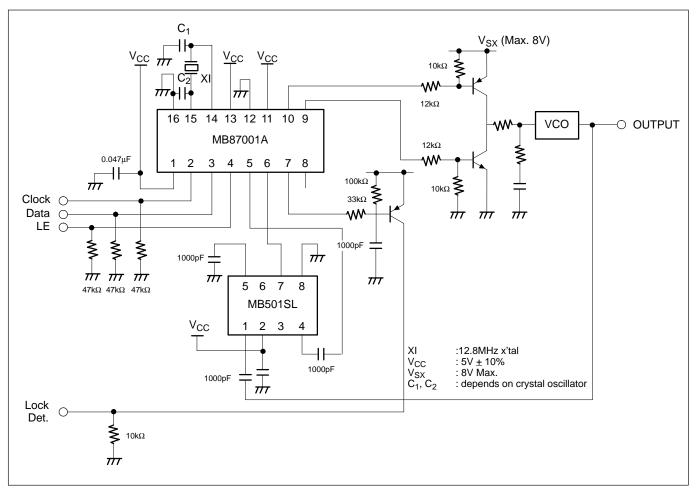
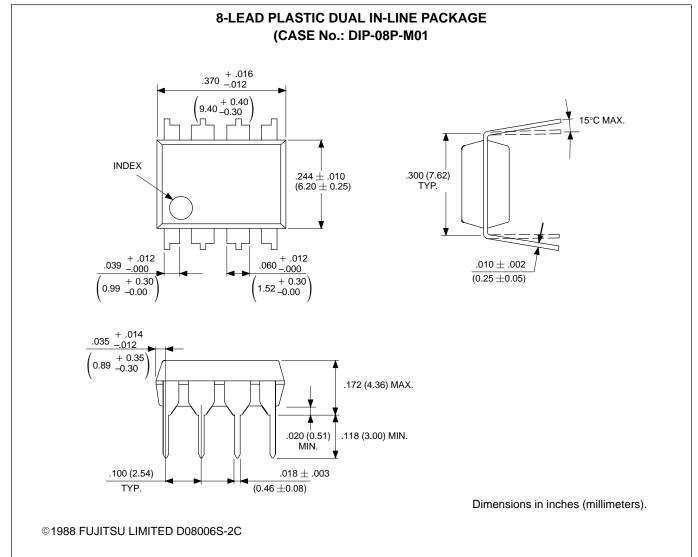
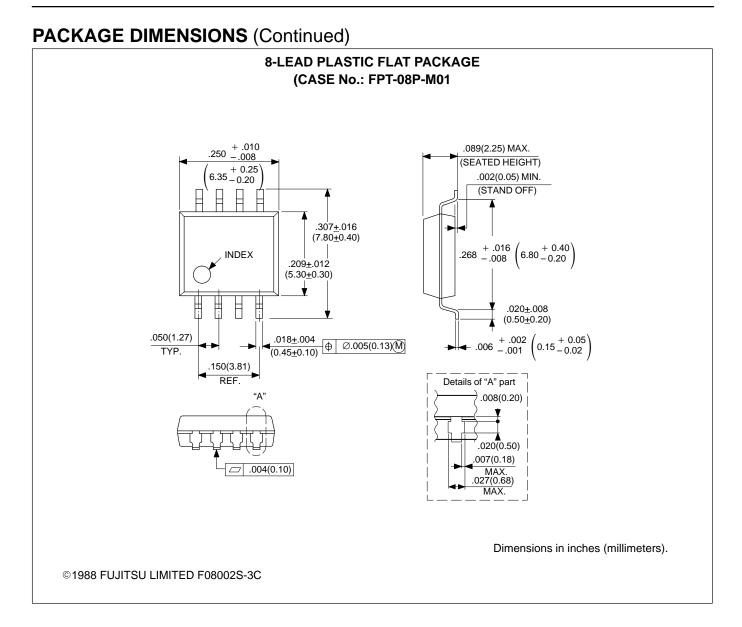


Figure 8. Typical Application Example

## **PACKAGE DIMENSIONS**





#### PACKAGE DIMENSIONS (Continued) **8-LEAD PLASTIC FLAT PACKAGE** (CASE No.: FPT-08P-M02 .061<u>+</u>.008 + .010 + .010 .199 - .008 (SEATED HEIGHT) (1.55+0.20) .006<u>+</u>.004 (STAND OFF) + 0.25 5.05-0.20 (0.15+0.10) .236<u>+</u>.016 (6.00<u>+</u>0.40) .197<u>+</u>.012 45° $(5.00 \pm 0.30)$ .154<u>+</u>.012 (3.90<u>+</u>0.30) ¥. .020+.008 H Η ÷ $(0.50 \pm 0.20)$ .016(0.40) .008+.002 .050(1.27) .017<u>+</u>.004 (0.20<u>+</u>0.05) Ø.005(0.13)(M) ¢ TYP. (0.42+0.10) "A' Details of "A" part .016(0.40) .008(0.20) .004(0.10) $\square$ .007(0.18) .150(3.81) MAX. REF. .026(0.65) MAX. Dimensions in inches (millimeters). ©1988 FUJITSU LIMITED F08004S-2C

#### All Rights Reserved.

Circuit diagrams utilizing Fujitsu products are included as a means of illustrating typical semiconductor applications. Complete information sufficient for construction purposes is not necessarily given.

The information contained in this document has been carefully checked and is believed to be reliable. However, Fujitsu assumes no responsibility for inaccuracies.

The information contained in this document does not convey any license under the copyrights, patent rights or trademarks claimed and owned by Fujitsu.

Fujitsu reserves the right to change products or specifications without notice.

No part of this publication may be copied or reproduced in any form or by any means, or transferred to any third party without prior written consent of Fujitsu.

# FUJITSU LIMITED

For further information please contact:

### Japan

FUJITSU LIMITED International Marketing Div. Furukawa Sogo Bldg., 6-1, Marunouchi 2-chome Chiyoda-ku, Tokyo 100, Japan Tel: (03) 3216-3211 Telex: 781-2224361 FAX: (03) 3215-0662

### North and South America

FUJITSU MICROELECTRONICS, INC. Integrated Circuits Division 3545 North First Street San Jose, CA 95134-1804, USA Tel: 408-922-9000 FAX: 408-432-9044

### Europe

FUJITSU MIKROELEKTRONIK GmbH Am Siebenstein 6-10, 6072 Dreieich-Buchschlag, Germany Tel: (06103) 690-0 Telex: 411963 FAX: (06103) 690-122

### Asia

FUJITSU MICROELECTRONICS ASIA PTE LIMITED 51 Bras Basah Road, Plaza By The Park, #06-04 to #06-07 Singapore 0719 Tel: 336-1600 Telex: 55573 FAX: 336-1609

©FUJITSU LIMITED 1990