

RT3NEEM

Composite Transistor With Resistor
For Switching Application
Silicon NPN Epitaxial Type

DESCRIPTION

RT3NEEM is a composite transistor built with two RT1N234 chips in SC-88 package.

FEATURE

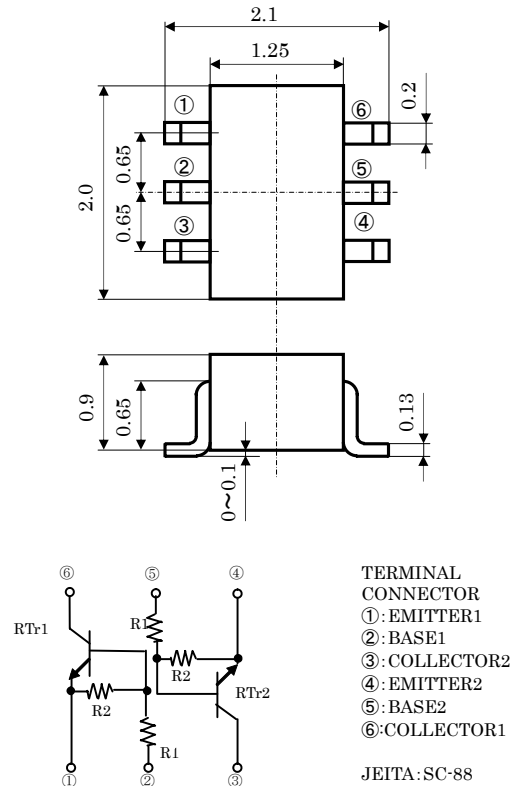
Silicon NPN epitaxial type.
Built in bias resistor.
Each transistor elements are independent.
Mini package for easy mounting

APPLICATION

Inverted circuit, switching circuit,
interface circuit, driver circuit

OUTLINE DRAWING

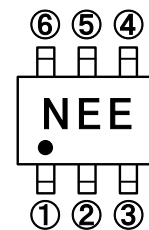
Unit:mm



MAXIMUM RATING (Ta=25°C) (The characteristics apply to both Tr1 and Tr2.)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
VIN	Input voltage	12	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
PC	Collector dissipation (Total, Ta=25°C)	150	mW
Tj	Junction temperature	+150	°C
Tstg	Storage temperature	-55~+150	°C

MARKING



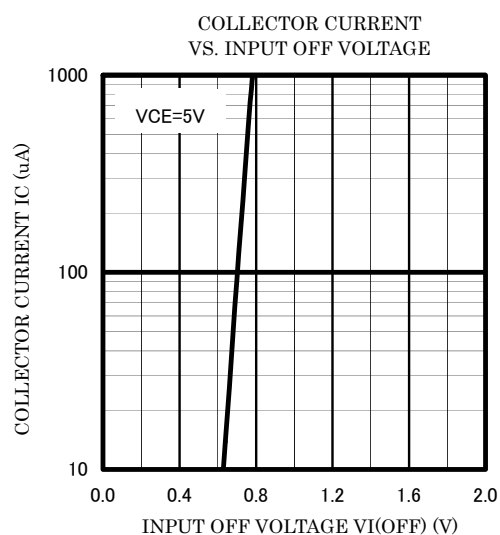
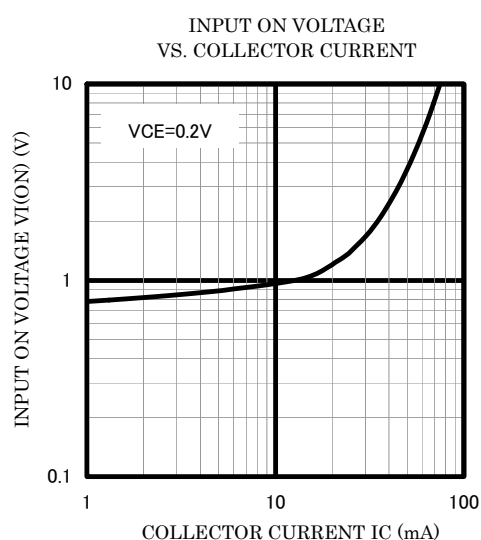
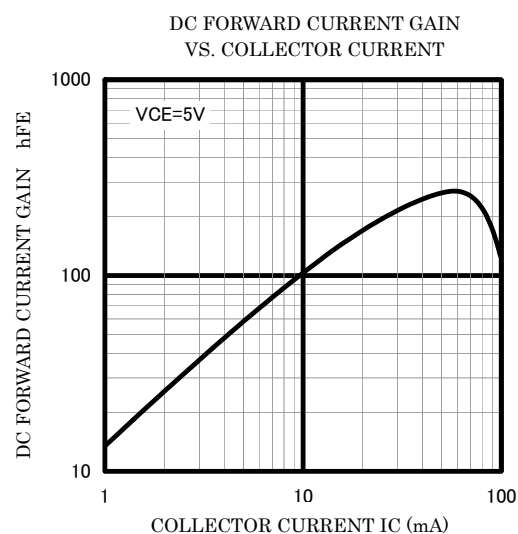
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ELECTRICAL CHARACTERISTICS (Ta=25°C) (The characteristics apply to both Tr1 and Tr2.)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
V(BR)CEO	Collector to Emitter break down voltage	I _C =100μA, R _{BE} =∞	50			V
ICBO	Collector cut off current	V _{CB} =50V, I _E =0			0.1	μA
hFE	DC forward current gain	V _{CE} =5V, I _C =10mA	50			-
V _{CE(sat)}	Collector to Emitter saturation voltage	I _C =10mA, I _B =0.5mA		0.1	0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =0.2V, I _C =5mA		0.8	1.4	V
V _{I(OFF)}	Input off voltage	V _{CE} =5V, I _C =100μA	0.5	0.7		V
R ₁	Input resistor		1.5	2.2	2.9	kΩ
R ₂ /R ₁	Resistor ratio		3.8	4.7	5.6	-
f _T	Gain band width product	V _{CE} =6V, I _E =-10mA		200		MHz

TYPICAL CHARACTERISTICS (Tr1, Tr2)





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