

# RT3PEEM

Composite Transistor With Resistor  
For Switching Application  
Silicon Epitaxial Type

## DESCRIPTION

RT3PEEM is a composite transistor built with two RT1P234 in SC-88 package.

## FEATURE

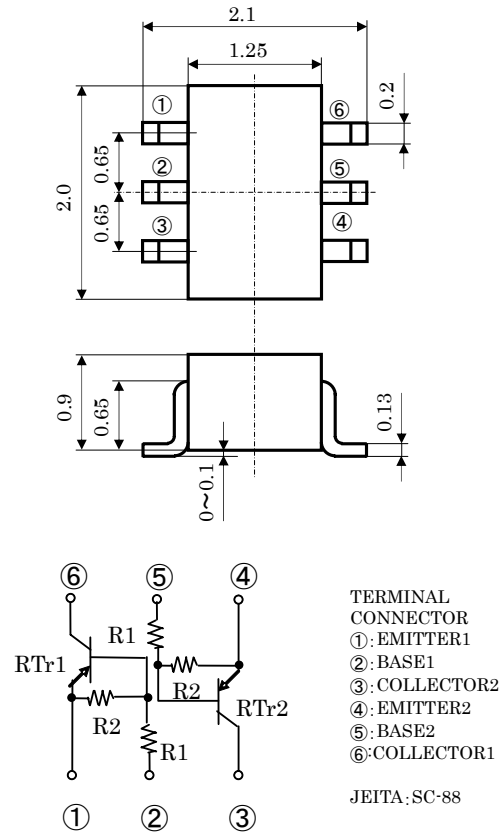
- Silicon epitaxial type
- Each transistor elements are independent.
- Mini package for easy mounting

## APPLICATION

- Inverted circuit, switching circuit,
- interface circuit, driver circuit

## OUTLINE DRAWING

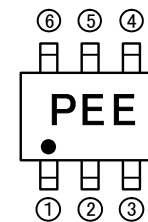
Unit: mm



## MAXIMUM RATING (Ta=25°C) (The characteristics apply to both Tr1 and Tr2.)

SYMBOL	PARAMETER	RATING	UNIT
V <sub>CBO</sub>	Collector to Base voltage	-50	V
V <sub>EBO</sub>	Emitter to Base voltage	-6	V
V <sub>CEO</sub>	Collector to Emitter voltage	-50	V
V <sub>IN</sub>	Input voltage	-12	V
I <sub>C</sub>	Collector current	-100	mA
I <sub>CM</sub>	Peak Collector current	-200	mA
P <sub>C</sub>	Collector dissipation (Total, Ta=25°C)	150	mW
T <sub>j</sub>	Junction temperature	+150	°C
T <sub>stg</sub>	Storage temperature	-55~+150	°C

## MARKING



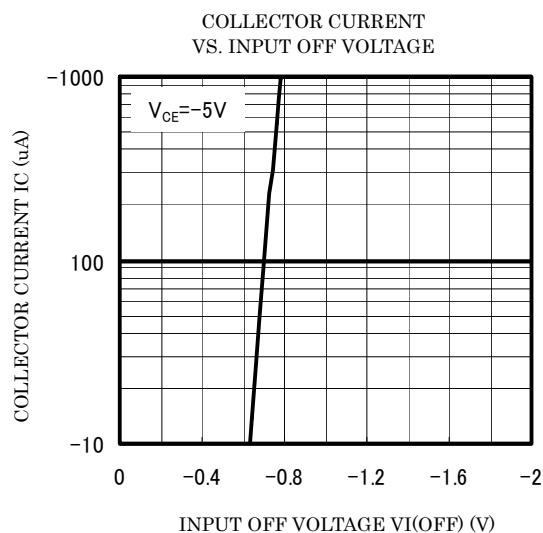
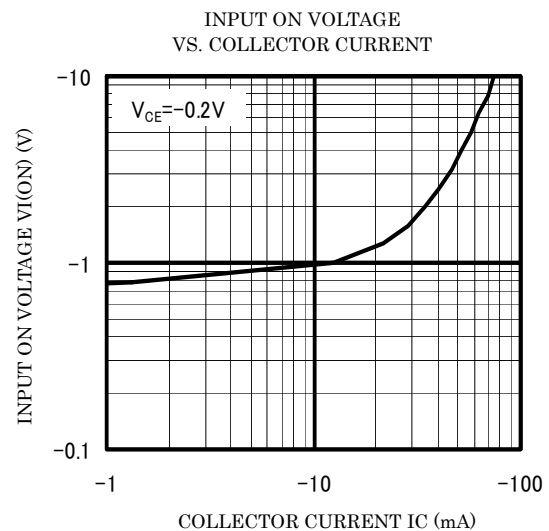
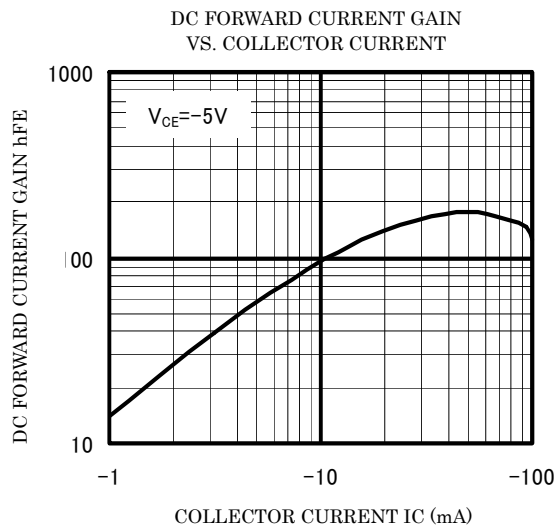
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## ELECTRICAL CHARACTERISTICS (Ta=25°C) (The characteristics apply to both Tr1 and Tr2.)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
V(BR)CEO	Collector to Emitter break down voltage	$I_C = -100\mu A, R_{BE} = \infty$	-50			V
ICBO	Collector cut off current	$V_{CB} = -50V, I_E = 0$			-0.1	$\mu A$
hFE	DC forward current gain	$V_{CE} = -5V, I_C = -10mA$	33			-
VCE(sat)	Collector to Emitter saturation voltage	$I_C = -10mA, I_B = -0.5mA$			-0.3	V
VI(ON)	Input on voltage	$V_{CE} = -0.2V, I_C = -5mA$		-0.8	-1.4	V
VI(OFF)	Input off voltage	$V_{CE} = -5V, I_C = -100\mu A$	-0.5	-0.7		V
R1	Input resistor		1.5	2.2	2.9	k $\Omega$
R2/R1	Resistor ratio		3.8	4.7	5.6	-
fT	Gain band width product	$V_{CE} = -6V, I_E = 10mA, f = 100MHz$		150		MHz

## TYPICAL CHARACTERISTICS (RT1,RT2)





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