

LOW DROPOUT VOLTAGE REGULATOR

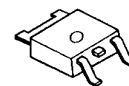
■ GENERAL DESCRIPTION

The NJM2855 is a 3-terminal low dropout voltage regulator. Advanced Bipolar technology achieves low noise, high ripple rejection.

It delivers up to 5V/1A output power with the maximum input voltage of 10V.

The NJM2855 is suitable for various applications such as portable / consumer devices.

■ PACKAGE OUTLINE

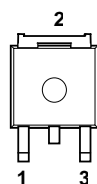


NJM2855DL1

■ FEATURES

- High Ripple Rejection 75dB typ. (f=1kHz, Vo=3V Version)
- Output Noise Voltage Vno=45μVrms typ.
- Output capacitor with 2.2μF ceramic capacitor (Vo≥2.7V)
- Output Current Io(max.)=1A
- High Precision Output Vo±1.0%
- Low Dropout Voltage 0.20V typ. (Io=600mA)
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Bipolar Technology
- Package Outline TO-252-3

■ PIN CONFIGURATION

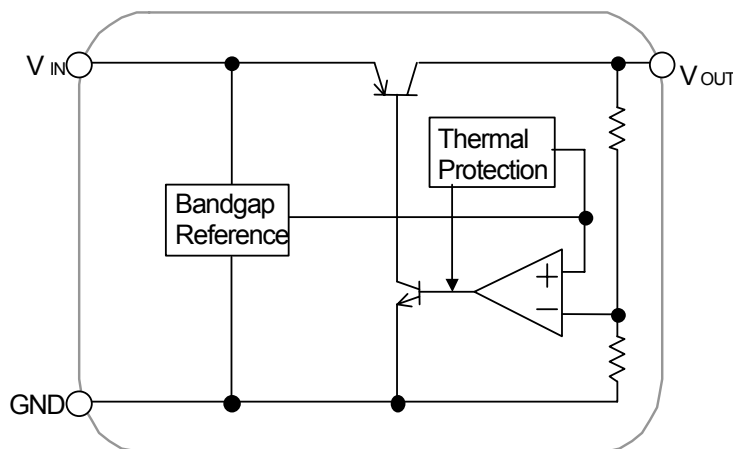


PIN FUNCTION

- 1. V_{IN}
- 2. GND
- 3. V_{OUT}

NJM2855DL1

■ EQUIVALENT CIRCUIT



■ OUTPUT VOLTAGE RANK LIST

| Device Name | V _{OUT} |
|---------------|------------------|
| NJM2855DL1-15 | 1.5V |
| NJM2855DL1-18 | 1.8V |
| NJM2855DL1-23 | 2.3V |
| NJM2855DL1-25 | 2.5V |
| NJM2855DL1-03 | 3.0V |
| NJM2855DL1-33 | 3.3V |
| NJM2855DL1-05 | 5.0V |

Output voltage options available : 1.5 ~ 5.0V (0.1V step)

■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

| PARAMETER | SYMBOL | RATINGS | UNIT |
|-----------------------|------------------|------------|------|
| Input Voltage | V _{IN} | +10 | V |
| Power Dissipation | P _D | 1190(*1) | mW |
| Operating Temperature | T _{opr} | -40 ~ +85 | °C |
| Storage Temperature | T _{stg} | -40 ~ +150 | °C |

(*1): Mounted on glass epoxy board based on EIA/JEDEC. (114.3x76.2x1.6mm: 2Layers, copper area 100mm²)

■ OPERATING VOLTAGE

V_{IN}=+2.5V ~ +8V (In case of Vo<2.3V version)

■ ELECTRICAL CHARACTERISTICS

(V_{IN}=Vo+1V, C_{IN}=0.33μF, Co=2.2μF(1.7V<Vo≤2.6V:4.7μF, Vo≤1.7V:10μF), Ta=25°C)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---|-----------------------------------|--|-------|------|-------|--------|
| Output Voltage | V _o | I _o =30mA | -1.0% | - | +1.0% | V |
| Input Voltage | V _{IN} | | - | - | 8 | V |
| Quiescent Current | I _Q | I _o =0mA | - | 400 | 600 | μA |
| Output Current | I _o | V _o -0.3V | 1000 | 1300 | - | mA |
| Line Regulation | ΔV _o /ΔV _{IN} | V _{IN} =V _o +1V~V _o +6V(V _o ≤2V), V _{IN} =V _o +1V~8V(V _o >2V), I _o =30mA | - | - | 0.10 | %/V |
| Load Regulation | ΔV _o /ΔI _o | I _o =0 ~ 1A | - | - | 0.004 | %/mA |
| Dropout Voltage(*2) | ΔV _{I-O} | I _o =600mA | - | 0.20 | 0.28 | V |
| Ripple Rejection | RR | e _{in} =200mVrms, f=1kHz, I _o =10mA V _o =3.0V Version | - | 75 | - | dB |
| Average Temperature Coefficient of Output Voltage | ΔV _o /ΔT _a | T _a =0~85°C, I _o =10mA | - | ±50 | - | ppm/°C |
| Output Noise Voltage | V _{NO} | f=10Hz~80kHz, I _o =10mA, V _o =3.0V Version(*3) | - | 45 | - | μVrms |
| Input Voltage | V _{IN} | | - | - | 8 | V |

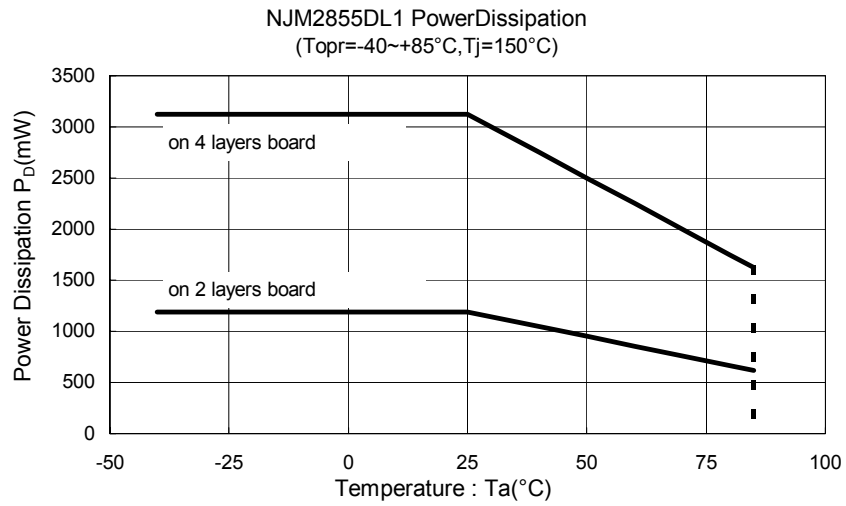
(*2): The output voltage excludes under 2.1V.

(*3): V_o>2.0V : V_{IN}=V_o+1V, V_o≤2.0V : V_{IN}=3.0V

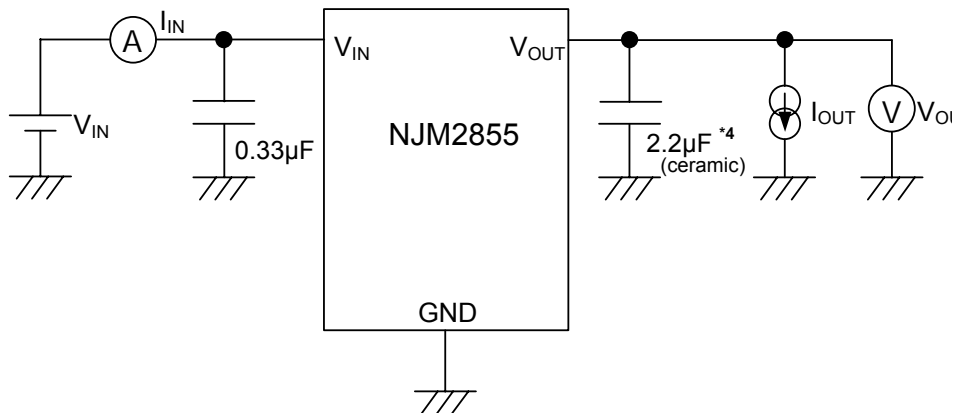
The above specification is a common specification for all output voltages.

Therefore, it may be different from the individual specification for a specific output voltage.

■ POWER DISSIPATION vs. AMBIENT TEMPERATURE

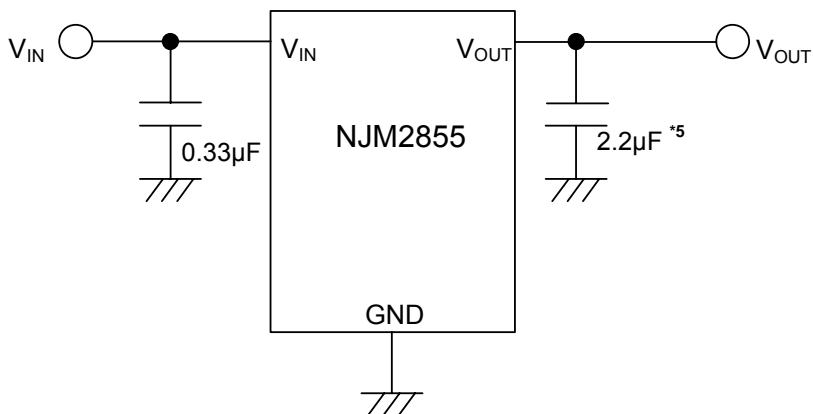


■ TEST CIRCUIT



*4 1.7V < V_o ≤ 2.6V version: $C_o=4.7\mu\text{F}$ (ceramic)
 $V_o \leq 1.7\text{V}$ version: $C_o=10\mu\text{F}$ (ceramic)

■ TYPICAL APPLICATION



*5 1.7V < V_o ≤ 2.6V version: $C_o=4.7\mu\text{F}$
 $V_o \leq 1.7\text{V}$ version: $C_o=10\mu\text{F}$

***Input Capacitance C_{IN}**

Input Capacitance C_{IN} is required to prevent oscillation and reduce power supply ripple for applications with high power supply impedance or a long power supply line.

Use the C_{IN} value of $0.33\mu\text{F}$ greater to avoid the problem.

C_{IN} should connect between GND and V_{IN} as short as possible.

***Output Capacitance C_o**

Output capacitor (C_o) is required for a phase compensation of the internal error amplifier. The capacitance and the equivalent series resistance (ESR) influence stability of the regulator.

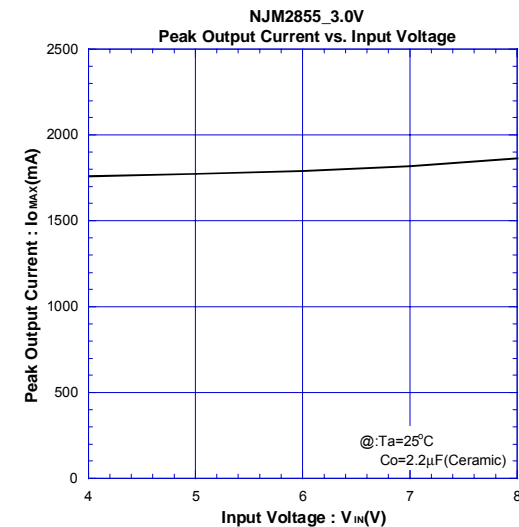
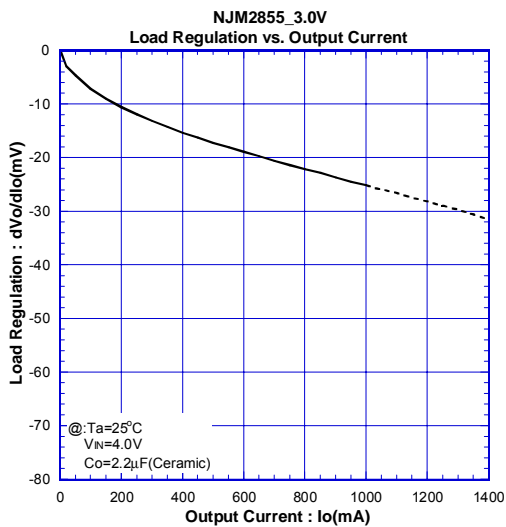
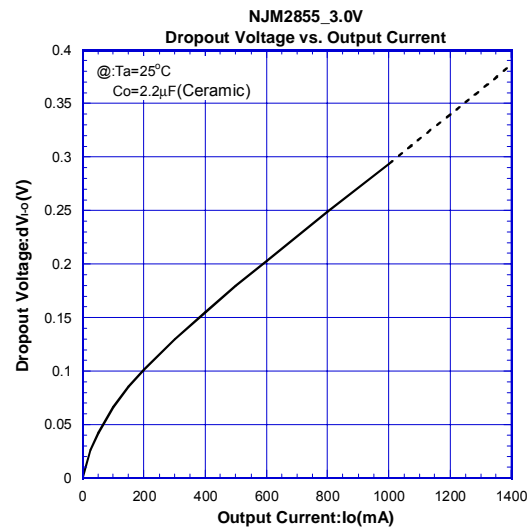
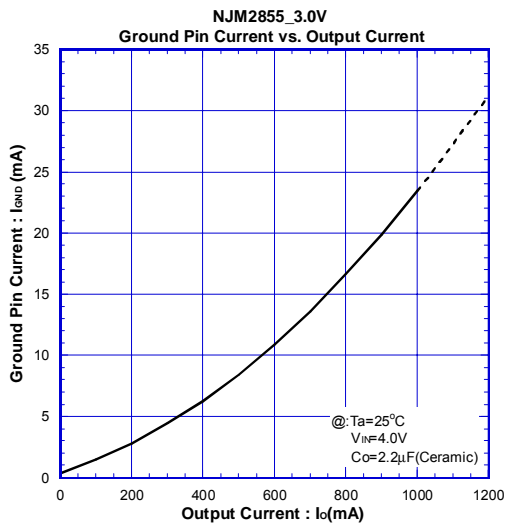
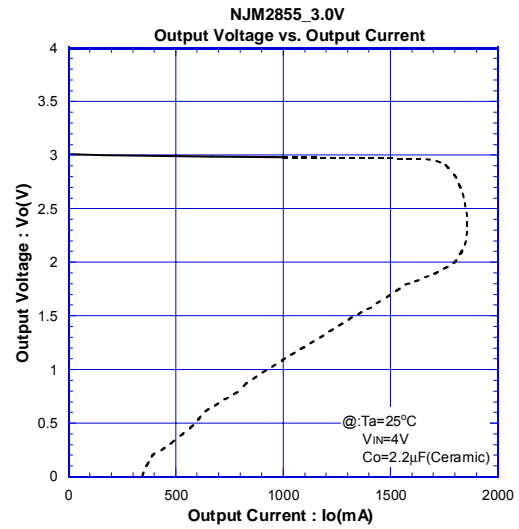
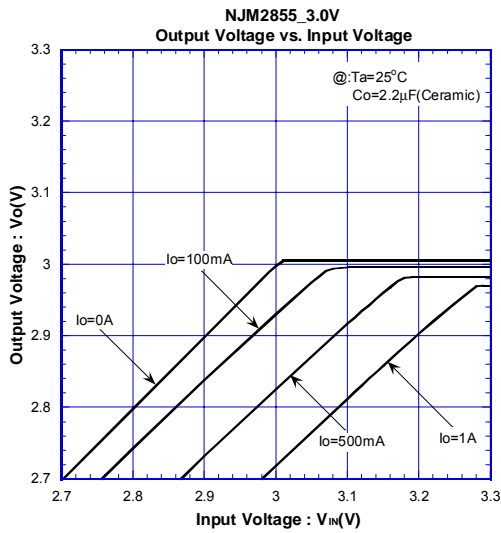
If use a smaller C_o , it may cause excess output noise or oscillation of the regulator due to lack of the phase compensation. Therefore, use C_o with the recommended capacitance or greater value and connect between V_o terminal and GND terminal with minimal wiring.

The recommended capacitance depends on the output voltage. Low voltage regulator requires greater value of the C_o . Thus, check the recommended capacitance for each output voltage.

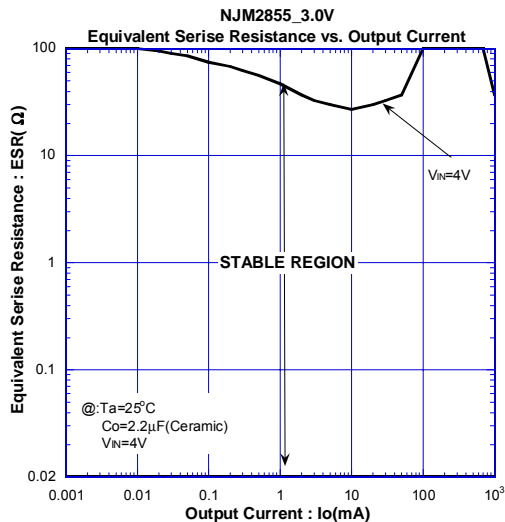
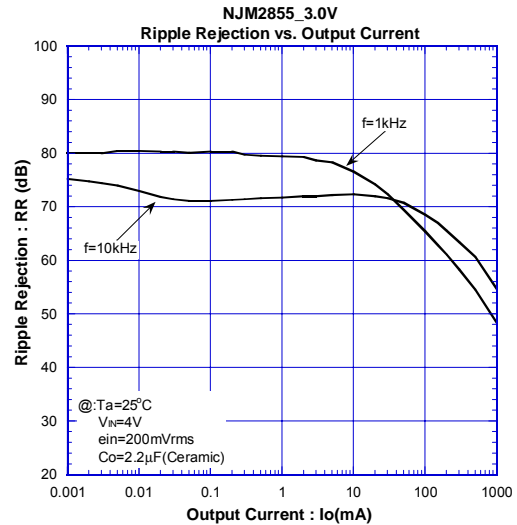
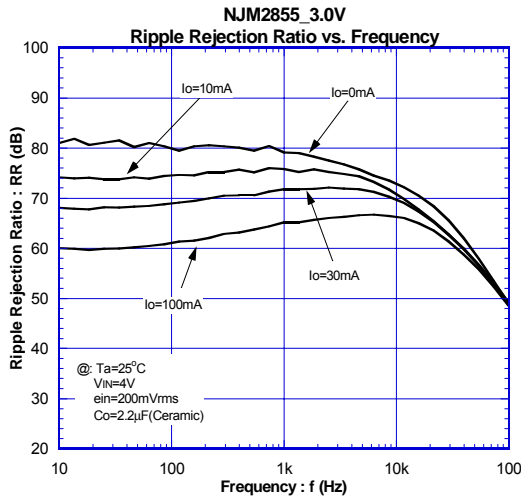
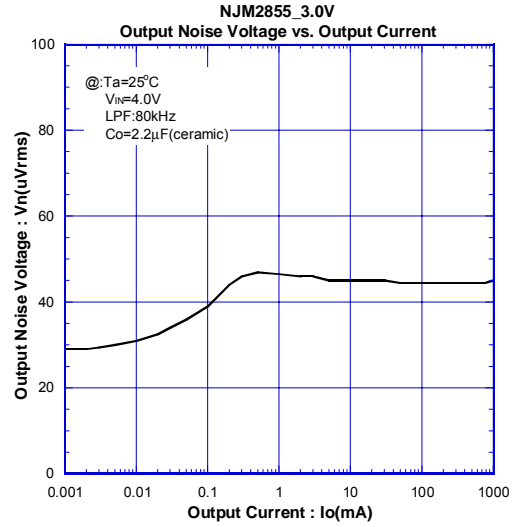
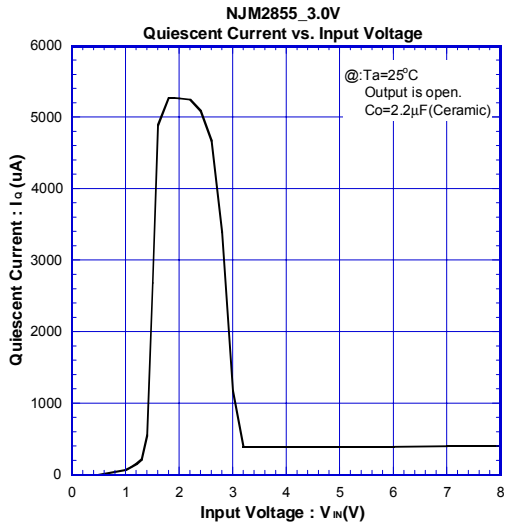
Use of a greater C_o reduces output noise and ripple output, and also improves transient response of the output voltage against rapid load change.

This product is designed to work with any capacitor including a low ESR capacitor for the C_o ; however, refer "Equivalent Series Resistance vs. Output Current" and choose suitable capacitor.

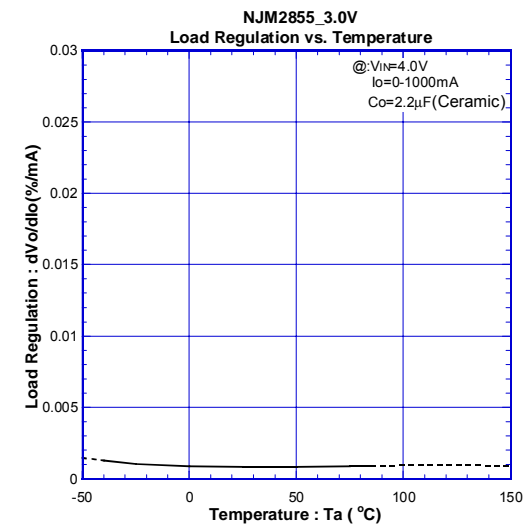
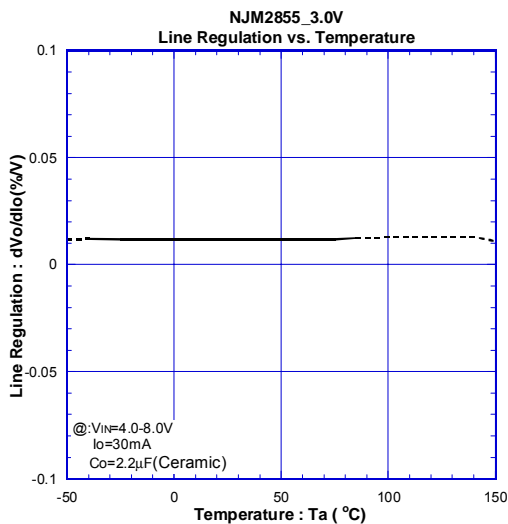
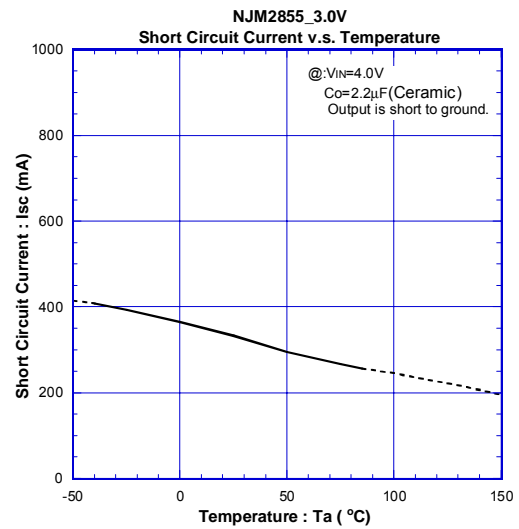
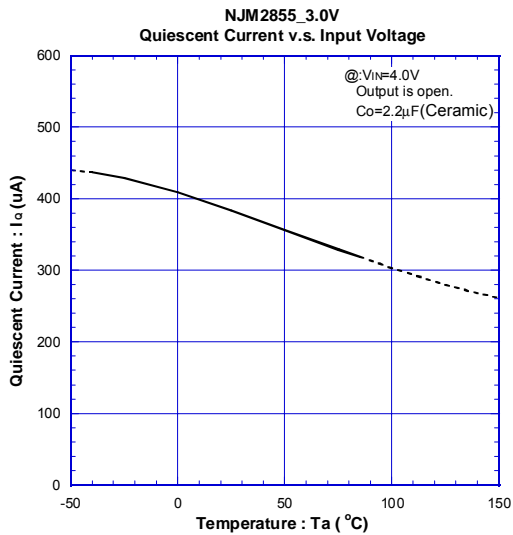
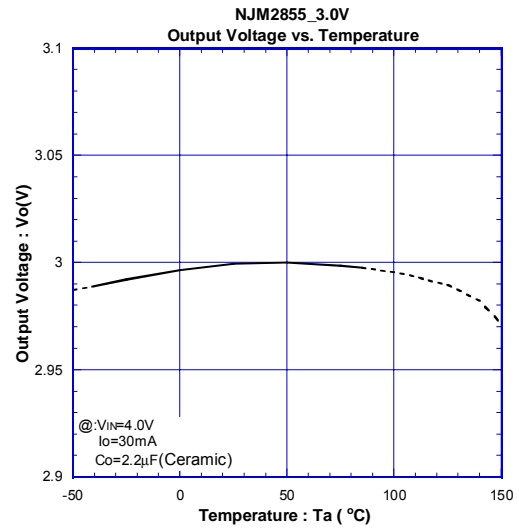
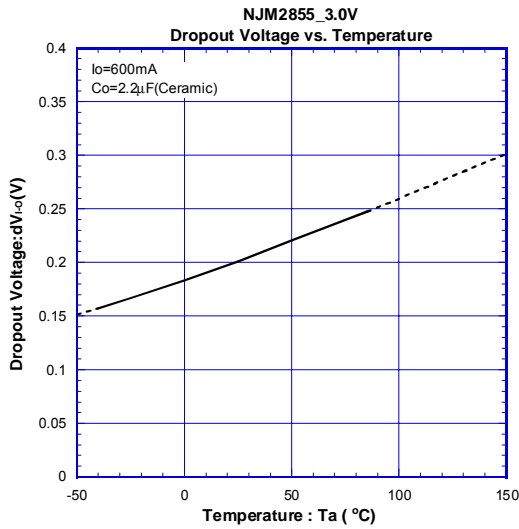
TYPICAL CHARACTERISTICS



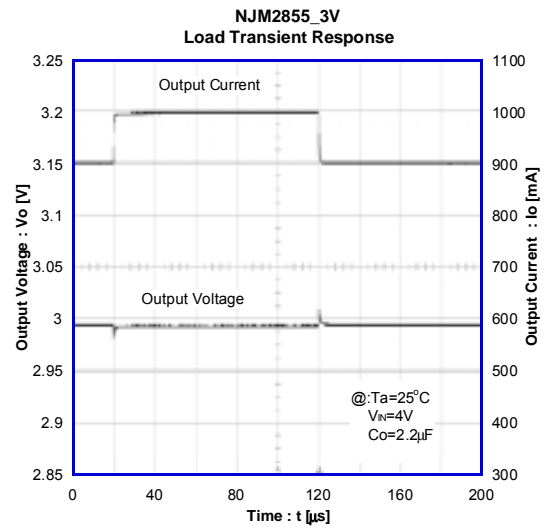
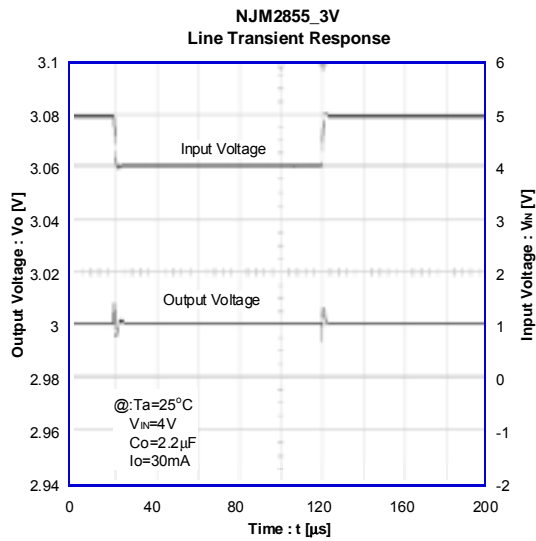
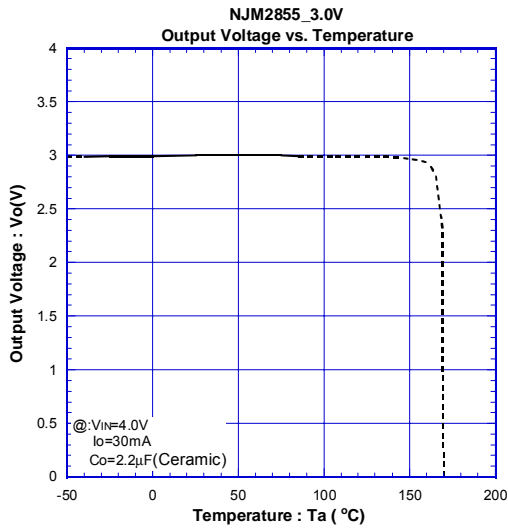
TYPICAL CHARACTERISTICS



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TYPICAL CHARACTERISTICS



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