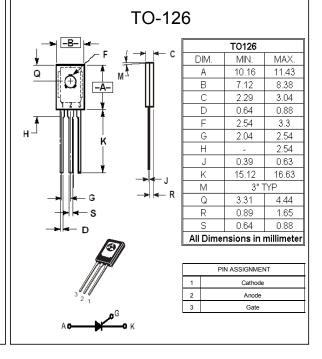


Sensitive Gate Sillicon Controlled Rectifiers Reverse Blocking Thyristors

SCRs 4 AMPERES RMS 600 VOLTS

FEATURES

- Glass-Passivated Surface for Reliability and Uniformity
- Power Rated at Economical Prices
- Practical Level Triggering and Holding Characteristics
- Flat, Rugged, Thermopad Construction for Low Thermal
- Resistance, High Heat Dissipation and Durability



MAXIMUM RATINGS (Tj= 25℃ unless otherwise noticed)

Rating	Symbol	Value	Unit
Peak Repetitive Off– State Voltage (TJ= -40 to 110℃, Sine Wave, 50 to 60 Hz; Gate Open)	VDRM, VRRM	600	Volts
On-State RMS Current (Tc = 80°C) 180° Conduction Angles	IT(RMS)	4.0	Amp
Peak Non-Repetitive Surge Current (1/2 Cycle, Sine Wave, 60 Hz, TJ = 25℃)	Ітѕм	25	Amps
Circuit Fusing Consideration (t = 8.3 ms)	l t	2.6	A ² s
Forward Peak Gate Power	Рсм	0.5	Watt
Forward Average Gate Power	PG(AV)	0.1	Watt
Operating Junction Temperature Range @ Rate VDRM and VRRM	TJ	-40 to +110	°C
Storage Temperature Range	Tstg	-40 to +150	°C
Notice: (1) VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for	Re	v.0, Jul-2007, KT	TXD06

Notice: (1) VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded



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INCRIVIAL	. CHANAC LENIO II	-

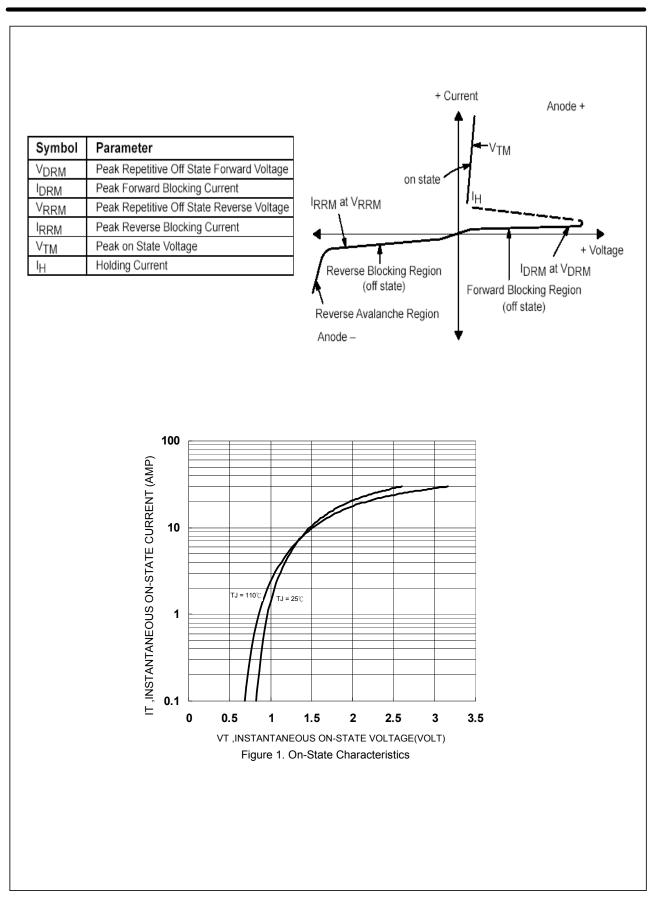
Characteristic	Symbol	Value	Unit
Thermal Resistance - Junction to Case (AC) - Junction to Ambient	RthJC RthJA	8.33 80	°C/W
Maximum Lead Temperature for Soldering Purposes 1/16" from Case for 10 Seconds	TL	260	$^{\circ}$

ELECTRICAL CHARACTERISTICS (TJ=25°C unless otherwise noted)

Characteristics	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Peak Reptitive Forward or Reverse Blocking Current (VAK=Rated VDRM and VRRM; RGK =1K Ohms) TJ = 25° C TJ = 110° C	IDRM IRRM			10 200	uA
ON CHARACTERISTICS				•	
Peak Forward On-State Voltage (ITM= 8.2A Peak, Pulse Width ≤ 1 to 2 ms, Duty Cycle $\leq 2\%$)	Vтм			2.2	Volts
Gate Trigger Current (VAK = 12 V; RL = 100 Ohms) (1)	lgт			200	uA
Holding Current (VAK = 12 V, RL = 100 Ohms)	Ін			5.0	mA
Gate Trigger Voltage (V _{AK} = 12 V ; R _L =100 Ohms) (1)	VgT			0.8	Volts
Latching Current (VAK = 12 V, RL = 100 Ohms)	IL			10	mA
DYNAMIC CHARACTERISTICS					
Critical Rate of Rise of Off-State Voltage (VAK =0.67% Rated VDRM,Exponential Waveform, TJ=110°C ,RGK=1Kohm)	dv/dt		10		V/us

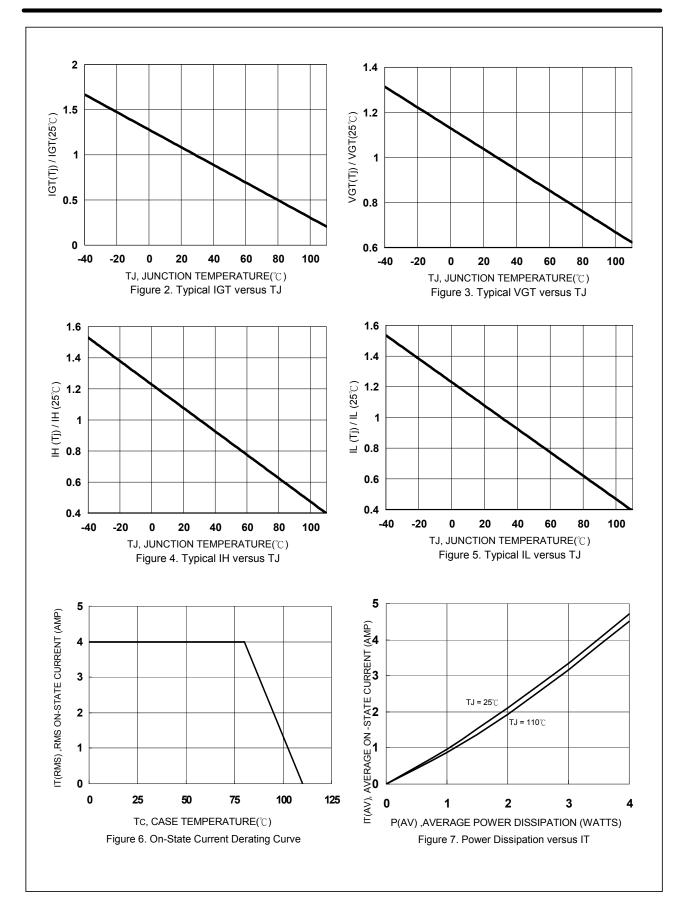
⁽¹⁾ RGK current is not included in measurement



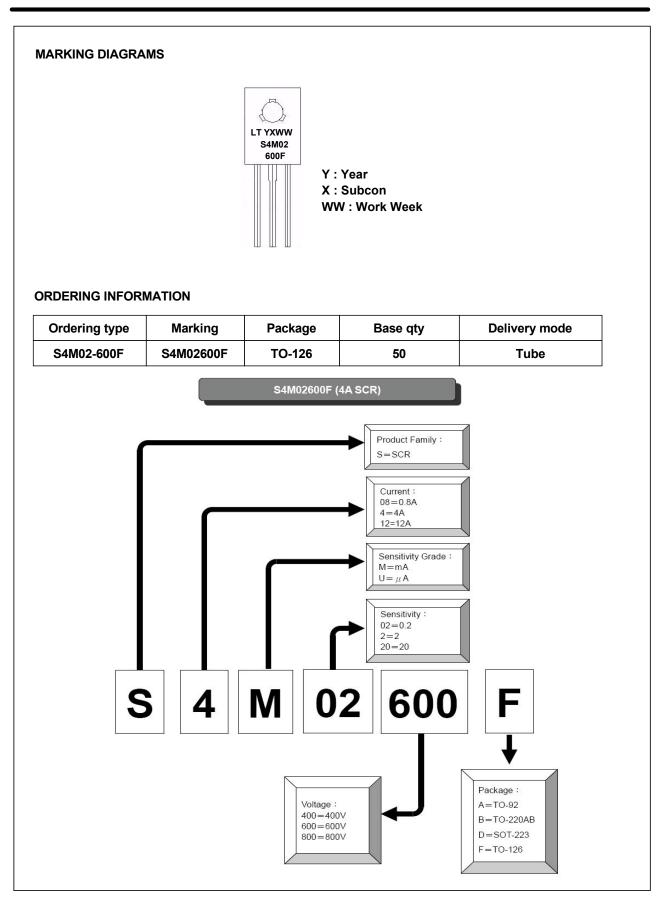


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