UNR5225G, UNR5226G

Silicon NPN epitaxial planar type

For muting

■ Features

- \bullet Low collector-emitter saturation voltage $V_{\text{CE}(\text{sat})}$, optimum for the muting circuit
- The use with high current value is possible

■ Resistance by Part Number

M	(R_1)	(R_2)	
• UNR5225G	FZ	$10~\mathrm{k}\Omega$	_
• UNR5226G	FY	$4.7~\mathrm{k}\Omega$	

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	30	V	
Collector-emitter voltage (Base open)	V _{CEO}	20	V	
Emitter-base voltage (Collector open)	V_{EBO}	5	V	
Collector current	I_C	600	mA	
Total power dissipation	P _T	150	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

■ Package

- Code SMini3-F2
- Pin Name
 - 1: Base
 - 2: Emitter
 - 3: Collector

■ Internal Connection

$$\begin{array}{c} R_1 \\ B \circ \longrightarrow W \\ R_2 \\ \end{array} \quad \begin{array}{c} C \\ \\ C \\ \end{array}$$

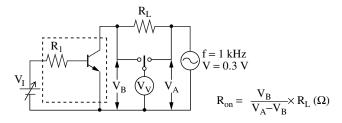
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

Parar	neter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base vol	tage (Emitter open)	V_{CBO}	$I_{\rm C} = 1 \ \mu A, \ I_{\rm E} = 0$	30			V
Collector-emitter voltage (Base open)		V _{CEO}	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	20			V
Emitter-base voltag	ge (Collector open)	V_{EBO}	$I_E = 1 \mu A, I_C = 0$	5			V
Collector-base cutoff	current (Emitter open)	I_{CBO}	$V_{CB} = 30 \text{ V}, I_{E} = 0$			1	μΑ
Emitter-base cutoff cu	urrent (Collector open)	I_{EBO}	$V_{EB} = 5 \text{ V}, I_{C} = 0$			1	μΑ
Forward current t	ransfer ratio	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 50 \text{ mA}$	100		600	_
Collector-emitter saturation voltage		V _{CE(sat)}	$I_C = 50 \text{ mA}, I_B = 2.5 \text{ mA}$			80	mV
Input resistance	UNR5226G	R_1		-30%	4.7	+30%	kΩ
	UNR5225G				10		
ON resistance *	UNR5226G	R _{on}	$V_I = 7 \text{ V}, R_L = 1 \text{ k}\Omega, f = 1 \text{ kHz}$		0.95		Ω
	UNR5225G				1.5		
Transition freque	ncy	f_T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz

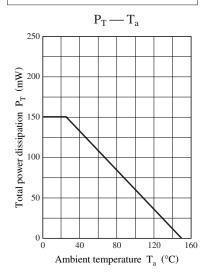
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

^{2. *:} Refer to Ron measurement circuit

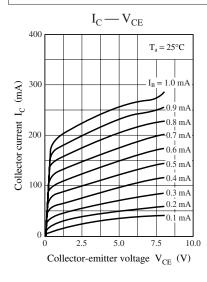
- Electrical Characteristics (continued) $T_a = 25$ °C ± 3 °C
- R_{on} measurement circuit

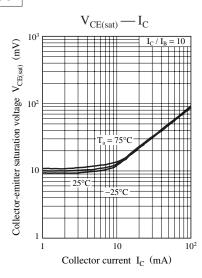


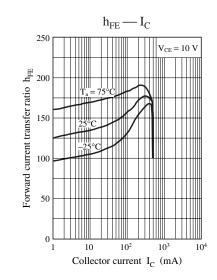
Common characteristics chart



Characteristics charts of UNR5225G

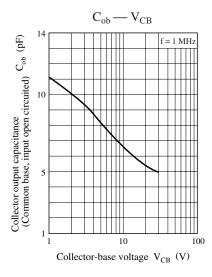


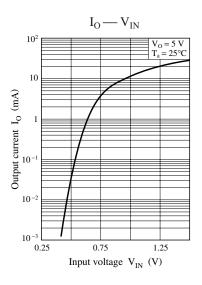


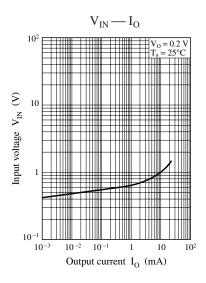


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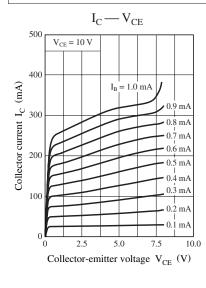
Panasonic

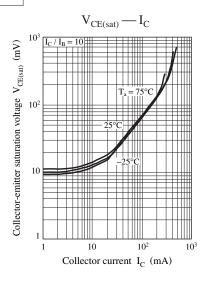


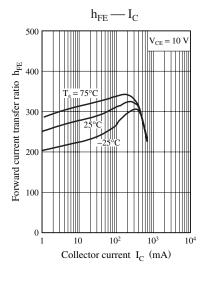


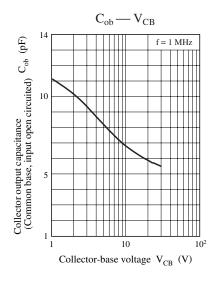


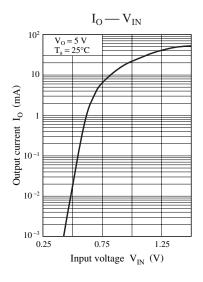
Characteristics charts of UNR5226G

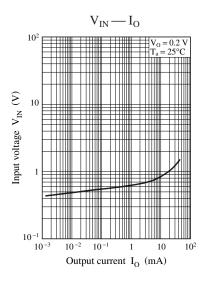






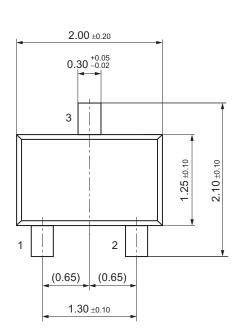


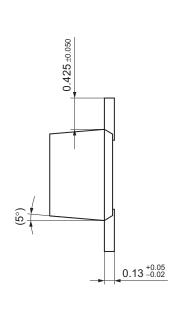


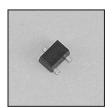


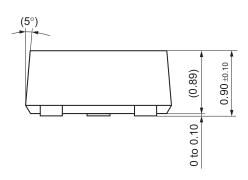
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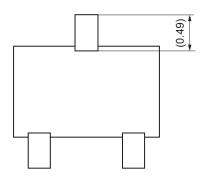
SMini3-F2 Unit: mm











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