UNR92AAJ

Silicon NPN epitaxial planar type

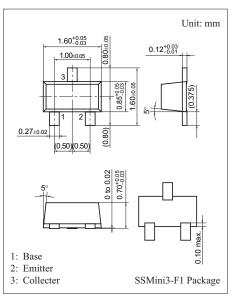
For digital circuits

Features

- Optimum for high-density mounting and downsizing of the equipment
- Contribute to low power consumption

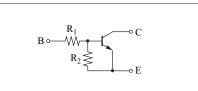
Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	50	V	
Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Collector current	I _C	80	mA	
Total power dissipation	P _T	125	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55 to +125	°C	



Marking Symbol: HL

Internal Connection



Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, \ I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{\rm CB} = 50$ V, $I_{\rm E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = 6 \text{ V}, I_{\rm C} = 0$			0.1	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$	80			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0.3 \text{ mA}$			0.25	V
Output voltage high-level	V _{OH}	$V_{CC} = 5 V, V_B = 0.5 V, R_L = 1 k\Omega$	4.9			V
Output voltage low-level	V _{OL}	$V_{CC} = 5 V, V_B = 5 V, R_L = 1 k\Omega$			0.2	V
Input resistance	R ₁		-30%	100	+30%	kΩ
Resistance ratio	R ₁ / R ₂		0.8	1.0	1.2	
Transition frequency	f _T	$V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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