

SuperBlue™ Generation II LEDs Preliminary Data Sheet C430CB230-S2100

Cree's low-current SuperBlue Generation II LEDs combine highly efficient GaN with Cree's proprietary G•SiC® substrate to deliver the ultimate price/performance for blue LEDs. The C430CB230-S2100 is designed for automotive applications or any application where wavelength stability and chip robustness are critical.

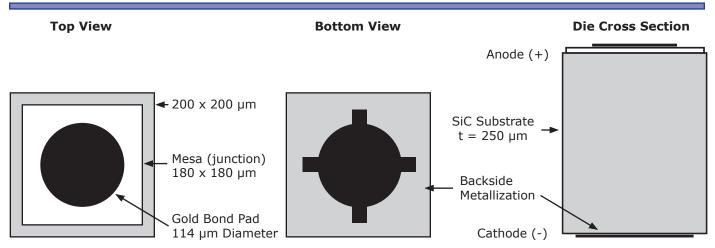
FEATURES

- Low Wavelength Variation
 - 461-465 nm at 10 mA
- High Performance
 - 0.85 mW (463 nm)
- Superior SiC Substrate Technology
- Excellent Chip-to-Chip Consistency
- High Reliability
- Single Wire Bond Structure
- Class 2 ESD Rating

APPLICATIONS

- Automotive Applications
 - Dashboard Lighting
 - Interior Lighting
 - Toggle Switch Lighting
- Electronic Signs and Displays
- Indicator Lights

C430CB230-S2100 Chip Diagram





Maximum Ratings at T _A = 25°C Notes 183	C430CB230-S2100
DC Forward Current	15 mA
Peak Forward Current (1/10 duty cycle @ 1kHz)	35 mA
LED Junction Temperature	125°C
Reverse Voltage	5 V
Operating Temperature Range	-40°C to +100°C
Storage Temperature Range	-40°C to +100°C
Electrostatic Discharge Threshold (HBM)Note 2	1000 V
Electrostatic Discharge Classification (MIL-STD-883E) ^{Note 2}	Class 2

Typical Electrical/Optical Characteristics at $T_A = 25$ °C, If = 10 mA Note 3											
Part Number	Forward Voltage (V,, V)		Radiant Flux (P, mW)				Dominant Wavelength (λ _d , nm)			Full Width Half Max (λ _p , nm)	
	Тур.	Max.	Min.	Тур.	Max.	Тур.	Min.	Тур.	Max.	Тур.	
C430CB230-S2100	4.0	4.5	0.425	0.80	10	423	461	463	465	59	

Mechanical Specifications	C430CB230-S0100			
Description	Dimension	Tolerance		
P-N Junction Area (μm ²)	180 x 180	± 35		
Top Area (μm ²)	200 x 200	± 35		
Bottom Area (μm ²)	200 x 200	± 35		
Chip Thickness (µm)	250	± 25		
Au Bond Pad Diameter (μm)	114	± 20		
Au Bond Pad Thickness (µm)	1.1	± 0.5		
Back Contact Metal Diameter (µm)	114	± 20		

Notes:

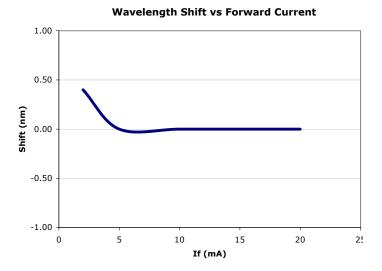
- 1. Maximum ratings are package dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Ratings for other packages may differ. The forward currents (DC and Peak) are not limited by the die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (<5 seconds).
- Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown. The ESD classification of Class II is based on sample testing according to MIL-STD-883E.
- 3. All products conform to the listed minimum and maximum specifications for electrical and optical characteristics, when assembled and operated at 10 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by the manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Optical characteristics measured in an integrating sphere using Illuminance A.



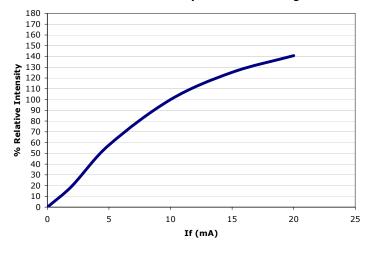
Characteristic Curves

These are representative measurements for the CB230 LED product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

Forward Current vs Forward Voltage 25.0 22.5 20.0 17.5 15.0 15.0 12.5 10.0 10.0 7.5 5.0 2.5 0.0 0.0 1.0 2.0 3.0 4.0 5.0 Vf (V)



Relative Intensity vs Forward Voltage





Radiation Pattern

This is a representative radiation pattern for the CB230 LED product. Actual patterns will vary slightly for each chip.

