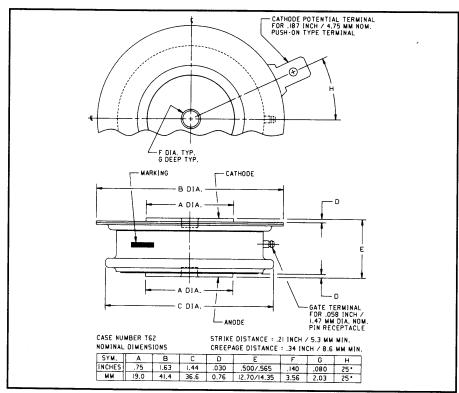


Powerex, Inc., 200 Hillis Street, Youngwood, Pennsylvania 15697-1800 (412) 925-7272 Powerex, Europe, S.A. 428 Avenue G. Durand, BP107, 72003 Le Mans, France (43) 41.14.14

Phase Control SCR 300 Amperes Average 1300 Volts



C380__X555 Phase Control SCR 300 Amperes Average, 1300 Volts

Scale = 2"

C380__X555 (Outline Drawing)

Ordering Information:

Select the complete nine or ten digit part number you desire from the table, i.e. C380PCX555 is a 1300 Volt, 300 Ampere Phase Control SCR.

	Voltage		Current	
Туре	V _{DRM} V _{RRM}	Code	I _{T(av)}	
C380X555	400	D	300	
	600	М		
	800	N		
	1000	Р		
	1200	PB		
	1300	PC		

Description:

Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak (Pow-R-Disc) devices employing the field-proven amplifying (di/namic) gate.

Fea	atures: Low On-State Voltage		
	High di/dt		
	High dv/dt		
	Hermetic Packaging		
	Excellent Surge and I ² t Ratings		
	High Temperature Operation		
Applications			

applications:

	Power	Supplies
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	Battery	Chargers	S
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C380__X555 Phase Control SCR 300 Amperes Average, 1300 Volts

Absolute Maximum Ratings

Absolute Maximum Hamis			
	Symbol	C380X555	Units
RMS On-State Current @ T _C = 85°C	I _{T(RMS)}	450	Amperes
Average On-State Current @ T _C = 85°C	I _{T(av)}	300	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (60Hz)	I _{TSM}	3200	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (50Hz)	I _{TSM}	2900	Amperes
Critical Rate-of-Rise of On-State Current (Non-Repetitive)	di/dt	600	Amperes/μs
Critical Rate-of-Rise of On-State Current (Repetitive)	di/dt	300	Amperes/μs
l²t (for Fusing), 8.3 milliseconds	l²t	42,000	A ² sec
Peak Gate Power Dissipation	P _{GM}	10	Watts
Average Gate Power Dissipation	P _{G(av)}	2	Watts
Storage Temperature	T _{STG}	-40 to 150	*C
<u> </u>		-40 to 150	•C
Operating Temperature		720 to 880	lb.
Mounting Force			kN
Mounting Force		3.2 to 3.92	KIY

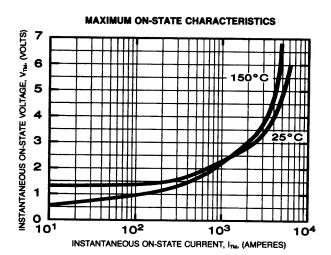
Electrical and Thermal Characteristics

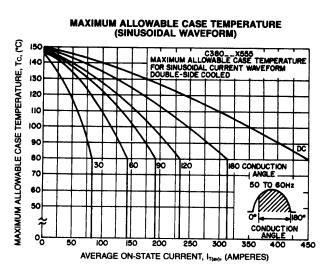
Characteristics	Symbol	Symbol Test Conditions		Units
Voltage—Blocking State Maximums		T _{.i} = 150°C, V = V _{DRM}	45	mA
Forward Leakage, Peak	IDRM		45	mA
Reverse Leakage, Peak	IRRM	$T_J = 150^{\circ}C$, $V = V_{RRM}$	45	111/3
Current—Conducting State Maximums Peak On-State Voltage	V _{TM}	$I_{TM} = 1500A$ Peak, $T_C = 25^{\circ}C$	2.85	Volts
Switching Typical Turn-Off Time	t _q	$T_J=+150^{\circ}\text{C},\ I_{TM}=250\ \text{Amperes},\ V_R=50\ \text{Volts}\ \text{Minimum},\ V_{DRM}\ (\text{Reapplied}),\ \text{Rate-of-Rise of Reapplied Off-State}\ \text{voltage}=20\ \text{Volts}/\mu\text{sec}\ (\text{Linear})\ \text{Gate Bias During Turn-off Interval}=0\ \text{Volts},\ 100\Omega.\ \text{Duty Cycle}\leq0.01\%$	75	μsec
Typical Delay Time	t _d	T _C = +25°C, I _T = 100 Adc, V _{DRM} = Rated Gate Supply: 10 Volt Open Circuit, 25 ohm, 0.1 μsec maximum rise time	1.0	μsec
Min. Critical dv/dt exponential to V _{DRM}	dv/dt	T _J = 150°C, Gate Open	200	V/µsec
Thermal Maximum Thermal Resistance, double sided cooling Junction to Case	R _{esc}		0.095	°C/Watt
Case to Sink, Lubricated	R _{ecs}		0.02	°C/Watt
Gate Maximum Parameters Gate Current to Trigger	l _{GT}	$T_{C}=25^{\circ}\text{C},V_{D}=6 ext{Vdc};R_{L}=3\Omega$	150	mA
Gate Voltage to Trigger	V _{GT}	$T_C = -40$ °C to 150°C, $V_D = 6$ Vdc, $R_L = 3\Omega$	3	Volts
Non-Triggering Gate Voltage	V _{GDM}	$T_J = 150$ °C, $R_L = 1000\Omega$, Rated V_{DRM}	0.15	Volts
Peak Forward Gate Current	I _{GTM}		10	Amperes
Peak Reverse Gate Voltage	V _{GRM}		5	Volts

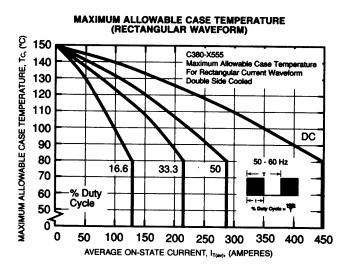


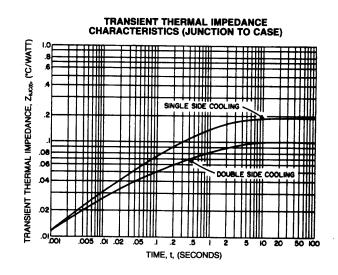
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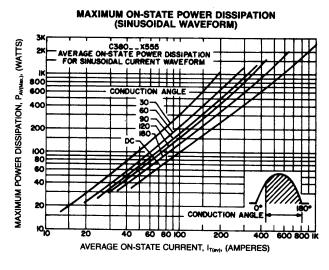
C380_ _X555 Phase Control SCR 300 Amperes Average, 1300 Volts

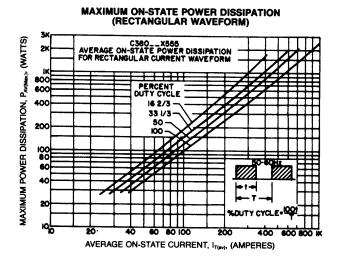










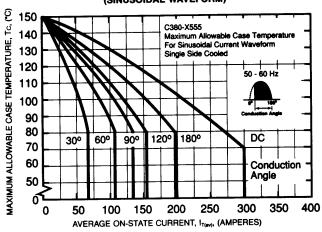




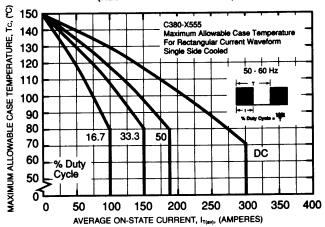
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C380_ _X555 Phase Control SCR 300 Amperes Average, 1300 Volts

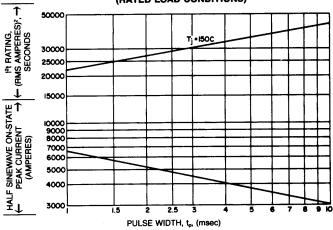
MAXIMUM ALLOWABLE CASE TEMPERATURE (SINUSOIDAL WAVEFORM)



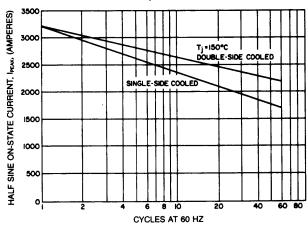
MAXIMUM ALLOWABLE CASE TEMPERATURE (RECTANGULAR WAVEFORM)



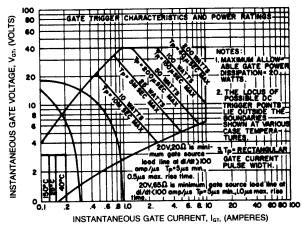
SUB-CYCLE SURGE AND I²t RATINGS (RATED LOAD CONDITIONS)



MAXIMUM ALLOWABLE SURGE ON-STATE CURRENT (NON-REPETITIVE)



GATE CHARACTERISTICS



NOTES:

- Maximum allowable gate power dissipation = 2 watts.
 The locus of possible DC trigger points lie outside the
- The locus of possible DC trigger points lie outside the boundaries shown at various case temperatures.
- 3. Tp = Rectangular Gate Current Pulse Width.