

Prepared		Product Specifications AN7586	Ref No.	A - 1
Checked			Total Page	12
Approved			Page No.	1

Structure	Silicon Monolithic Bipolar IC
Appearance	SIL-7 Pins Plastic Package (Power Type With Fin)
Application	Low Frequency Amplifier
Function	Mono 10W Audio Power Amplifier, with standby circuit and incorporating protection circuits

A	Absolute Maximum Ratings				
No.	Item	Symbol	Ratings	Unit	Note
1	Storage Temperature	Tstg	-55 ~ +150	° C	1
2	Operating Ambient Temperature	Topr	-25 ~ +75	° C	1
3	Operating Ambient Pressure	Popr	$1.013 \times 10^5 \pm 0.61 \times 10^5$ (1.0 ± 0.6)	Pa (atm)	
4	Operating Constant Acceleration	Gopr	9,810 (1,000)	m / s ² (G)	
5	Operating Shock	Sopr	4,900 (500)	m / s ² (G)	
6	Power Supply Voltage	Vcc	35.0	V	
7	Power Supply Current	Icc	2.0	A	
8	Power Dissipation	PD	18.7	W	2

Operating Supply Voltage Range	Vcc	10.0 V ~ 32.0 V
--------------------------------	-----	-----------------

Note : 1) Except these items, all other measurements are taken at Ta = 25 °C.

2) Ta = 75 °C with infinite heat sink.

Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications AN7586	Ref No.	A-2
Checked			Total Page	12
Approved			Page No.	2

A	Absolute Maximum Ratings				
No.	Item	Symbol	Ratings	Unit	Note
1	Pin Voltage (Pin 3)	V ₃	-0.3 ~ +3.0	V	

Note) For the above mentioned terminals do not apply a voltage or current that is outside the described range.

Eff. Date	Eff. Date	Eff. Date	Eff. Date	
11-Feb-1999				

Prepared		Product Specifications AN7586	Ref No.	B - 1
Checked			Total Page	12
Approved			Page No.	3

No.	Item	Symbol	Test Cct.	Condition	Limit			Unit	Note
					Min	Typ	Max		
					B Electrical Characteristics (Ambient temperature is 25°C±2°C unless otherwise specified)				
1	Quiescent Current	I _{cq}		V _{in} =0mV	-	30	60	mA	
2	Output End Noise Voltage	V _{no}		No input, R _g =10k	-	0.22	0.4	mV	2
3	Voltage Gain	G _v		V _{in} =57mV	32	34	36	dB	
4	Total Harmonic Distortion	THD		V _{in} =57mV	-	0.2	0.4	%	
5	Maximum Output Power	P _o		V _{cc} =26V, THD=10%	8.0	10.0	-	W	
6	Ripple Rejection Ratio	RR		V _r =1 V _{rms} , f _r =120Hz, R _g =10k	45	55	-	dB	2
7	Stand-by On Voltage	V _{stb-on}		No input I _{cc} ≤ 0.1mA	-	-	5.0	V	
8	Stand-by Off Voltage	V _{stb-off}		No input I _{cc} ≥ 9.5mA	8.5	-	-	V	

Note : 1) f=1kHz, R_L=8Ω, V_{cc}=26V, unless otherwise stated

2) For this measurement, use the 20Hz~20kHz (12dB/OCT) filter.

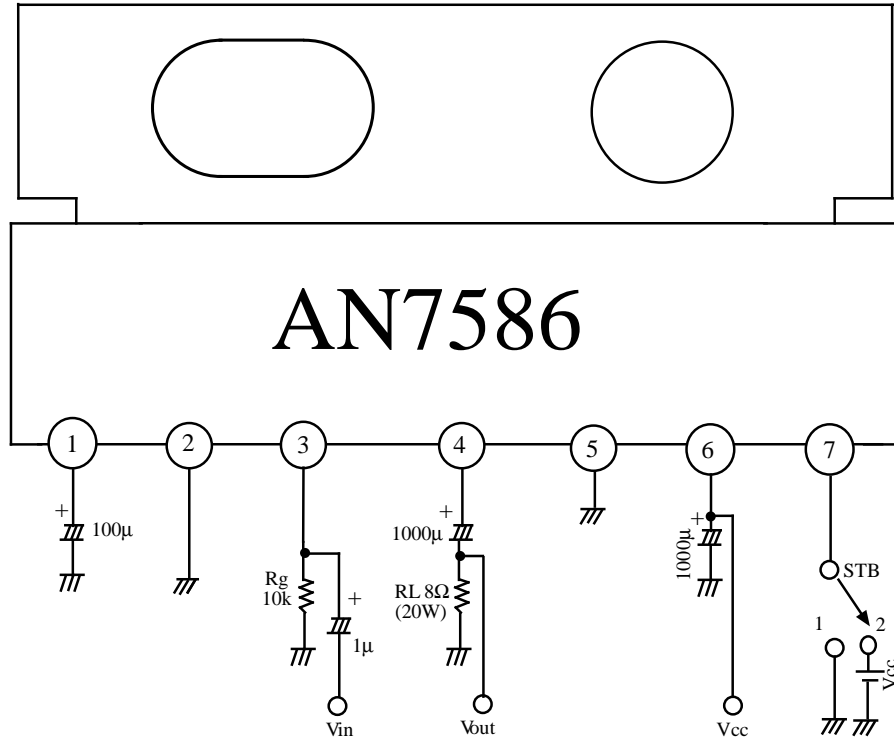
Eff. Date	Eff. Date	Eff. Date	Eff. Date	
11-Feb-1999				

Prepared		Product Specifications	Ref No.	C - 1
Checked			Total Page	12
Approved			Page No.	4

AN7586

Description of Test Circuits and Test Methods

Test Circuit :



Note:

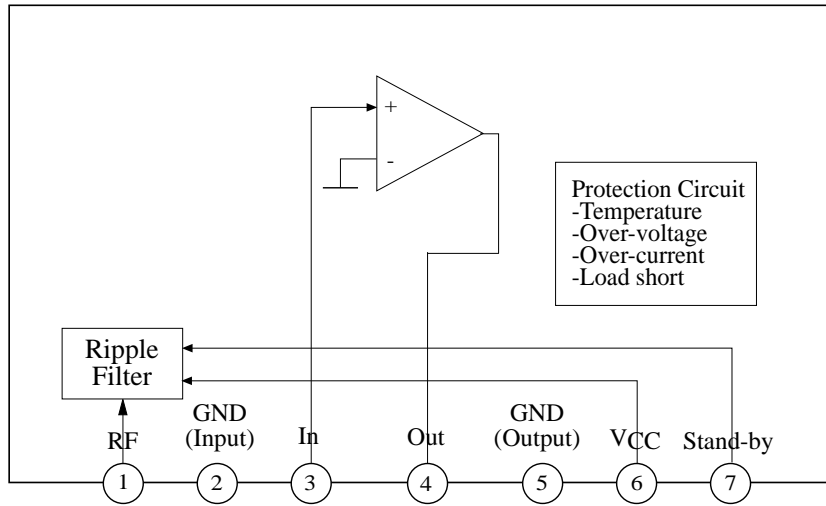
For ' OPERATIONAL MODE ', connect STB to Vcc.

Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications	Ref No.	D - 1
Checked			Total Page	12
Approved			Page No.	5

AN7586

Circuit Function Block Diagram



Pin Descriptions

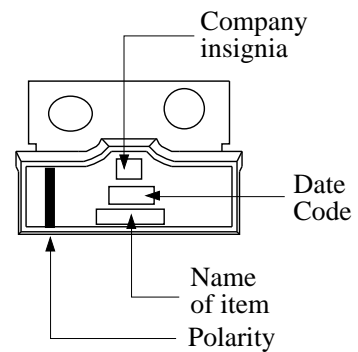
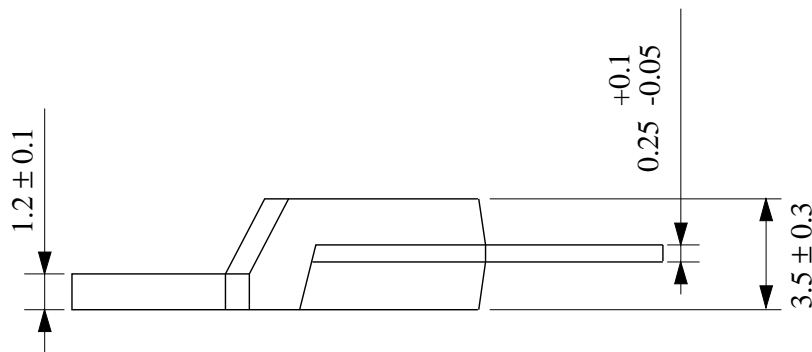
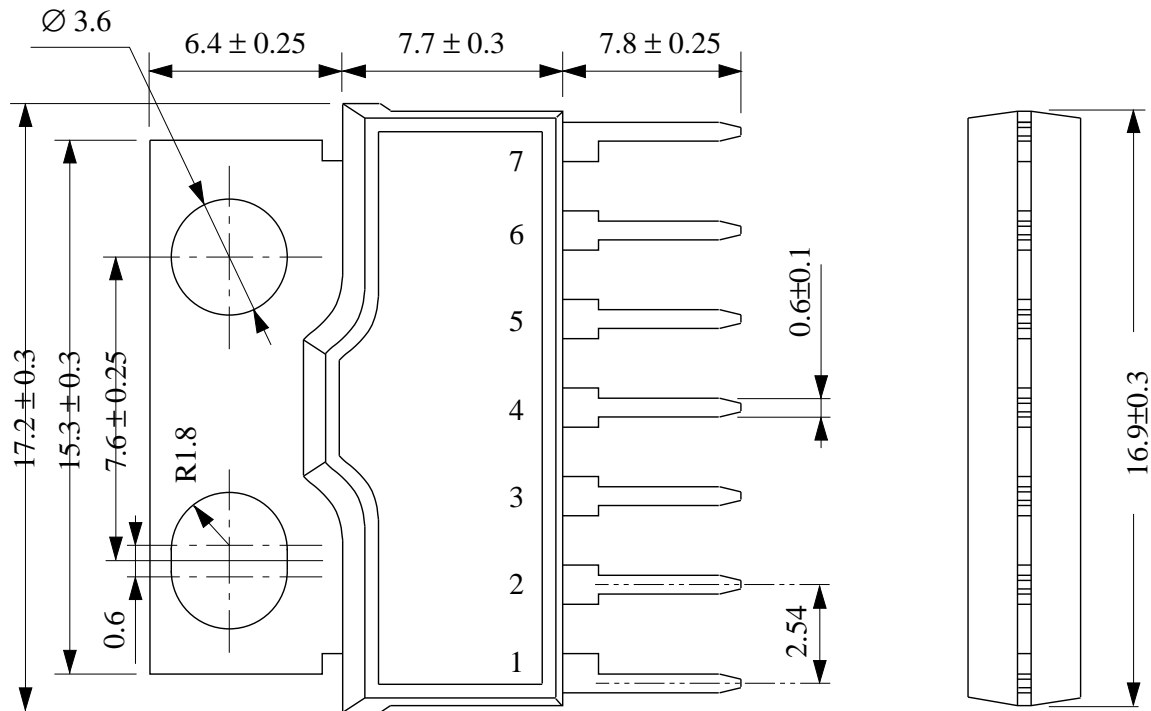
Pin No.	Pin Descriptions	Pin No.	Pin Descriptions
1	Ripple Filter	5	GND (output)
2	GND (input)	6	Vcc
3	Input	7	Standby
4	Output		

Eff. Date	Eff. Date	Eff. Date	Eff. Date	
11-Feb-1999				

Prepared		Product Specifications AN7586	Ref No.	E-1
Checked			Total Page	12
Approved			Page No.	6

Package Name	FP - 7S
--------------	---------

Unit : mm



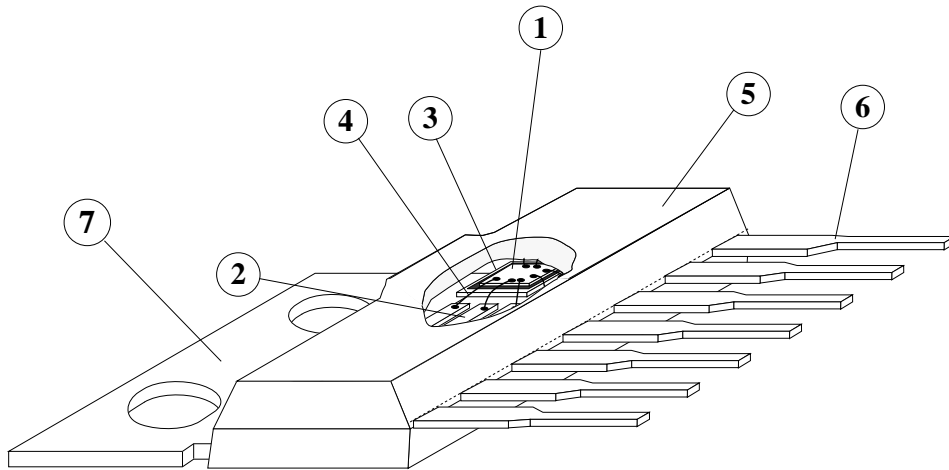
Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications AN7586	Ref No.	F
Checked			Total Page	12
Approved			Page No.	7

(Structure Description)

Chip surface passivation	SiN,	PSG,	Others ()	①	
Lead frame material	Fe group,	Cu group,	Others ()	②, ⑥	
Inner lead surface process	Ag plating,	Au plating,	Others ()	②	
Outer lead surface process	Solder plating,	Solder dip,	Others ()	⑥	
Chip mounting method	Ag paste,	Au-Si alloy,	Solder,	Others ()	③
Wire bonding method	Thermalsonic bonding,		Others ()	④	
Wire material	Au,	Diameter 38 μm	Others ()	④	
Mold material	Epoxy,		Others ()	⑤	
Molding method	Transfer mold,	Multiplunger mold,	Others ()	⑤	
Heat Fin Material	Fe group,	Cu group,	Others ()	⑦	

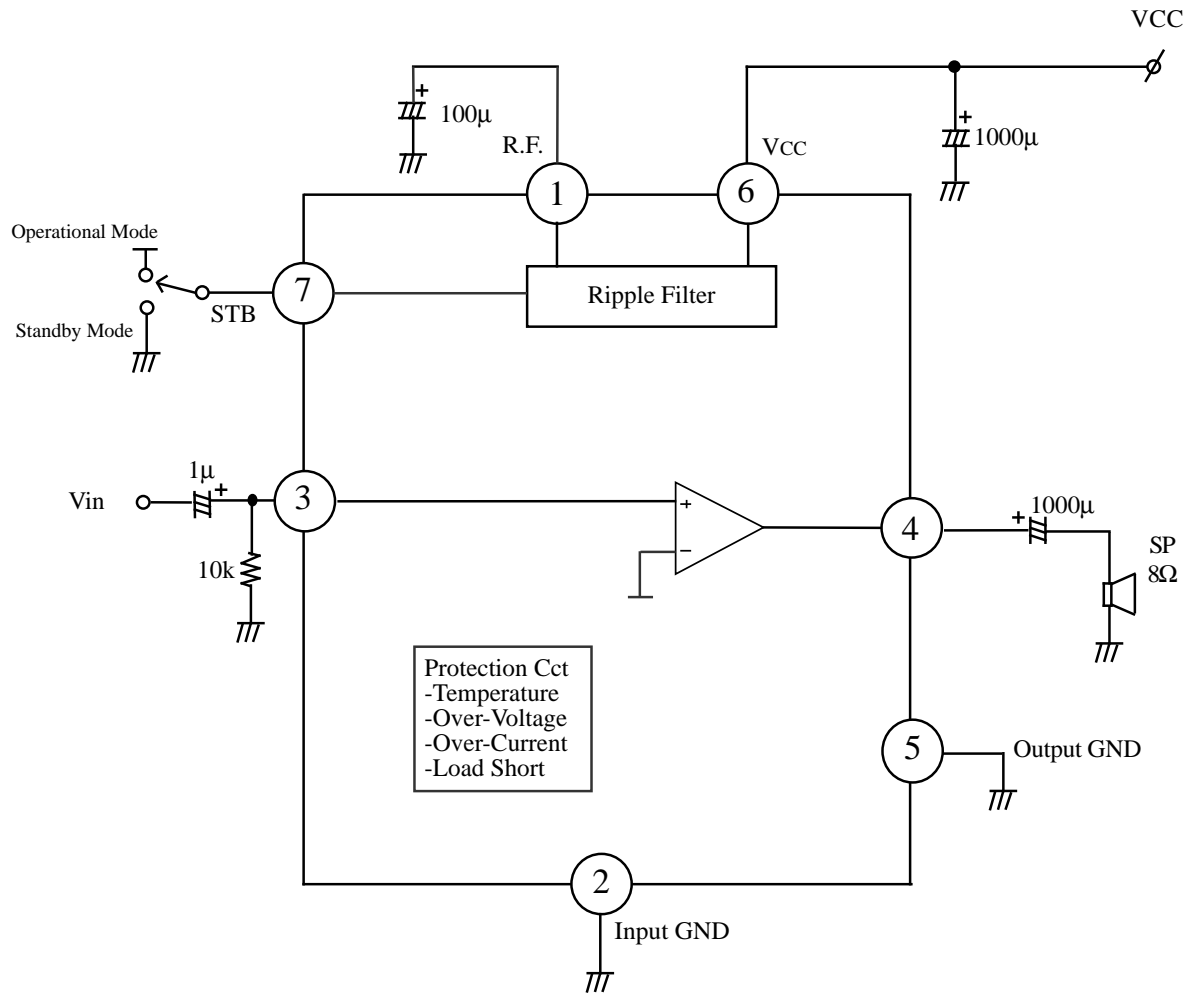
Package FP-7S



Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications AN7586	Ref No.	G - 1
Checked			Total Page	12
Approved			Page No.	8

Application Circuit



Operational Mode	VCC
Standby Mode	0V

Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications (Technical Data) AN7586	Ref No.	G - 2
Checked			Total Page	12
Approved			Page No.	9

Pin No.	Function	Adjacent Circuitry	Pins Description / Signal	DC Bias (V)
1	Ripple Filter		This is the pin to connect the positive terminal of a ripple filter capacitor.	$V_{CC} - 1.5V_{BE}$
2	Input GND		Input ground pin.	0V
3	Input		This is the amplifier input pin.	0V

Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications (Technical Data) AN7586	Ref No.	G - 2
Checked			Total Page	12
Approved			Page No.	10

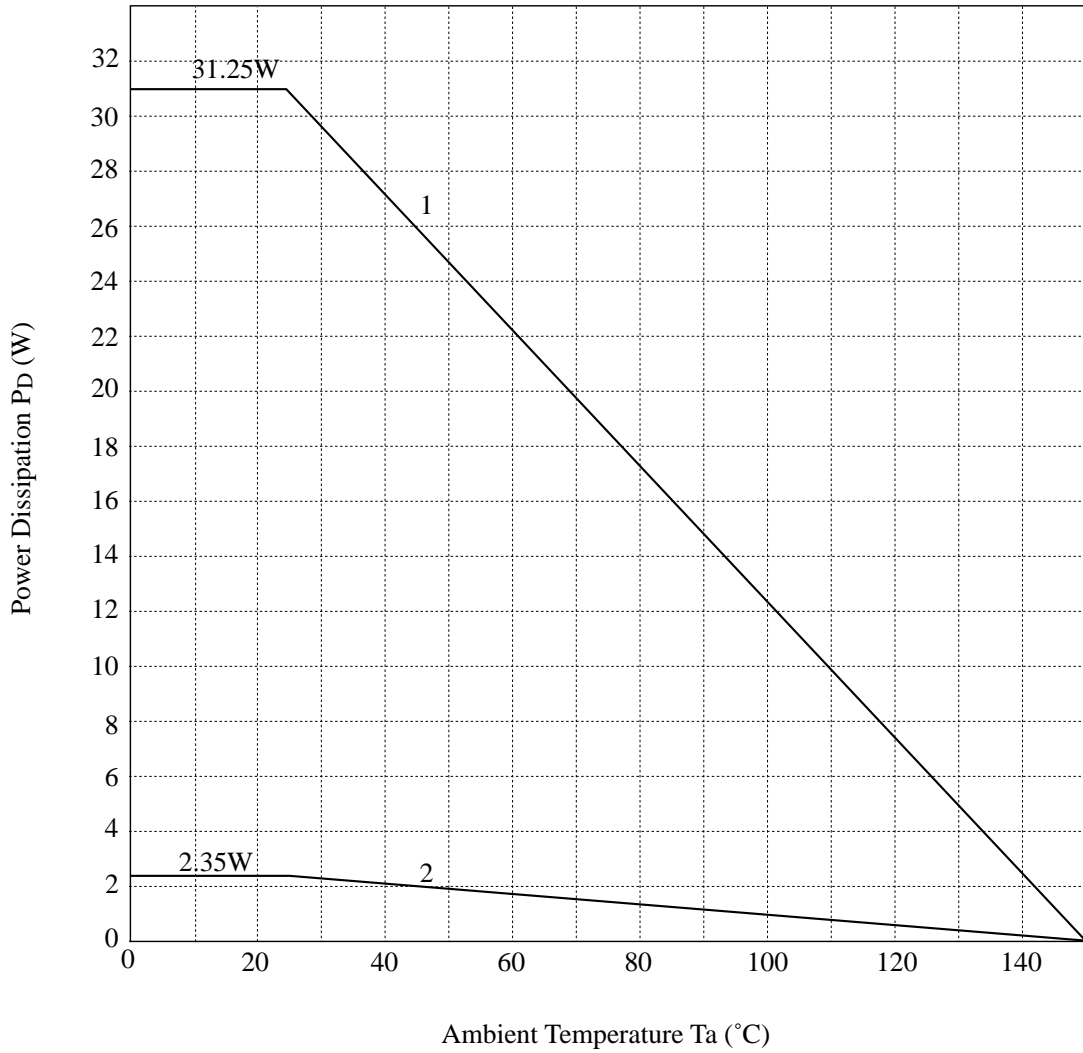
Pin No.	Function	Adjacent Circuitry	Pins Description / Signal	DC Bias (V)
4	Output		Output pin	$V_{cc}/2$
5	Output Gnd		Output ground.	0V
6	Vcc		This is the power supply pin.	Typ: 26V
7	Standby		Standby control pin. Standby Mode = 0 V Operational Mode = V_{cc}	

Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications AN7586	Ref No.	G-3
Checked			Total Page	12
Approved			Page No.	11

FP-7S Power Dissipation vs. Ambient Temperature

- 1 Infinity Heat Sink : $R_{th(j-c)} = 4 \text{ } ^\circ\text{C/W}$
- 2 No Heat Sink : $R_{th(j-a)} = 53.2 \text{ } ^\circ\text{C/W}$



Eff. Date	Eff. Date	Eff. Date	Eff. Date
11-Feb-1999			

Prepared		Product Specifications AN7586	Ref No.	H
Checked			Total Page	12
Approved			Page No.	12

Application's Precautions

- (1) External heatsink is needed when used. External heatsink should be fixed to the chassis.
- (2) Fin of the IC can be connected to GND.
- (3) Please prevent "Output to V_{CC} short", "Output to GND short", "Pin Shift" in direction of Pin 7 and "Reverse Insertion" to avoid damaging the IC.
- (4) The temperature protection circuit will operate at T_j around 150°C. However, if temperature decrease, the protection circuit will automatically be deactivated and resume normal operation.

Eff. Date	Eff. Date	Eff. Date	Eff. Date	
11-Feb-1999				