

DM7445 BCD to Decimal Decoders/Drivers

General Description

These BCD-to-decimal decoders/drivers consist of eight inverters and ten, four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of BCD input logic ensures that all outputs remain off for all invalid (10-15) binary input conditions. These decoders feature high-performance, NPN output transistors designed for use as indicator/relay drivers, or as open-collector logic-circuit drivers. The high-breakdown output transistors are compatible for interfacing with most MOS integrated circuits.

Features

- Full decoding of input logic
- 80 mA sink-current capability
- All outputs are off for invalid BCD input conditions

Connection Diagram

Dual-In-Line Package INPUTS OUTPUTS v_{cc} 0 1 2 3 4 5 6 7 8 GND OUTPUTS

Order Number DM5445J, DM5445W or DM7445N See Package Number J16A, N16E or W16A

Function Table

No.	Inputs				Outputs									
	D	С	В	Α	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
4	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
5	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
6	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
7	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
9	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
Т	Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
N	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
V	Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Α	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
ı	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
D														

H = High Level (Off), L = Low Level (On)

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Absolute Maximum Ratings (Note 1)

Operating Free Air Temperature Range

Supply Voltage 7V
Input Voltage 5.5V
Output Voltage 30V

Recommended Operating Conditions

Symbol	Parameter	DM5445			DM7445			Units
		Min	Nom	Max	Min	Nom	Max	
V _{cc}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	٧
V _{IH}	High Level Input Voltage	2			2			٧
V _{IL}	Low Level Input Voltage			0.8			0.8	٧
V _{OH}	High Level Output Voltage			30			30	٧
I _{OL}	Low Level Output Current			20			20	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Condit	ions	Min	Тур	Max	Units
					(Note 2)		
$\overline{V_{l}}$	Input Clamp Voltage	V _{CC} = Min, I _I =	V _{CC} = Min, I _I = -12 mA			-1.5	٧
I _{CEX}	High Level Output	V _{CC} = Min, V _O	= 30V			250	μΑ
	Current	V _{IL} = Max, V _{IH}	= Min				
V _{OL}	Low Level Output	V _{CC} = Min, I _{OL}	= Max		0.2	0.4	
	Voltage	$V_{IH} = Min, V_{IL} =$	= Max				٧
		I _{OL} = 80 mA			0.5	0.9	
		V _{CC} = Min					
- I ₁	Input Current @ Max	V _{CC} = Max, V _I	= 5.5V			1	mA
	Input Voltage						
I _{IH}	High Level Input Current	V _{CC} = Max, V _I	= 2.4V			40	μΑ
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I	= 0.4V			-1.6	mA
Icc	Supply Current	V _{CC} = Max	DM54		43	62	mA
		(Note 3)	DM74		43	70	

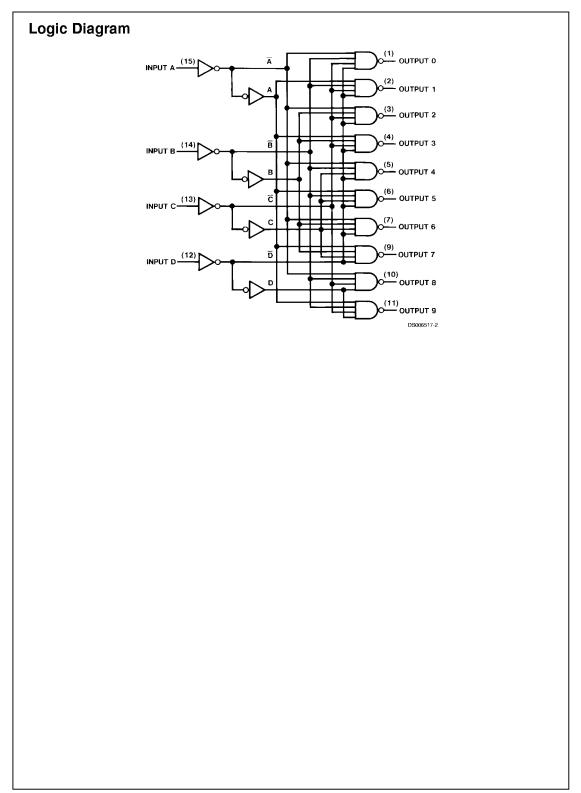
Switching Characteristics

at V_{CC} = 5V and T_A = 25°C (See Section 1 for Test Waveforms and Output Load)

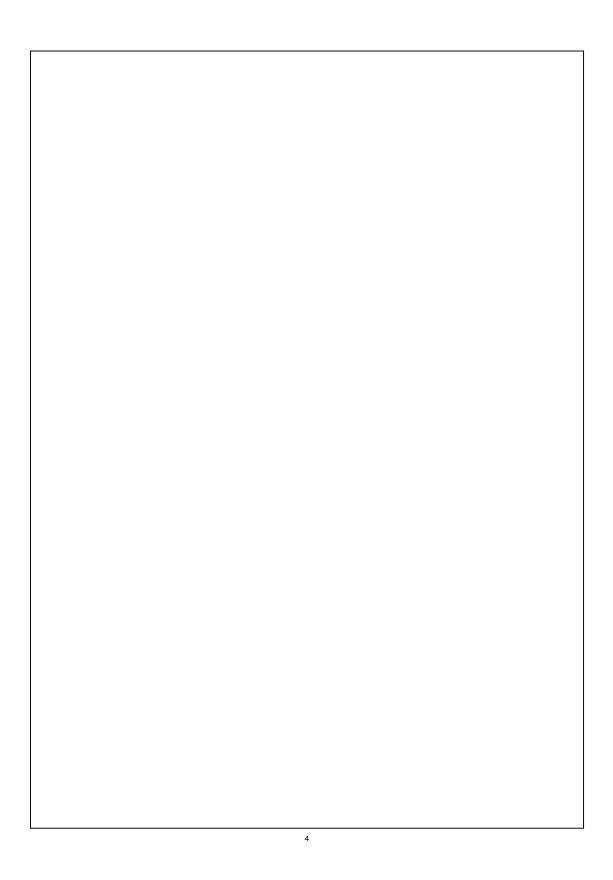
Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	C _L = 15 pF		49.5	ns
	Low to High Level Output	$R_L = 100\Omega$			
t _{PHL}	Propagation Delay Time			49.5	ns
	High to Low Level Output				

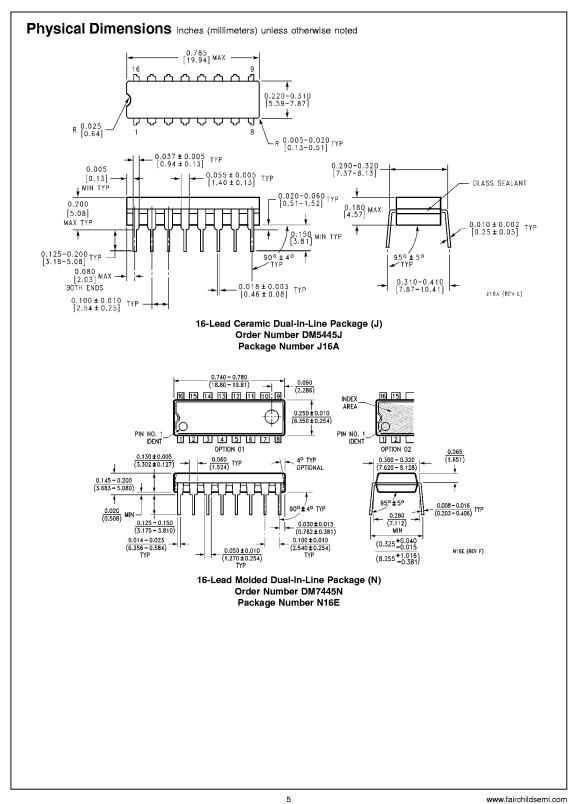
Note 2: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 3: I_{CC} is measured with all inputs grounded and all outputs open.



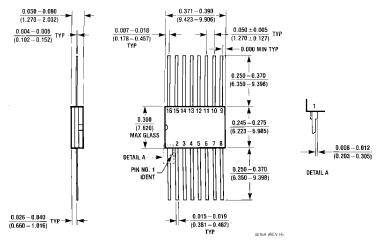
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Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Ceramic Flat Package (W) Order Number DM5445W Package Number W16A

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