## Features:

- Near-Zero propagation delay
- $25 \Omega$ series resistor termination
- $5 \Omega$ switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power ( $0.2 \mu \mathrm{~A}$ typical)
- Ideally suited for notebook applications
- Pin compatible with QS32X245
- Industrial operating temperature: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
- Packaging ( Pb -free \& Green available):
- 40-pin 150-mil wide plastic BQSOP (B)


## Block Diagram



## Truth Table ${ }^{(1)}$

| Function | $\overline{\text { BE }} \mathbf{n}$ | A0-15 |
| :--- | :---: | :---: |
| Disconnect | H | Hi-Z |
| Connect | L | B0-15 |

Note:

1. $\mathrm{H}=$ High Voltage Level, $\mathrm{L}=$ Low Voltage Level,
$\mathrm{Hi}-\mathrm{Z}=$ High Impedance

## Description:

Pericom Semiconductor's PI5C32X2245 is a 16-bit, 2-port bus switch. Two enable signals ( $\overline{\mathrm{BEn}}$ ) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise. The device has a built-in $25 \Omega$ resistor to reduce noise resulting from reflections, thus eliminating the need for an external terminating resistor.

## Pin Configuration



Pin Description

| Pin Name | I/O | Description |
| :---: | :---: | :--- |
| $\overline{\mathrm{BE}} \mathrm{n}$ | I | Bus Enable Input (Active LOW) |
| A0-A15 | I/O | Bus A |
| B0-B15 | I/O | Bus B |

Maximum Ratings
(Above which the useful life may be impaired. For user guidelines, not tested.)


## Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $\mathrm{TA}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{VCC}=5 \mathrm{~V} \pm 10 \%$ )

| Parameters | Description | Test Conditions ${ }^{(1)}$ | Min. | Typ ${ }^{(2)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IH }}$ | Input HIGH Voltage | Guaranteed Logic HIGH Level | 2.0 |  |  | V |
| $\mathrm{V}_{\text {IL }}$ | Input LOW Voltage | Guaranteed Logic LOW Level | -0.5 |  | 0.8 |  |
| $\mathrm{I}_{\mathrm{IH}}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {CC }}$ |  |  | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {IL }}$ | Input LOW Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{GND}$ |  |  | $\pm 1$ |  |
| Iozh | High Impedance Output Current | $0 \leq \mathrm{A}, \mathrm{B} \leq \mathrm{V}_{\mathrm{CC}}$ |  |  | $\pm 1$ |  |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{I}_{\text {IN }}=-18 \mathrm{~mA}$ |  | -0.7 | -1.2 | V |
| $\mathrm{I}_{\text {OS }}$ | Short Circuit Current ${ }^{(3)}$ | $\mathrm{A}(\mathrm{B})=0 \mathrm{~V}, \mathrm{~B}(\mathrm{~A})=\mathrm{V}_{\mathrm{CC}}$ | 100 |  |  | mA |
| $\mathrm{V}_{\mathrm{H}}$ | Input Hysteresis at Control Pins |  |  | 150 |  | mV |
| $\mathrm{R}_{\mathrm{OH}}$ | Switch On-Resistance ${ }^{(4)}$ | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{V}_{\text {IN }}=0.0 \mathrm{~V}, \mathrm{ION}=48 \mathrm{~mA}$ |  | 28 | 40 | $\Omega$ |
|  |  | $\mathrm{V}_{\text {CC }}=$ Min., $\mathrm{V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{I}$ IN $=15 \mathrm{~mA}$ |  | 35 | 48 |  |

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{Vcc}=5.0 \mathrm{~V}, \mathrm{TA}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between $A$ and $B$ pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two $(A, B)$ pins.

Capacitance ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{f}=1 \mathrm{MHz}$ )

| Parameters $^{(1)}$ | Description | Test Conditions | Typ | Units |
| :---: | :--- | :---: | :---: | :---: |
| Cin | Input Capacitance |  | 6 |  |
| CofF | A/B Capacitance, Switch Off | VIN $=0 \mathrm{~V}$ | pF |  |
| Con | A/B Capacitance, Switch On |  | 6 |  |

## Notes:

1. This parameter is determined by device characterization but is not production tested.

## Power Supply Characteristics

| Parameters | Description | Test Conditions ${ }^{(1)}$ | Min. | Typ ${ }^{(2)}$ | Max. | Units |
| :---: | :--- | :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{CC}}$ | $\begin{array}{l}\text { Quiescent Power } \\ \text { Supply Current }\end{array}$ | $\mathrm{V}_{\mathrm{CC}}=$ Max. | $\mathrm{V}_{\mathrm{IN}}=\mathrm{GND}$ or $\mathrm{V}_{\mathrm{CC}}$ |  |  |  |$)$

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $\mathrm{Vcc}=5.0 \mathrm{~V},+25^{\circ} \mathrm{C}$ ambient.
3. Per TTL driven input $(\mathrm{VIN}=3.4 \mathrm{~V}$, control inputs only $)$; A and B pins do not contribute to Icc.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

## Switching Characteristics over Operating Range

| Parameters | Description | Conditions | Com. |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Max. |  |
| tpLH tpHL | Propagation Delay ${ }^{(1,2)}$ Ax to Bx, Bx to Ax | $\begin{aligned} \mathrm{C}_{\mathrm{L}} & =50 \mathrm{pF} \\ \mathrm{R}_{\mathrm{L}} & =500 \Omega \end{aligned}$ |  | 1.25 | ns |
| $\begin{aligned} & \mathrm{t}_{\mathrm{PZH}} \\ & \mathrm{t}_{\mathrm{PZL}} \end{aligned}$ | Bus Enable Time $\overline{\mathrm{BEx}}$ to Ax or Bx |  | 1.5 | 7.5 |  |
| $\begin{aligned} & \text { tpHZ }^{\text {tel }} \end{aligned}$ | Bus Disable Time $\overline{\mathrm{BE}} \mathrm{x}$ to Ax or Bx |  | 1.5 | 5.5 |  |

## Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## Packaging Mechanical: 40-pin BQSOP (B)



## Ordering Information

| Ordering Code | Package Code | Package Description |
| :--- | :---: | :--- |
| PI5C32X2245B | B | 40 -pin BQSOP |
| PI5C32X2245BE | B | Pb-free \& Green, 40-pin BQSOP |

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- $\mathrm{E}=\mathrm{Pb}$-free \& Green
- Adding an X suffix $=$ Tape/Reel

