

August 1986 Revised April 2000

# DM74S153

# **Dual 1-of-4 Line Data Selector/Multiplexer**

### **General Description**

Each of these data selectors/multiplexers contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR-invert gates. Separate strobe inputs are provided for each of the two four-line sections.

#### **Features**

- Permits multiplexing from N lines to 1 line
- Performs parallel-to-serial conversion
- Strobe (enable) line provided for cascading (N lines to
- High fan-out, low-impedance, totem-pole outputs
- Typical average propagation delay times

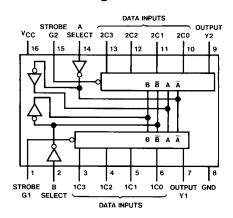
From data 6 ns From strobe 9.5 ns From select 12 ns

■ Typical power dissipation 225 mW

### **Ordering Code:**

Order Number	Package Number	Package Description
DM74S153N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

### **Connection Diagram**



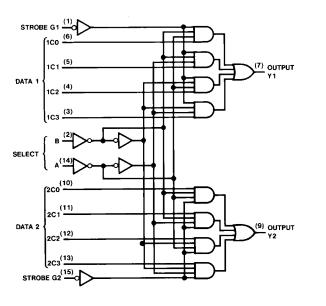
# **Function Table**

	Select Inputs		Data Inputs		Strobe	Output	
В	Α	C0	C1	C2	C3	G	Y
X	Х	Χ	Χ	Χ	Χ	Н	L
L	L	L	Χ	Χ	Χ	L	L
L	L	Н	Х	Х	Х	L	Н
L	Н	Χ	L	Х	Χ	L	L
L	Н	Х	Н	Х	Х	L	Н
Н	L	Х	Х	L	Х	L	L
Н	L	Х	Х	Н	Х	L	Н
Н	Н	Χ	Χ	Х	L	L	L
Н	Н	Х	Х	Х	Н	L	Н

Select inputs A and B are common to both sections. H = HIGH Level

L = LOW Level X = Don't Care

# Logic Diagram



www.fairchildsemi.com

# **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V Input Voltage 5.5V Operating Free Air Temperature Range  $0^{\circ}\text{C to } +70^{\circ}\text{C}$  Storage Temperature Range  $-65^{\circ}\text{C to } +150^{\circ}\text{C}$ 

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-1	mA
I <sub>OL</sub>	LOW Level Output Current			20	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

### **Electrical Characteristics**

over recommended operating free air temperature (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.2	V
V <sub>OH</sub>	HIGH Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max, V_{IH} = Min$	2.7	3.4		V
V <sub>OL</sub>	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.5	V
I <sub>I</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			50	μА
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.5V$			-2	mA
Ios	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	-40		-100	mA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max (Note 4)		45	70	mA

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

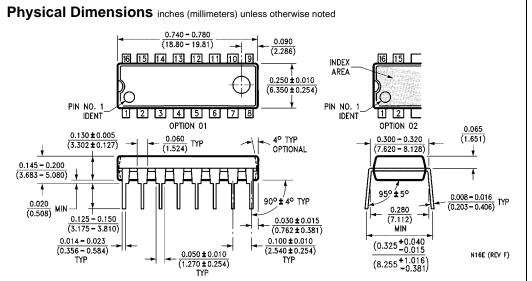
Note 4:  $\ensuremath{\text{I}_{\text{CC}}}$  is measured with all outputs OPEN and all inputs grounded.

### **Switching Characteristics**

at  $V_{CC}=5V$  and  $T_A=25^{\circ}C$ 

Symbol	Parameter	From (Input) To (Output)	$R_L = 280\Omega$				
			C <sub>L</sub> = 15 pF		C <sub>L</sub> = 50 pF		Units
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time	Data to Y		9		12	ns
	LOW-to-HIGH Level Output	Data to 1					110
t <sub>PHL</sub>	Propagation Delay Time	Data to Y		9		12	ns
	HIGH-to-LOW Level Output	Data to 1					
t <sub>PLH</sub>	Propagation Delay Time	Select to Y		18		20	ns
	LOW-to-HIGH Level Output					20	
t <sub>PHL</sub>	Propagation Delay Time	Select to Y		18		21	ns
	HIGH-to-LOW Level Output					21	
t <sub>PLH</sub>	Propagation Delay Time	Strobe to Y		15		18	ns
	LOW-to-HIGH Level Output			15			
t <sub>PHL</sub>	Propagation Delay Time	Strobe to Y		42.5		17	ns
	HIGH-to-LOW Level Output	Strope to Y		13.5			

www.fairchildsemi.com



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

Fairchild does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and Fairchild reserves the right at any time without notice to change said circuitry and specifications.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com

www.fairchildsemi.com