FAIRCHILD

SEMICONDUCTOR

DM74LS257B **3-STATE Quad 2-Data Selectors/Multiplexers**

General Description

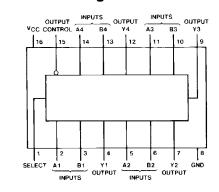
These Schottky-clamped high-performance multiplexers feature 3-STATE outputs that can interface directly with data lines of bus-organized systems. With all but one of the common outputs disabled (at a high impedance state), the low impedance of the single enabled output will drive the bus line to a HIGH or LOW logic level. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the output enable circuitry is designed such that the output disable times are shorter than the output enable times.

This 3-STATE output feature means that n-bit (paralleled) data selectors with up to 258 sources can be implemented

Ordering Code:

Order Number	Package Number	Package Description
DM74LS257BM	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74LS257BN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Devices also available	in Tape and Reel. Specify	by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

	Output Y			
Output Control	Select	Α	в	LS257
Н	Х	Х	Х	Z
L	L	L	х	L
L	L	н	х	н
L	н	х	L	L
L	н	Х	Н	н
		't Care Impedanc	e (off)	1

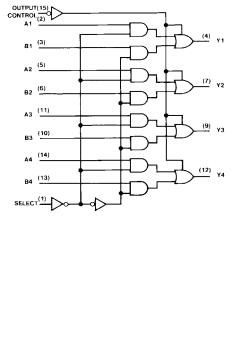
© 1999 Fairchild Semiconductor Corporation DS006417 Logic Diagram

Features

performance

high-performance systems

■ Typical power dissipation: 50 mW



June 1989

for data buses. It also permits the use of standard TTL reg-

■ 3-STATE versions LS157 and LS158 with same pinouts

Schottky-clamped for significant improvement in A-C

isters for data retention throughout the system.

Provides bus interface from multiple sources in

Average propagation delay from data input 12 ns

Revised November 1999

www.fairchildsemi.com

Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. the device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The Recommended Operating Conditions table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-2.6	mA
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

DC Electrical Characteristics

Symbol	Parameter	Conditions		Min	Typ (Note 2)	Max	Units	
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	V	
V _{он}	HIGH Level Output Voltage	V _{IL} = Max, V _{IH} = Min		2.4	3.1		V	
V _{OL}	LOW Level Output	V _{IL} = Max, V _{IH} = Min			0.35	0.5	v	
	Voltage	I _{OL} = 12 mA, V _{CC} = Min			0.25	0.4	v	
1	Input Current @ Max	V _{CC} = Max,	Select			0.2	mA	
	Input Voltage	V ₁ = 7V	Other			0.1	MA	
н	HIGH Level Input	V _{CC} = Max,	Select			40	μA	
	Current	$V_{I} = 2.7V$	Other			20		
IIL	LOW Level Input	V _{CC} = Max,	Select			-0.8	mA	
	Current	$V_{I} = 0.4V$	Other			-0.4		
I _{OZH}	OFF-State Output Current with	$V_{CC} = Max, V_O = 2.7V$ $V_{IH} = Min, V_{IL} = Max$				20		
	HIGH Level Output Voltage Applied					20	μA	
OZL	OFF-State Output Current with	$V_{CC} = Max, V_O = 0.4V$ $V_{IH} = Min, V_{IL} = Max$				20		
	LOW Level Output Voltage Applied					-20	μA	
os	Short Circuit Output Current	V _{CC} = Max (Note 3)		-20		-100	mA	
ССН	Supply Current with Outputs HIGH	V _{CC} = Max (Note 4)			5.9	10	mA	
CCL	Supply Current with Outputs LOW	V _{CC} = Max (Note 4)			9.2	16	mA	
ccz	Supply Current with Outputs Disabled	V _{CC} = Max (Note 4)			12	19	mA	

Note 2: All typicals are at V_{CC} = 5V, T_A = $\overline{25^{\circ}C}$.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 4: I_{CC} is measured with all outputs open and all possible inputs grounded, while achieving the stated output conditions.

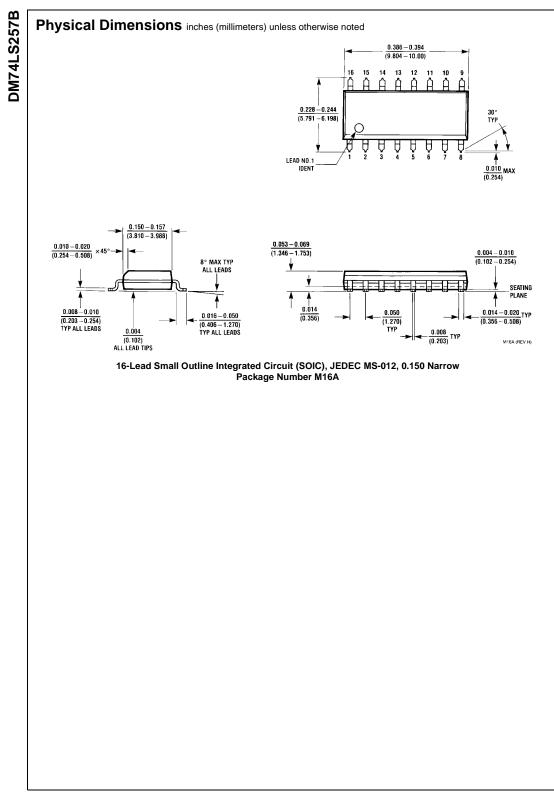
www.fairchildsemi.com

	Parameter			RL = 6	667 Ω		
Symbol		From (Input) To (Output)	C _L = 45 pF		C _L = 150 pF		Units
			Min	Max	Min	Max	1
t _{PLH}	Propagation Delay Time	Data to Output	18		27	ns	
	LOW-to-HIGH Level Output	Data to Output		10		21	115
t _{PHL}	Propagation Delay Time	Data ta Outaut	18	10		27	
	HIGH-to-LOW Level Output	Data to Output		27	21	ns	
t _{PLH}	Propagation Delay Time	Select to Output	28	35	25	ns	
	LOW-to-HIGH Level Output	Select to Output					
t _{PHL}	Propagation Delay Time	Select to Output	35		42	ns	
	HIGH-to-LOW Level Output	Select to Output			42		
t _{PZH}	Output Enable Time	Output Control to V	15	15		27	
	to HIGH Level Output	Output Control to Y		4	21	ns	
t _{PZL}	Output Enable Time	Output Control to Y	28	29		38	
	to LOW Level Output			20	30	ns	
t _{PHZ}	Output Disable Time from	Output Control to Y	28	29			ns
	HIGH Level Output (Note 5)					115	
t _{PLZ}	Output Disable Time from	Output Control to Y		25			
	LOW Level Output (Note 5)		25	20			ns

Note 5: $C_L = 5 \text{ pF}$

www.fairchildsemi.com

DM74LS257B



www.fairchildsemi.com

4

