PI5C32160C

16-Bit to 32-Bit, DeMux PCI Hot-Plug Bus Switch with –1.5V Undershoot Protection

Features

- R_{ON} is 5Ω typical
- Pull-up on B1 and B2 ports
- Undershoot protection on A-port only: –1.5V
- Low Power: 70µA typical
- Industrial Operation Temperature: –40ºC to +85ºC
- Near-Zero propagation delay
- Switching speed: 5ns max.
- Channel on capacitance: 15pF max.
- V_{CC} Operating Range: +4.5V to +5.5V
- >2kV ESD protection (human body model)
- >100 MHz bandwidth (or clock rate) at 20pF load capacitance
- Packaging (Pb-free & Green available):
  – 56-pin TSSOP (A)

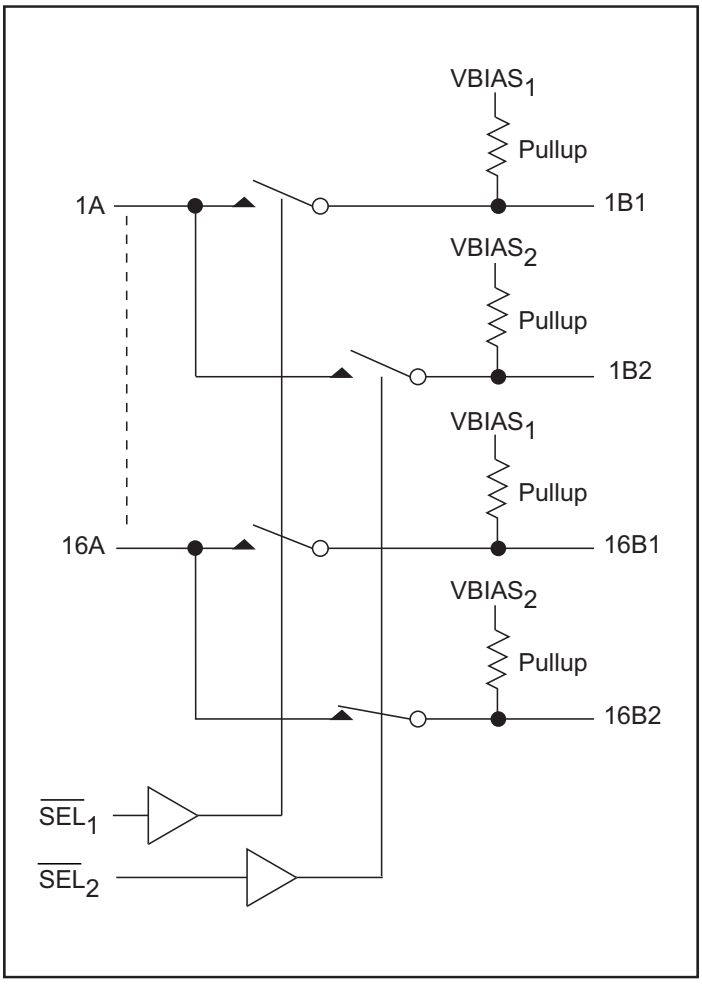
Description

Pericom Semiconductor's PI5C32160C is a 16 to 32-bit demultiplexer bus switch. Industry leading advantages include a propagation delay of 250ps, resulting from 5Ω channel resistance, and low I/O capacitance. A port demultiplexes to either 1B and 2B or to both. The switch is bidirectional.

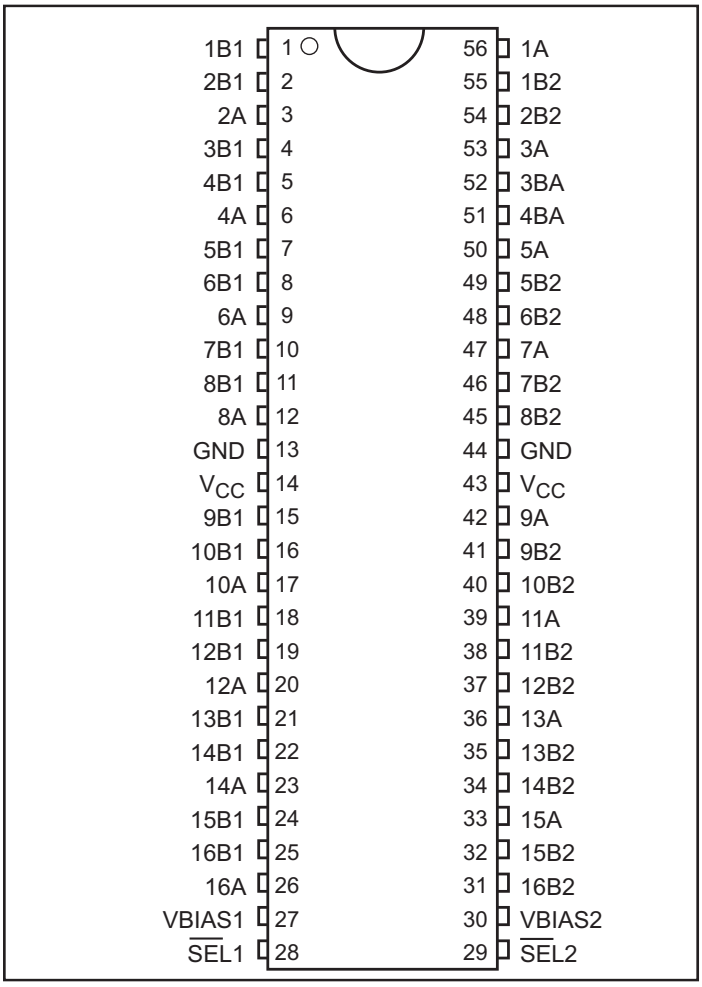
Application

- Provides PCI Hot-Plugging

Block Diagram



Pin Description



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V_{CC} Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±10%, V_{BIAS} = 1.3V to V_{CC})

Parameters	Description	Test Conditions[1]	Min.	Typ[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±1	
I_{OZH}	High Impedance Output Current	A= 0V or V_{CC} Max., $V_{BIAS}1$ = $V_{BIAS}2$ = V_{CC} Max.			±1	
I_{OZL}	Low Impedence Output Current	B = 0V, $V_{BIAS}1$ = $V_{BIAS}2$ = V_{CC} Max.	0.25		5	mA
		B = V_{CC} max., $V_{BIAS}1$ = $V_{BIAS}2$ = V_{CC} Max.	–1.0			µA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18mA		–0.7	–1.8	V
R_{ON}	Switch On-Resistance[3]	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA		5	8	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA		10	15	

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
3. Measured by the voltage drop between A and B pins at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A & B) pins.

Truth Table

Function	$\overline{SEL_1}$	$\overline{SEL_2}$
$_NA$ to $_NB_1$	L	H
$_NA$ to $_NB_2$	H	L
$_NA$ to $_NB_1$ and $_NB_2$	L	L
$_NB_1$, $_NB_2$ to V_{BIAS}	H	H

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[1]	Description	Test Conditions	Typ.	Max.	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	3.5		pF
C_{OFF}	A/B Capacitance, Switch Off		6.5		
C_{ON}	A/B Capacitance, Switch On		13.5		

Notes:
1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		70	200	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.4V[3] other pin=V_{CC} or GND			2.5	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., A and B Pins Open Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); A and B pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Test Conditions	PI5C32160C			Units
			Com.			
			Min.	Typ.	Max.	
t_{PLH} t_{PHL}	Propagation Delay[1,2] A to B	C_L = 50pF R_L = 500Ω		0.25		ns
t_{PZH} t_{PZL}	Bus Enable Time SEL TO A,B		1.3		5.0	
t_{PHZ} t_{PLZ}	Bus Disable Time SEL to A,B		0.5		5.0	

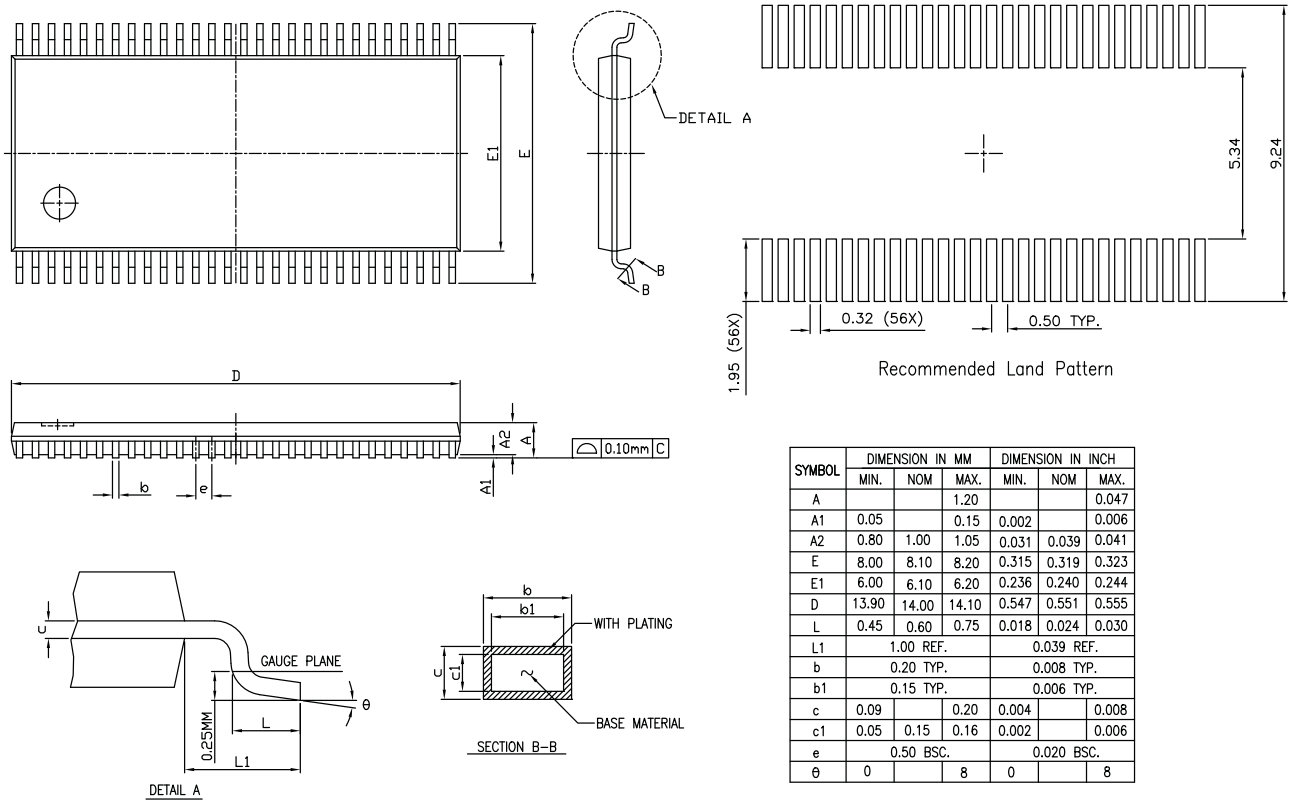
Notes:
1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Applications Information

Logic Inputs

The logic control inputs can be driven up to +5.5V regardless of the supply voltage. For example, given a +5.0V supply, IN may be driven low to 0V and high to 5.5V. Driving IN Rail-to-Rail® minimizes power consumption. Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} before applying Vbias and signals to the input/output pins.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd



SYMBOL	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM	MAX.	MIN.	NOM	MAX.
A			1.20			0.047
A1	0.05		0.15	0.002		0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
E	8.00	8.10	8.20	0.315	0.319	0.323
E1	6.00	6.10	6.20	0.236	0.240	0.244
D	13.90	14.00	14.10	0.547	0.551	0.555
L	0.45	0.60	0.75	0.018	0.024	0.030
L1	1.00 REF.			0.039 REF.		
b	0.20 TYP.			0.008 TYP.		
b1	0.15 TYP.			0.006 TYP.		
c	0.09		0.20	0.004		0.008
c1	0.05	0.15	0.16	0.002		0.006
e	0.50 BSC.			0.020 BSC.		
θ	0		8	0		8

Notes:
1. Controlling dimensions in millimeters.
2. Ref: JEDEC MO-153F/EE
3. Package Outline Exclusive of Mold Flash and Metal Burr

PERICOM Semiconductor Corporation

DATE: 09/11/06

DESCRIPTION: 56-pin, 240-mil wide TSSOP

PACKAGE CODE: A56

DOCUMENT CONTROL #: PD-1502 **REVISION: M**

06-0736

Note:
- For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

Ordering Information

Ordering Code	Package Code	Package Description
PI5C32160CAEX	A	Pb-free & Green, 56-pin 240-mil wide, TSSOP

Notes:
- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel