TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC153F,TC74VHC153FN,TC74VHC153FT,TC74VHC153FK

Dual 4-Channel Multiplexer

The TC74VHC153 is an advanced high speed CMOS DUAL 4-CHANNEL MULTIPLEXERs fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Each of these data (1C0-1C3, 2C0-2C3) is selected by the two address inputs A and B.

Separate strobe inputs ($1\overline{G}$, $\ 2\overline{G}$) are provided for each of the two four-line sections.

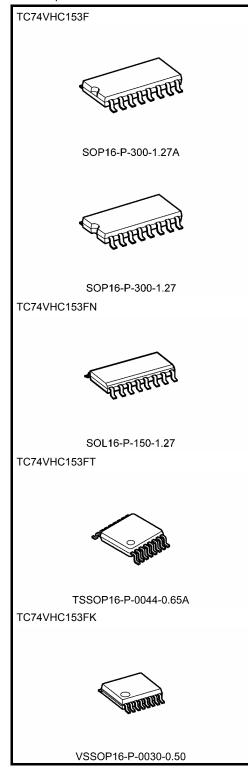
The strobe input (\overline{G}) can be used to inhibit the data output; the output is fixed in low level while the strobe input is held high.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: $t_{pd} = 5.0$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: $V_{CC (opr)} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74ALS153

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

 SOP16-P-300-1.27A
 : 0.18 g (typ.)

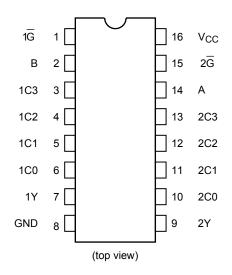
 SOP16-P-300-1.27
 : 0.18 g (typ.)

 SOL16-P-150-1.27
 : 0.13 g (typ.)

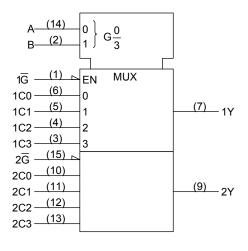
 TSSOP16-P-0044-0.65A
 : 0.06 g (typ.)

 VSSOP16-P-0030-0.50
 : 0.02 g (typ.)

Pin Assignment



IEC Logic Symbol

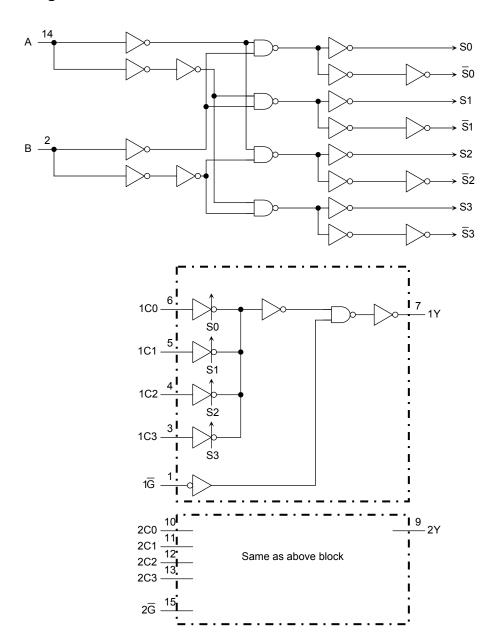


Truth Table

| Select Inputs | | | Data | Inputs | | Strobe | Output | |
|---------------|---|----|------|--------|----|--------|--------|--|
| В | Α | C0 | C1 | C2 | C3 | IG | Υ | |
| Х | Х | Х | Х | Х | Х | Н | L | |
| L | L | L | Х | Х | Х | L | L | |
| L | L | Н | Х | Х | Х | L | Н | |
| L | Н | Х | L | Х | Х | L | L | |
| L | Н | Х | Н | Х | Х | L | Н | |
| Н | L | Х | Х | L | Х | L | L | |
| Н | L | Х | Х | Н | Х | L | Н | |
| Н | Н | Х | Х | Х | L | L | L | |
| Н | Н | Х | Х | Х | Н | L | Н | |

X: Don't care

System Diagram



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Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Rating | Unit |
|------------------------------------|------------------|-------------------------------|------|
| Supply voltage range | V _{CC} | −0.5 to 7.0 | V |
| DC input voltage | V _{IN} | −0.5 to 7.0 | V |
| DC output voltage | Vout | -0.5 to V _{CC} + 0.5 | V |
| Input diode current | lıK | -20 | mA |
| Output diode current | lok | ±20 | mA |
| DC output current | I _{OUT} | ±25 | mA |
| DC V _{CC} /ground current | Icc | ±50 | mA |
| Power dissipation | PD | 180 | mW |
| Storage temperature | T _{stg} | -65 to 150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Operating Range (Note)

| Characteristics | Symbol | Rating | Unit | |
|--------------------------|------------------|--|------|--|
| Supply voltage | V _{CC} | 2.0 to 5.5 | V | |
| Input voltage | V _{IN} | 0 to 5.5 | V | |
| Output voltage | V _{OUT} | 0 to V _{CC} | V | |
| Operating temperature | T _{opr} | -40 to 85 | °C | |
| Input rise and fall time | dt/dv | 0 to 100 (V _{CC} = 3.3 ± 0.3 V) | ns/V | |
| input rise and rail unie | ui/uv | 0 to 20 (V _{CC} = 5 \pm 0.5 V) | | |

Note : The operating range must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.



Electrical Characteristics

DC Characteristics

| Characteristics | Symbol | Test Condition $V_{CC}\left(V\right)$ | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit | |
|---------------------------|-----------------|---|--------------------------|---------------------|-----------------------|------|-----------------------|-----------------------|-----------------------|----|
| | , | | | V _{CC} (V) | Min | Тур. | Max | Min | Max | |
| High-level input | | - | | 2.0 | 1.50 | _ | _ | 1.50 | _ | |
| voltage | V _{IH} | | | 3.0 to 5.5 | V _{CC} × 0.7 | _ | _ | V _{CC} × 0.7 | _ | V |
| Low-level input | V _{IL} | _ | | 2.0 | | _ | 0.50 | _ | 0.50 | ٧ |
| voltage | | | | 3.0 to 5.5 | _ | _ | V _{CC} × 0.3 | _ | V _{CC} × 0.3 | |
| | V _{ОН} | | | 2.0 | 1.9 | 2.0 | _ | 1.9 | _ | |
| | | V _{IN} = V _{IH} or V _{IL} | $I_{OH} = -50 \mu A$ | 3.0 | 2.9 | 3.0 | _ | 2.9 | _ | |
| High-level output voltage | | | | 4.5 | 4.4 | 4.5 | _ | 4.4 | _ | V |
| | | | $I_{OH} = -4 \text{ mA}$ | 3.0 | 2.58 | _ | _ | 2.48 | _ | |
| | | | $I_{OH} = -8 \text{ mA}$ | 4.5 | 3.94 | _ | _ | 3.80 | _ | |
| | VoL | V _{IN} = V _{IH} or V _{IL} | | 2.0 | _ | 0.0 | 0.1 | _ | 0.1 | |
| | | | $I_{OL} = 50 \mu A$ | 3.0 | _ | 0.0 | 0.1 | _ | 0.1 | V |
| Low-level output voltage | | | | 4.5 | _ | 0.0 | 0.1 | _ | 0.1 | |
| | | | I _{OL} = 4 mA | 3.0 | | | 0.36 | _ | 0.44 | |
| | | | I _{OL} = 8 mA | 4.5 | _ | | 0.36 | | 0.44 | |
| Input leakage current | I _{IN} | V _{IN} = 5.5 V or GND | | 0 to 5.5 | | | ±0.1 | l | ±1.0 | μА |
| Quiescent supply current | I _{CC} | V _{IN} = V _{CC} or GND | | 5.5 | _ | _ | 4.0 | | 40.0 | μА |

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

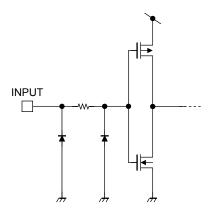
| Characteristics | Symbol | Test Condition | | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit |
|-------------------------------|--|----------------|---------------------|---------------------|-----------|------|------|---------------------|------|----------|
| | -, | | V _{CC} (V) | C _L (pF) | Min | Тур. | Max | Min | Max | |
| | t _{pLH} | _ | 3.3 ± 0.3 | 15 | _ | 7.7 | 11.9 | 1.0 | 14.0 | ns ns |
| Propagation delay time | | | | 50 | | 10.2 | 15.4 | 1.0 | 17.5 | |
| (Cn-Y) | | | 5.0 ± 0.5 | 15 | | 5.0 | 7.7 | 1.0 | 9.0 | |
| | | | | 50 | | 6.5 | 9.7 | 1.0 | 11.0 | |
| | t _р Lн t _р нL | _ | 3.3 ± 0.3 | 15 | | 10.8 | 16.7 | 1.0 | 19.5 | |
| Propagation delay time | | | | 50 | _ | 13.3 | 20.2 | 1.0 | 23.0 | |
| (A, B-Y) | | | 5.0 ± 0.5 | 15 | _ | 6.8 | 9.9 | 1.0 | 11.5 | |
| | | | | 50 | _ | 8.3 | 11.9 | 1.0 | 13.5 | |
| | t _р Lн t _р нL | _ | 3.3 ± 0.3 | 15 | _ | 6.3 | 10.1 | 1.0 | 12.0 | |
| Propagation delay time | | | | 50 | | 8.8 | 13.6 | 1.0 | 15.5 | |
| (G -Y) | | | 5.0 ± 0.5 | 15 | _ | 4.4 | 6.4 | 1.0 | 7.5 | |
| | | | | 50 | _ | 5.9 | 8.4 | 1.0 | 9.5 | |
| Input capacitance | C _{IN} | | _ | | | 4 | 10 | _ | 10 | pF |
| Power dissipation capacitance | C _{PD} | | | (Note) | | 20 | _ | _ | _ | pF |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

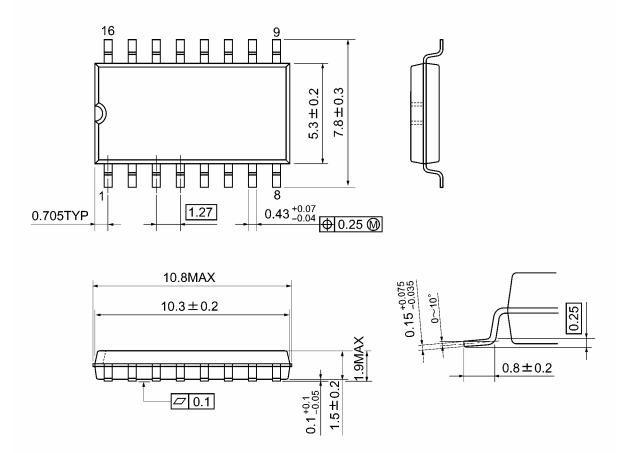
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Input Equivalent Circuit

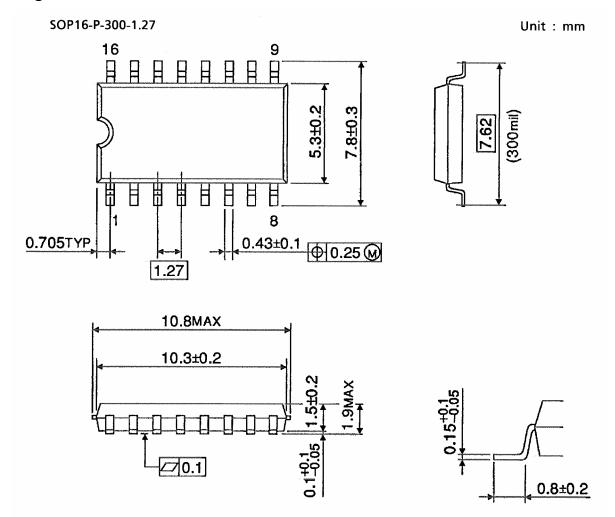


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SOP16-P-300-1.27A Unit: mm

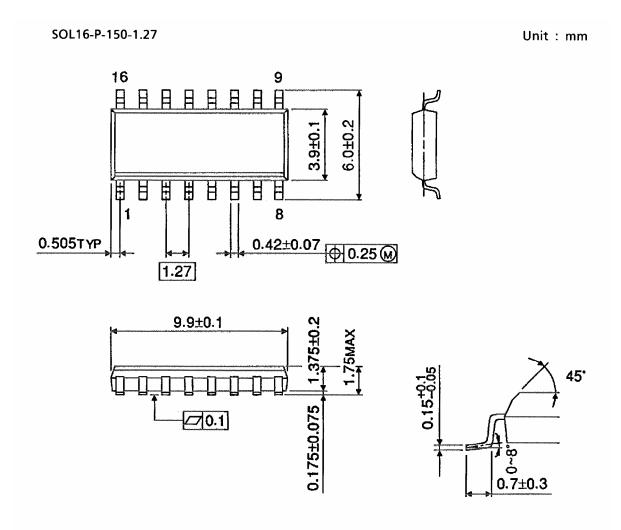


Weight: 0.18 g (typ.)



Weight: 0.18 g (typ.)

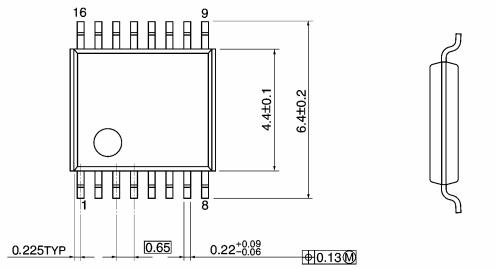
Package Dimensions (Note)

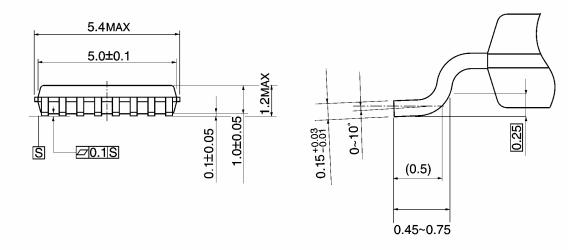


Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

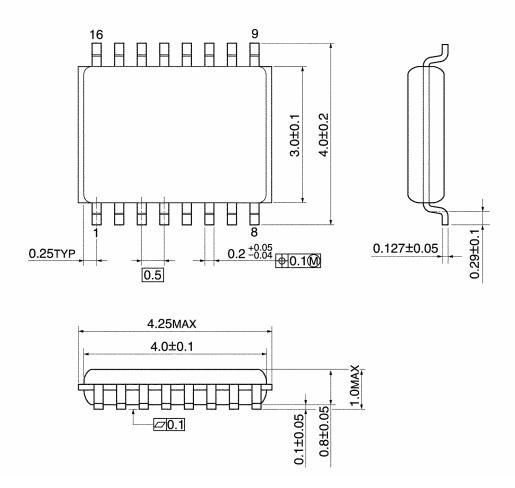
TSSOP16-P-0044-0.65A Unit: mm





Weight: 0.06 g (typ.)

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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