TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

### TC74VHC157F,TC74VHC157FN,TC74VHC157FT,TC74VHC157FK

#### **Quad 2-Channel Multiplexer**

The TC74VHC157 is an advanced high speed CMOS QUAD 2-CHANNEL MULTIPLEXER fabricated with silicon gate  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

It consists of four 2-input digital multiplexers with common select and strobe inputs.

When the STROBE input is held "H" level, selection of data is inhibited and all the outputs become "L" level.

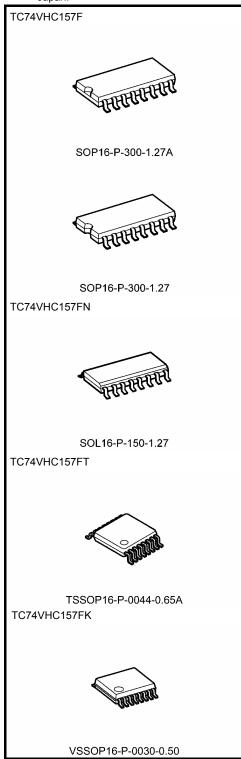
The SELECT decoding determines whether the A or B inputs get routed to their corresponding Y outputs.

An Input protection circuit ensures that 0 to 5.5~V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5~V to 3~V systems and on two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

#### **Features**

- High speed:  $t_{pd} = 4.1 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25 \text{°C}$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- · Power down protection is provided on all inputs.
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC \text{ (opr)}} = 2 \text{ V to } 5.5 \text{ V}$
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS157

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

 SOP16-P-300-1.27A
 : 0.18 g (typ.)

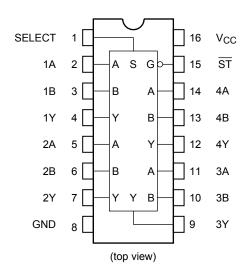
 SOP16-P-300-1.27
 : 0.18 g (typ.)

 SOL16-P-150-1.27
 : 0.13 g (typ.)

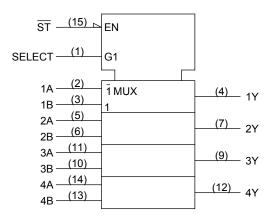
 TSSOP16-P-0044-0.65A
 : 0.06 g (typ.)

 VSSOP16-P-0030-0.50
 : 0.02 g (typ.)

# **Pin Assignment**



# **IEC Logic Symbol**



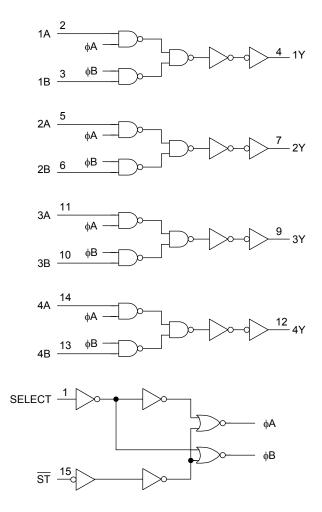
**Truth Table** 

	Inputs	Output		
ST	SELECT	Α	В	Output
Н	Х	Х	Х	L
L	L	L	Х	L
L	L	Н	Х	Н
L	Н	Х	L	L
L	Н	Х	Н	Н

X: Don't care

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### **System Diagram**



### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	$V_{CC}$	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

# **Recommended Operating Conditions (Note)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	−40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V <sub>CC</sub> = 3.3 ± 0.3 V)	ns/V	
input rise and fail time	uvuv	0 to 20 ( $V_{CC} = 5 \pm 0.5 \text{ V}$ )		

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition $V_{CC}\left(V\right)$		Ta = 25°C			Ta = −40 to 85°C		Unit	
	- J			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
High-level input		_		2.0	1.50	_	_	1.50	_	V
voltage	V <sub>IH</sub>			3.0 to 5.5	V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_	
Low-level input				2.0	_	_	0.50	_	0.50	
voltage	Low-level input voltage		_	3.0 to 5.5	_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	V
	V <sub>ОН</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	1.9	2.0	_	1.9	_	
			I <sub>OH</sub> = -50 μA	3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	-	V
			I <sub>OH</sub> = −4 mA	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	1	_	3.80	-	
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	_	0.0	0.1	_	0.1	
			I <sub>OL</sub> = 50 μA	3.0	_	0.0	0.1	_	0.1	
Low-level output voltage				4.5	_	0.0	0.1	_	0.1	V
Ü			I <sub>OL</sub> = 4 mA	3.0	_	_	0.36	_	0.44	
			I <sub>OL</sub> = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 or GND		0 to 5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	4.0	_	40.0	μΑ



### AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	- Cy20.		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	
		_	3.3 ± 0.3	15	_	6.2	9.7	1.0	11.5	ns
Propagation delay time	$t_{pLH}$			50	_	8.7	13.2	1.0	15.0	
(A, B-Y)	$t_{pHL}$		5.0 ± 0.5	15	_	4.1	6.4	1.0	7.5	
			3.0 ± 0.3	50	_	5.6	8.4	1.0	9.5	
		_	3.3 ± 0.3	15	_	8.4	13.2	1.0	15.5	- ns
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>			50	_	10.9	16.7	1.0	19.0	
(SELECT-Y)			5.0 ± 0.5	15	_	5.3	8.1	1.0	9.5	
,				50	_	6.8	10.1	1.0	11.5	
	<sup>‡</sup> рLН <sup>‡</sup> рНL	1	3.3 ± 0.3	15	_	8.7	13.6	1.0	16.0	
Propagation delay time				50	_	11.2	17.1	1.0	19.5	ns
( <del>ST</del> -Y)			5.0 ± 0.5	15	_	5.6	8.6	1.0	10.0	115
			3.0 ± 0.3	50	_	7.1	10.6	1.0	12.0	
Input capacitance	C <sub>IN</sub>		_		_	4	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>			(Note)	_	20	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

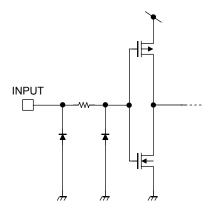
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 (per bit)$ 

### Noise Characteristics (input: $t_r = t_f = 3$ ns)

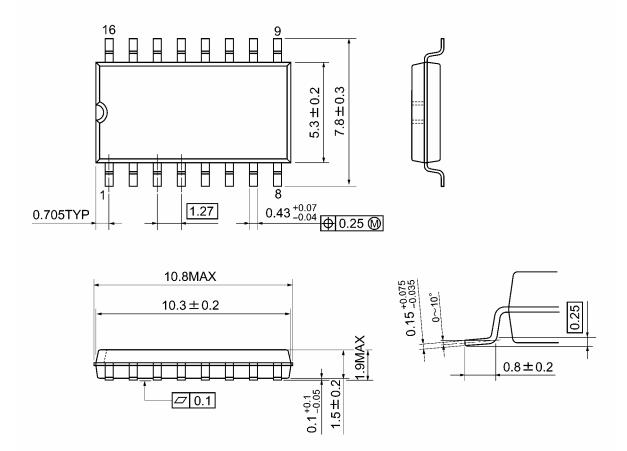
Characteristics	Symbol	Test Condition	_	Ta = 25°C		Unit
Characteristics	Symbol		V <sub>CC</sub> (V)	Тур.	Limit	Offic
Quiet output maximum dynamic $V_{OL}$	V <sub>OLP</sub>	C <sub>L</sub> = 50 pF	5.0	0.3	0.8	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	C <sub>L</sub> = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic input voltage	V <sub>IHD</sub>	C <sub>L</sub> = 50 pF	5.0	_	3.5	٧
Maximum low level dynamic input voltage	V <sub>ILD</sub>	C <sub>L</sub> = 50 pF	5.0	ı	1.5	<b>V</b>

# **Input Equivalent Circuit**

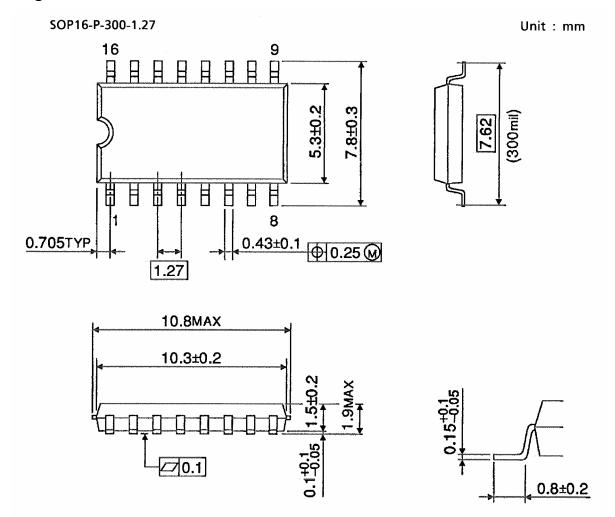


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SOP16-P-300-1.27A Unit: mm



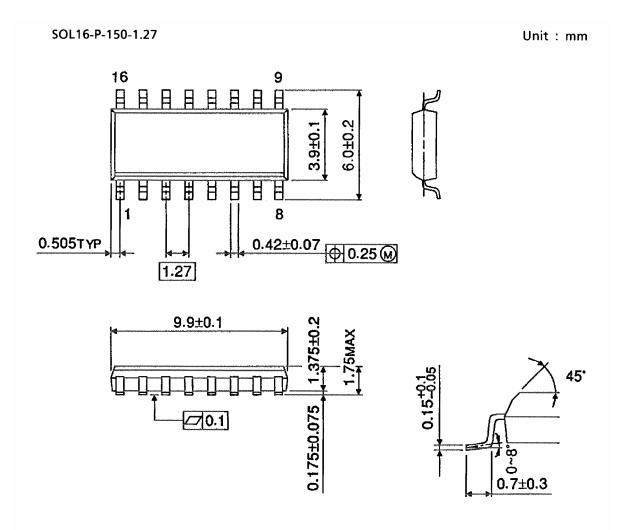
Weight: 0.18 g (typ.)



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Weight: 0.18 g (typ.)

# **Package Dimensions (Note)**

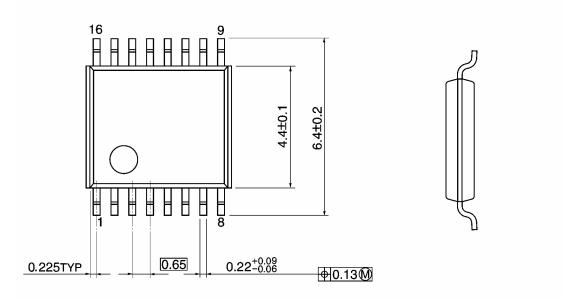


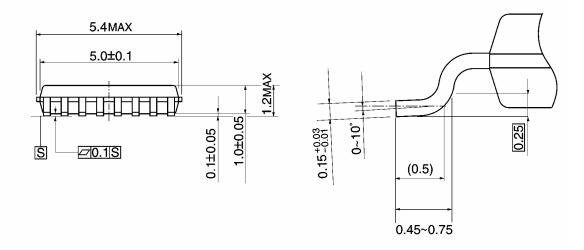
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Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

TSSOP16-P-0044-0.65A Unit: mm

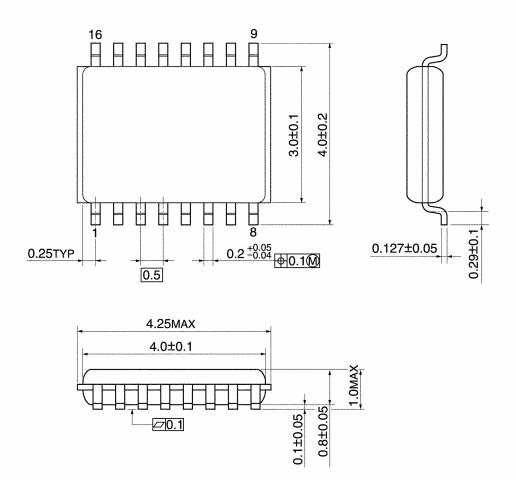




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Weight: 0.06 g (typ.)

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP16-P-300-1.27A SOL16-P-150-1.27 TSSOP16-P-0044-0.65A VSSOP16-P-0030-0.50

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