TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX138F,TC74LCX138FN,TC74LCX138FT,TC74LCX138FK

Low-Voltage 3-to-8 Line Decoder with 5-V Tolerant Inputs and Outputs

The TC74LCX138 is a high-performance CMOS 3-to-8 decoder. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) V_{CC} applications, but it could be used to interface to 5-V supply environment for inputs.

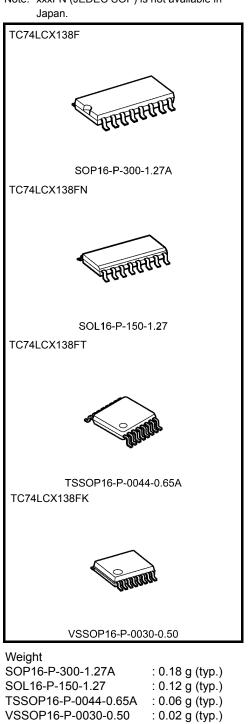
When the device is enabled, 3 binary select inputs (A, B and C) determine which one of the outputs $(\overline{Y}0 \cdot \overline{Y}7)$ will go low. When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

G1, $\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

All inputs are equipped with protection circuits against static discharge.

Features

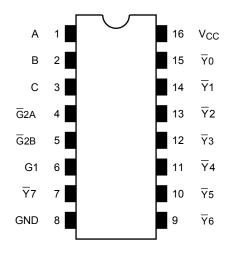
- Low-voltage operation: $V_{CC} = 1.65$ to 3.6 V
- High-speed operation: $t_{pd} = 6.0 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\min) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: $>\pm500$ mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 138 type



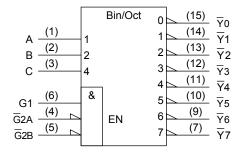
Note: The Electrical Characteristics of V_{CC}=1.8 \pm 0.15V is only applicable for products which manufactured from January 2009 onward.

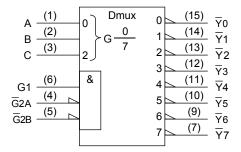
Note: xxxFN (JEDEC SOP) is not available in

Pin Assignment (top view)



IEC Logic Symbol





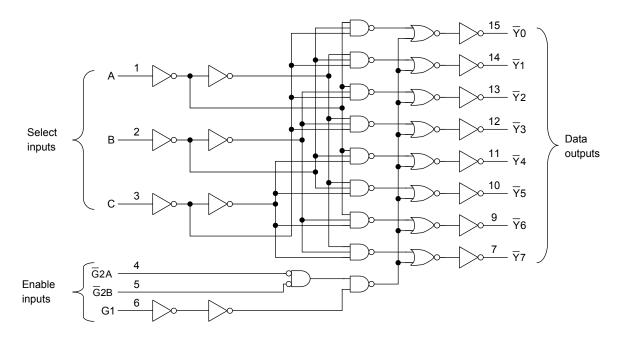
Truth Table

		Inp	uts			Outputs								
	Enable			Select		- Y0	T ₁	T ₂	¥3	¥4	¥5	¥6	T7	Selected Output
G1	G2A	G2B	С	В	А	10	TI	12	13	14	15	10	17	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Υ 0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	<u>¥</u> 1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	¥2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	н	Н	Н	<u>¥</u> 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	н	Н	Н	$\overline{Y}4$
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	¥5
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Υ 6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	¥7

X: Don't care

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System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	–0.5 to 7.0	V	
DC input voltage	V _{IN}	–0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)	V	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)		
Input diode current	I _{IK}	-50	mA	
Output diode current	I _{OK}	±50 (Note 4)	mA	
DC output current	IOUT	±50	mA	
Power dissipation	PD	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	–65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	1.65 to 3.6	V	
Power supply voltage	VCC	1.5 to 3.6 (Note 2)		
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 3)	V	
Output voltage	VOUT	0 to V_{CC} (Note 4)	v	
Output current	IOH/IOL	±24 (Note 5)	mA	
Output current	'OH/'OL	±12 (Note 6)	IIIA	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

- Note 2: Data retention only
- Note 3: $V_{CC} = 0 V$
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0$ to 3.6 V
- Note 6: $V_{CC} = 2.7$ to 3.0 V
- Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Cor	ndition		Min	Max	Unit
		0,110,01		V _{CC} (V)		max		
				1.65 to 2.3	V _{CC} × 0.9			
Input voltage	H-level	VIH		2.3 to 2.7	1.7			
				2.7 to 3.6	2.0	_	V	
input voltage	-				1.65 to 2.3		V _{CC} × 0.1	v
	L-level	VIL			2.3 to 2.7		0.7	
				2.7 to 3.6		0.8		
				I _{OH} = −100 μA	1.65 to 3.6	V _{CC} -0.2		V
	H-level		$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -4 mA	1.65	1.05	_	
				I _{OH} = -8 mA	2.3	1.7	_	
		Vон		I _{OH} = -12 mA	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4		
				I _{OH} = -24 mA	3.0	2.2		
Output voltage				I _{OL} = 100 μA	1.65 to 3.6		0.2	
				I _{OL} = 4 mA	1.65		0.45	
				I _{OL} = 8 mA	2.3	_	0.7	
	L-level	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 12 mA	2.7	_	0.4	
				I _{OL} = 16 mA	3.0		0.4	
				I _{OL} = 24 mA	3.0		0.55	
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μA
Power-off leakage cur	Power-off leakage current		$V_{IN}/V_{OUT} = 5.5 V$		0	_	10.0	μA
Quiescent supply current			$V_{IN} = V_{CC}$ or GND	1.65 to 3.6		10.0		
Quicocent supply cull	Cint	Icc	$V_{\mbox{\rm IN}}=3.6$ to $5.5~\mbox{\rm V}$	1.65 to 3.6		±10.0	μA	
Increase in Icc per inp	ut	ΔI_{CC}	$V_{IH} = V_{CC} - 0.6 \ V$		2.7 to 3.6	_	500	

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Symbol Test Condition		Min	Max	Unit
Characteristics	Symbol		V _{CC} (V)	IVIIII	Max	Offic
			1.8±0.15	_	25.0	
Propagation delay time	t _{pLH}		2.5±0.2	_	8.0	
(A, B, C- Y)	t _{pHL}	Figure 1, Figure 2	2.7	_	7.0	ns
		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0		
		Figure 1, Figure 2	1.8±0.15	_	25.0	ns
Propagation delay time	t _{pLH}		2.5±0.2	_	9.0	
(G1- Y)	t _{pHL}		2.7	_	8.0	
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	7.0	
			1.8±0.15		25.0	ns
Propagation delay time	t _{pLH}		2.5±0.2	_	8.0	
$(\overline{G}2 - \overline{Y})$	t _{pHL}	Figure 1, Figure 2	2.7		7.0	
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	
	t _{osLH}	(Note)	2.7		_	
Output to output skew	t _{osHL}	(NOTE)	$\textbf{3.3}\pm\textbf{0.3}$	—	1.0	ns

Note: Parameter guaranteed by design.

 $(t_{osLH} = |t_{pLHm} - t_{pLHn}|, t_{osHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5 \text{ ns}$, $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V_{OL}	VOLP	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	—	3.3	7	pF
Output capacitance	C _{OUT}	—	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	e) 3.3	25	pF

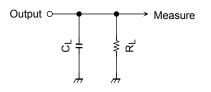
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

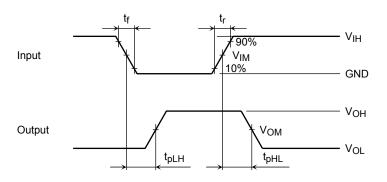
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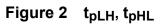
AC Test Circuit





AC Waveform





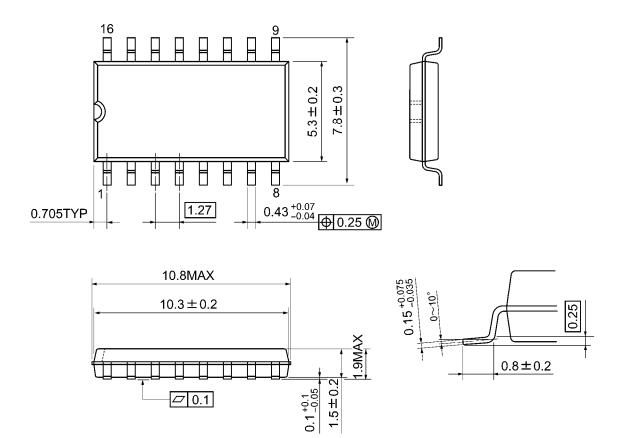
		V _{CC}						
	Symbol	$3.3\pm0.3~V$	2.5 + 0.2 V	1.8 ± 0.15 V				
		2.7V	2.5 ± 0.2 V	1.0 ± 0.15 V				
Input	VIH	2.7V	V _{CC}	V _{CC}				
	VIM	1.5V	V _{CC} /2	V _{CC} /2				
	tr,tf	2.5ns	2.0ns	2.0ns				
Output	V _{OM}	1.5V	V _{OH} /2	V _{OH} /2				
Load	CL	50pF	30pF	30pF				
	RL	500 Ω	500 Ω	1kΩ				



Package Dimensions

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL16-P-150-1.27

16 9 日日 Ħ Ħ Ħ Ħ <u>A</u> 3.9±0.1 6.0±0.2 Ħ H ₿ Ħ Ħ Ħ Ħ E 8 1 0.42±0.07 ⊕ 0.25 ₪ 0.505TYP 1.27 9.9±0.1 1.375±0.2 **1.75MAX** 0.15+0.15 45° 0.175±0.075 ☑ 0.1 ۍ 0.7±0.3

This package is not available in japan. Note:

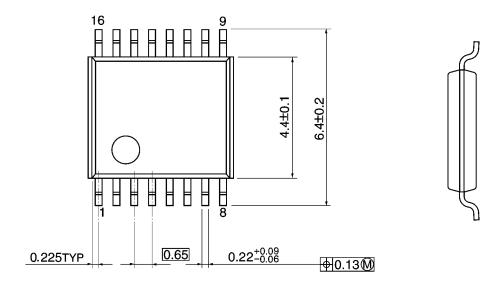
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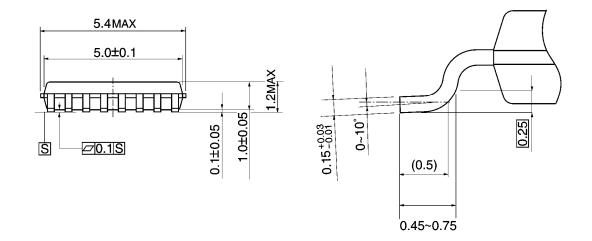
Unit : mm

Package Dimensions

TSSOP16-P-0044-0.65A

Unit: mm





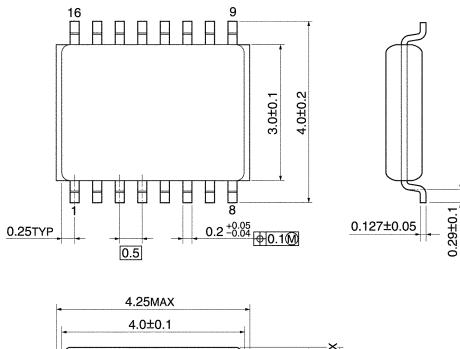
Weight: 0.06 g (typ.)

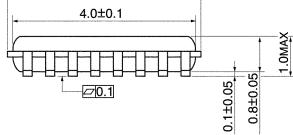


Package Dimensions

VSSOP16-P-0030-0.50

Unit: mm





Weight: 0.02 g (typ.)

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