# FAIRCHILD

SEMICONDUCTOR

## DM74LS503 8-Bit Successive Approximation Register (with Expansion Control)

### **General Description**

The DM74LS503 register has an active LOW Enable ( $\overline{E}$ ) input that is used in cascading two or more packages for longer word lengths. A HIGH signal on  $\overline{E}$ , after a START operation, forces Q7 HIGH and prevents the device from accepting serial data. With the  $\overline{E}$  input of an DM74LS503 connected to the  $\overline{CC}$  output of a preceding (more significant) device, the DM74LS503 will be inhibited until the preceding device is filled, causing its  $\overline{CC}$  output to go LOW. This LOW signal then enables the DM74LS503 to accept the serial data on subsequent clocks.

#### Features

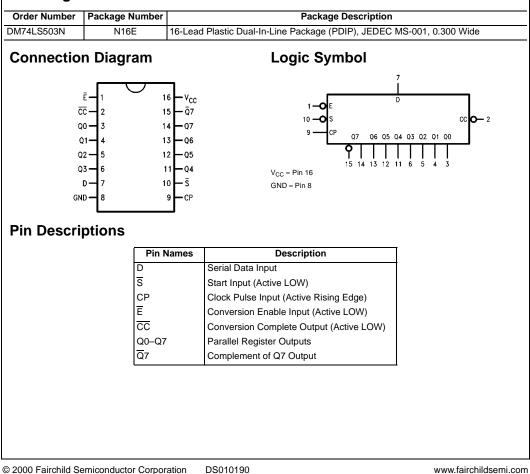
- Performs serial-to-parallel conversion
- Expansion control for longer words
- Storage and control for successive approximation A to D conversion

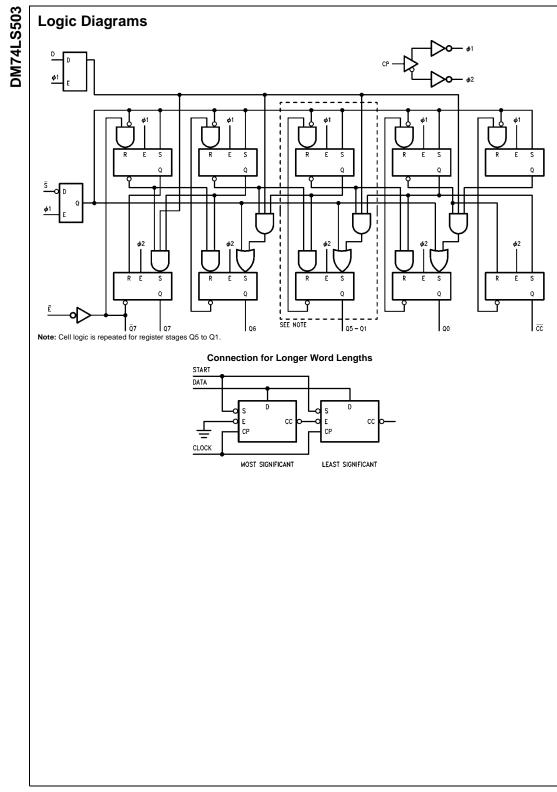
March 1989

Revised March 2000

Low power Schottky version of 2503

## Ordering Code:





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2

#### Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# DM74LS503

#### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>ОН</sub>	HIGH Level Output Voltage			-0.4	mA
I <sub>OL</sub>	LOW Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C
t <sub>S</sub> (H)	Setup Time HIGH or LOW	16			ns
t <sub>S</sub> (L)	S to CP	16			
t <sub>H</sub> (H)	Hold Time HIGH or LOW	0			ns
t <sub>H</sub> (L)	S to CP	0			
t <sub>S</sub> (H)	Setup Time HIGH or LOW	8			ns
t <sub>S</sub> (L)	D to CP	8			
t <sub>H</sub> (H)	Hold Time HIGH or LOW	10			ns
t <sub>H</sub> (L)	D to CP	10			
t <sub>W</sub> (H)	CP Pulse Width HIGH or LOW	46			
t <sub>W</sub> (L)		46			ns

#### **Electrical Characteristics**

Over recommended operating free air temperature range (unless otherwise noted)

V <sub>OH</sub> Hie Ou	IGH Level Iutput Voltage	$\label{eq:V_CC} \begin{split} &V_{CC} = \text{Min}, \ I_I = -18 \ \text{mA} \\ &V_{CC} = \text{Min}, \ I_{OH} = \text{Max}, \\ &V_{IL} = \text{Max} \end{split}$	2.7	-1.5	V
Ou	utput Voltage	66 611	2.7		
	1 8	V <sub>IL</sub> = Max	2.1		v
V	011/1		2.1		
V <sub>OL</sub> LC	OW Level	$V_{CC} = Min, I_{OL} = Max, V_{IH} = Min$		0.5	V
Ou	utput Voltage	$I_{OL} = 4 \text{ mA}, V_{CC} = Min$		0.4	
l <sub>i</sub> Inp	put Current @ Max	$V_{CC} = Max, V_I = 7V$		0.1	mA
Inp	put Voltage	$V_I = 10V$		0.1	
I <sub>IH</sub> HI	IGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$		20	μΑ
I <sub>IL</sub> LC	OW Level Input Current	$V_{CC} = Max, V_I = 0.4V$		-0.8	mA
I <sub>OS</sub> Sh	hort Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	-20	-100	mA
I <sub>CC</sub> Su	upply Current	V <sub>CC</sub> = Max		65	mA

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

#### **Switching Characteristics**

Symbol	Parameter	$R_L = 2 k\Omega,$	$\mathbf{R}_{\mathbf{L}} = 2 \mathbf{k} \Omega, \mathbf{C}_{\mathbf{L}} = 15 \mathbf{pF}$		
		Min	Max	Units	
f <sub>MAX</sub>	Maximum Count Frequency	15		MHz	
t <sub>PLH</sub>	Propagation Delay		35	ns	
t <sub>PHL</sub>	CP to Qn or CC		25		
t <sub>PLH</sub>	Propagation Delay		20		
t <sub>PHL</sub>	E to Q7		24	ns	

3

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