

August 1986 Revised March 2000

DM74LS14

Hex Inverter with Schmitt Trigger Inputs

General Description

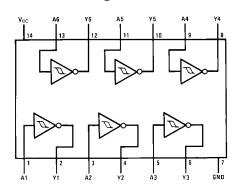
This device contains six independent gates each of which performs the logic INVERT function. Each input has hysteresis which increases the noise immunity and transforms a slowly changing input signal to a fast changing, jitter free output.

Ordering Code:

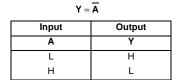
Order Number	Package Number	Package Description			
DM74LS14M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow			
DM74LS14SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide			
DM74LS14N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table



H = HIGH Logic Level L = LOW Logic Level

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DS006353

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Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C to } +150^{\circ}\text{C}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V_{T+}	Positive-Going Input Threshold Voltage (Note 2)	1.4	1.6	1.9	V
V_{T-}	Negative-Going Input Threshold Voltage (Note 2)	0.5	0.8	1	V
HYS	Input Hysteresis (Note 2)	0.4	0.8		V
I _{OH}	HIGH Level Output Current			-0.4	mA
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Note 2: V_{CC} = 5V.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 3)	Max	Units
/ _I	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
_{′он}	HIGH Level	V _{CC} = Min, I _{OH} = Max	2.7	3.4		V
	Output Voltage	$V_{IL} = Max$	2.7			V
OL.	LOW Level	V _{CC} = Min, I _{OL} = Max		0.35	0.5	V
	Output Voltage	put Voltage V _{IH} = Min		0.33		
		V _{CC} = Min, I _{OL} = 4 mA		0.25	0.4	
Γ+	Input Current at	$V_{CC} = 5V$, $V_I = V_{T+}$		-0.14		mA
	Positive-Going Threshold					
Γ–	Input Current at	$V_{CC} = 5V$, $V_I = V_{T-}$		-0.18		mA
	Negative-Going Threshold					
1	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
Н	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
IL	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.4	mA
os	Short Circuit Output Current	V _{CC} = Max (Note 4)	-20		-100	mA
ССН	Supply Current with Outputs HIGH	V _{CC} = Max		8.6	16	mA
CCL	Supply Current with Outputs LOW	V _{CC} = Max		12	21	mA

Note 3: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

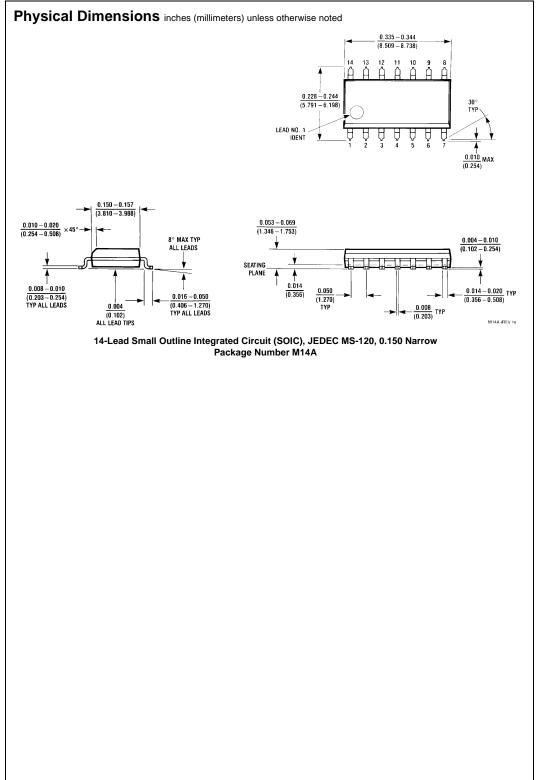
Note 4: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

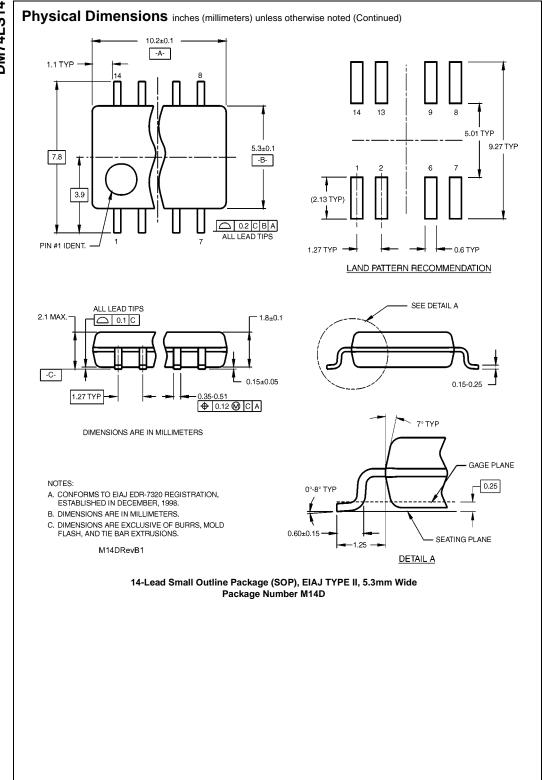
at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

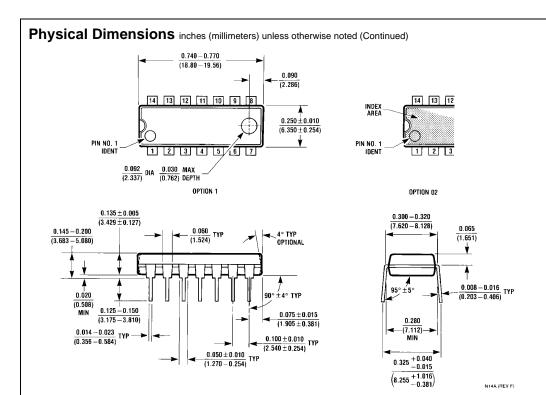
	Parameter	$R_L = 2 k\Omega$				
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	5	22	8	25	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	5	22	10	33	ns

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14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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