LOW VOLTAGE POWER AMPLIFIER

GENERAL DESCRIPTION

NJM2070 is a power amplification monolithic IC of wide Operating voltage range. It is applied for audio power amplifier in portable radio and handy cassette player.

FEATURES

Operating Voltage

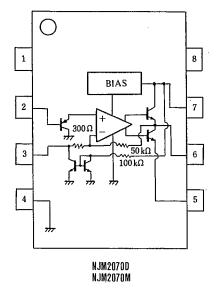
 $(1.8V \sim 15V)$ $4mA typ : V^+=6V)$

Low Operating Current Package Outline

DIP8, DMP8

Bipolar Technology

■ PIN CONFIGURATION



■ PACKAGE OUTLINE





PIN FUNCTION

- 1. NC
- 2. +INPUT 3. -INPUT 4. GND
- 5. GND
- 6. OUTPUT 7. V+ 8. NC

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ¹	15	V
Output Peak Current	Іор	1	A
Power Dissipation	Po	(DIP8) 700 (DMP8) 500 (note)	mW
Operating Temperature Range	Topr	-40∼+85	°C
Storage Temperature Range	Tstg	-40~+125	°C

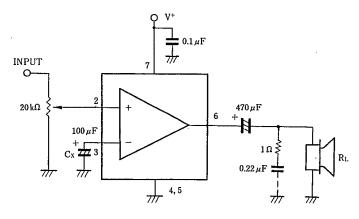
(note) At on PC board

■ ELECTRICAL CHARACTERISTICS

(V*=6V, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V+		1.8	_	15	V
Output Voltage	Vo		<u> </u>	2.7	<u> </u>	ν
Operating Current	lcc	$R_L = \infty$	_	4	7	mA
Input Bias Current	I _{IB}		_	200	_	nA
Output Power		THD=10%, f=1kHz				
	Po	$V^{+}=6V$, $R_L=4\Omega$	0.5	0.6	—	l w
	Po	$V^{+}=4.5V, R_{L}=4\Omega$	<u> </u>	0.32	—	w
	Po	$V^{+}=3V$, $R_L=4\Omega$		120	 	mW
	Po	$V^{+}=2V$, $R_L=4\Omega$	<u> </u>	30	—	mW
		THD=1%, f=1kHz				
	Po	$V^{+}=6V$, $R_L=4\Omega$		500	l —	mW
•	Po	$V^{+}=4.5V, R_{L}=4\Omega$		250		mW
Total Harmonic Distortion	THD	$P_0 = 0.4W$, $R_L = 4\Omega$, $f = 1kHz$		0.25	—	%
Voltage Gain	Av	ſ=1kHz	41	44	47	dB
Input Impedance	Z _{IN}	f=1kHz	100	_	l —	kΩ
Equivalent Input Noise Voltage	V _{NII}	$R_S = 10k\Omega$, A Curve		2.5		μ٧
	V _{N12}	$R_S = 10k\Omega$, $B = 22Hz \sim 22kHz$	_	3	l —	μ٧
Ripple Rejection	RR	$f = 100 \text{Hz}, C_X = 100 \mu \text{F}$	24	30	—	dB
Cut Off Frequency	f _H	$A_V = -3dB$ from $f = 1kHz$	l —	200		kHz
		$R=8\Omega$, $P_{O}=250$ mW				

■ TYPICAL APPLICATION AND TEST CIRCUIT

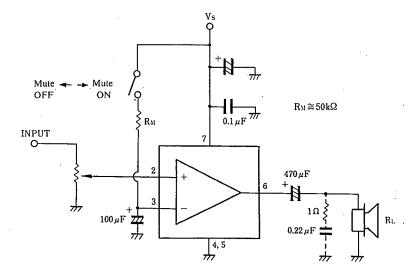


■ OSCILLATION PREVENTION

Put in series a 1Ω resistor and a $0.22~\mu\text{F}$ capacitor on parallel to load, if the load is speaker. Recommend putting in parallel between pin 4 and pin 7, 0.1 μF and more than $100~\mu\text{F}$ capacitors with good high frequency characteristics near to the ground and supply voltage pins on parallel.

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■ MUTING CIRCUIT



N.	JN	12	N	7	N
.	JIY		v	•	v

MEMO

[CAUTION]
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