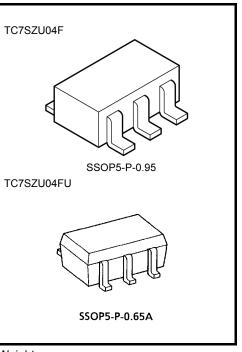
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZU04F,TC7SZU04FU

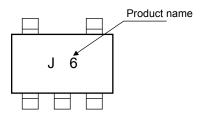
Inverter (Un-Buffer)

Features

- High output drive: $\pm 16 \text{ mA} (\text{min}) \text{ at } V_{CC} = 4.5 \text{ V}$
- Low quiescent power: $I_{CC} = 2 \mu A (max)$
 - at V_{CC} = 5.5 V, Ta = 25°C
- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V
- 5.5-V tolerant input



Marking

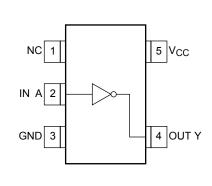


Weight SSOP5-P-0.95 :0.016 g (typ.) SSOP5-P-0.65A: 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input diode current	I _{IK}	-20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	ΤL	260	°C

Pin Assignment (top view)

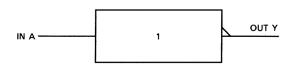


Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Logic Diagram



A	Y	
L	Y H	
Н	L	

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.8~5.5	V	
Supply voltage	vcc	1.5~5.5 (Note 1)	v	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	

Note 1: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	vmbol Tost Condition			Ta = 25°C			Ta =4	Ta = -40~85°C	
		Test	Test Condition		Min	Тур.	Max	Min	Max	Unit
High-level input voltage				1.8	$\begin{array}{c} 0.85 \\ \times \ V_{CC} \end{array}$	_	_	$\begin{array}{c} 0.85 \\ \times V_{CC} \end{array}$	_	V
				2.3-5.5	$0.8 \\ \times V_{CC}$		_	$0.8 \times V_{CC}$	_	v
Low-level input voltage			1.8	_		0.15 × V _{CC}	_	$0.15 \times V_{CC}$	v	
	۷IL			2.3-5.5	_		$0.2 \\ \times V_{CC}$	_		$0.2 \\ \times V_{CC}$
				1.8	1.6	1.8		1.6	_	
		$V_{IN} = V_{IL}$	I _{OH} = -100 μA	2.3	2.1	2.3		2.1	—	
		VIN – VIL	IOH = -100 μA	3.0	2.7	3.0		2.7	_	
High-level	Vou			4.5	4.0	4.4		4.0	_	V
output voltage	Vон	V _{IN} = GND	$I_{OH} = -4 \text{ mA}$	2.3	1.9	2.14		1.9	_	V
			I _{OH} = -8 mA	3.0	2.4	2.75		2.4		
			$I_{OH} = -12 \text{ mA}$	3.0	2.3	2.61		2.3	_	
			$I_{OH} = -16 \text{ mA}$	4.5	3.8	4.13		3.8	_	
	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 100 μA	1.8		0	0.2		0.2	- V
				2.3		0	0.2		0.2	
				3.0		0	0.3		0.3	
Low-level				4.5		0	0.5		0.5	
output voltage		VIN = VCC	$I_{OL} = 4 \text{ mA}$	2.3		0.1	0.3		0.3	
			I _{OL} = 8 mA	3.0	—	0.17	0.4		0.4	
			$I_{OL} = 12 \text{ mA}$	3.0		0.25	0.55		0.55	
			I _{OL} = 16 mA	4.5	—	0.26	0.55		0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0-5.5	_	_	±1	_	±10	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		5.5	—	_	2	—	20	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay time		$\begin{array}{l} C_L = 15 \ \text{pF}, \\ R_L = 1 \ \text{M}\Omega \end{array} \\ \\ \hline C_L = 50 \ \text{pF}, \\ R_L = 500 \ \Omega \end{array} \end{array}$	1.8	1.0	_	8.5	1.0	9.0	ns
			2.5 ± 0.2	0.8	_	6.2	0.8	6.5	
	tplh tphl		$\textbf{3.3}\pm\textbf{0.3}$	0.5	_	4.5	0.5	4.8	
			5.0 ± 0.5	0.5	_	3.9	0.5	4.1	
			$\textbf{3.3}\pm\textbf{0.3}$	1.0	_	6.0	1.5	6.5	
			5.0 ± 0.5	0.8	_	5.0	0.8	5.5	
Input capacitance	CIN	—	0-5.5	_	4.5	_	_	_	pF
Power dissipation capacitance	C	(Note 2)	3.3	_	6.3		_	_	
	C _{PD} (Note 2		5.5	_	9.5	_	_		pF

Note2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

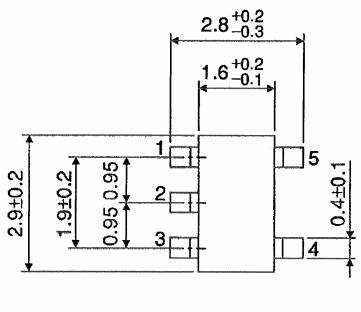
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

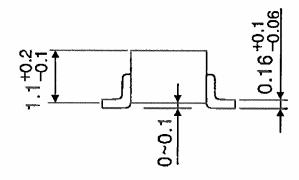
<u>TOSHIBA</u>

Package Dimensions

SSOP5-P-0.95

Unit : mm

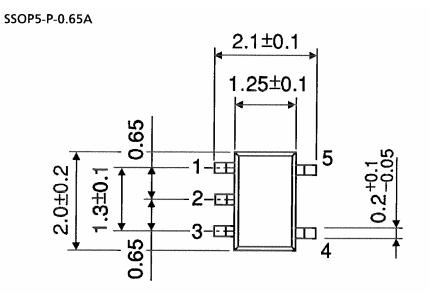


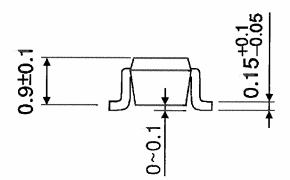


Weight: 0.016 g (typ.)

<u>TOSHIBA</u>

Package Dimensions





Weight: 0.006 g (typ.)

Unit : mm

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20070701-EN GENERAL

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