# High Temperature Analog Multiplexers 16-Channel Single-Ended / 8-Channel Differential HT506 / HT507 

The High Temperature HT506/HT507 monolithic multiplexers consist of sixteen analog switches, 4-bit decode for channel selection, reference for logic switching thresholds, and enable pin for device deactivation where applications require. These multiplexers are fabricated with Honeywell's dielectrically isolated latch-up free high temperature (HTMOS ${ }^{\text {TM }}$ ) linear process. Performance is specified over the full -55 to $+225^{\circ} \mathrm{C}$ temperature range. Typically, parts will operate up to $+300^{\circ} \mathrm{C}$ for a year, with derated performance. All parts are burned in at $250^{\circ} \mathrm{C}$. The input buffers are designed to operate from either TTL or CMOS levels while providing a break-before-make action. The HT506 switches one of the sixteen single-ended inputs to a common output, while the HT507 switches one of the eight differential inputs to a differential output. These parts are available in standard pinout 28-pin DIP Ceramic Packages.

## Applications

> Down-Hole Oil, Gas, and Geothermal Well
> Avionics
> Turbine Engine Control
> Industrial Process Control
> Electric Power Conversion
> Heavy Duty Internal Combustion Engine


## FEATURES AND BENEFITS

- Specified over -55 to $+225^{\circ} \mathrm{C}$
- 16:1 Single-Ended or 8:1 Differential Configuration
- No latch-up
- On resistance $400 \Omega$ at $225^{\circ} \mathrm{C}$
- Output leakage less than $2.5 \mu \mathrm{~A}$ at $225^{\circ} \mathrm{C}$
- Designed to continuously operate for at least 5 years at $225^{\circ} \mathrm{C}$
- Enable and address inputs compatible with TTL and/or 5V CMOS logic
- 10 V analog input/output range ( $\pm 5 \mathrm{~V}$ or 0 to 10 V )
- Split and single supply capability
- Break-Before-Make Switching


## HT506/HT507

## PACKAGE PINOUTS

PACKAGE PINOUT HT506


PACKAGE PINOUT HT507


## ELECTRICAL CHARACTERISTICS (Split Supply)

Temperature range -55 to $+225^{\circ} \mathrm{C}$, typical $@+25^{\circ} \mathrm{C}, \mathrm{V}+=+5 \mathrm{~V}, \mathrm{~V}-=-5 \mathrm{~V}, \mathrm{GND}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0.8, \mathrm{~V}_{\mathrm{IH}}=2.4 \mathrm{~V}$, unless otherwise specified

| Symbol | Parameters | Test Conditions | Typical (1) | Worst Case (2) |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | MIN | MAX |  |

## Analog Switch

| $\mathrm{V}_{\text {ANALOG }}$ | Analog Signal Range |  |  | -5 | 5 | V |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{r}_{\mathrm{DS}(\mathrm{ON})}$ | Drain-Source On-Resistance | $V_{D} \pm 5 \mathrm{~V}, I_{S}=-10 \mathrm{~mA}$ Sequence Each Switch On | 100 |  | 400 | $\Omega$ |
| $\Delta \mathrm{r}_{\text {DS(ON }}$ | $\mathrm{r}_{\mathrm{DS}(\mathrm{ON})}$ Matching between Channels | $\mathrm{V}_{\mathrm{D}}= \pm 5 \mathrm{~V}$ | 2 |  |  | \% |
| $\mathrm{I}_{\text {S(OFF) }}$ | Source Off Leakage Current | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}$ | 0.01 |  | 200 | nA |
| $\mathrm{l}_{\text {(OFF) }}$ | Drain Off Leakage Current | $\mathrm{V}_{\mathrm{D}}= \pm 5 \mathrm{~V}, \mathrm{~V}_{\text {EN }}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{S}}= \pm 5 \mathrm{~V}$ | 0.04 | -2500 | 2500 | nA |
| $\mathrm{I}_{\mathrm{D}(\mathrm{ON})}$ | Drain On Leakage Current | Sequence Each Switch On | 0.04 | -2500 | 2500 | nA |

## Digital Control

| $\mathrm{V}_{\mathrm{IH}}$ | Logic High Input Voltage |  |  | 2.4 |  | V |
| :--- | :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{~V}_{\mathrm{IL}}$ | Logic Low Input Voltage |  | 0.8 |  |  | V |
| $\mathrm{I}_{\mathrm{IH}}$ | Logic High Input Current | $\mathrm{V}_{\mathrm{A}}=2.4 \mathrm{~V}, 10 \mathrm{~V}$ |  | -1 | 1 | $\mu \mathrm{~A}$ |
| $\mathrm{I}_{\mathrm{IL}}$ | Logic Low Input Current | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}, 2.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{A}}=0 \mathrm{~V}$ |  | -1 | 1 | $\mu \mathrm{~A}$ |
| $\mathrm{C}_{\mathrm{IN}}$ | Logic Input Capacitance | $\mathrm{f}=1 \mathrm{MHz}$ | 7 |  |  | pF |

## Dynamic Characteristics

| ton | Address/Enable Turn-On Time | trise/tfall<50ns |  | 100 | 400 | ns |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| toff | Address/Enable Turn-Off Time | trise/tfall<50ns |  | 30 | 200 | ns |
| Q | Charge Injection | $\mathrm{C}_{\mathrm{L}}=1 \mathrm{nF}, \mathrm{V}_{\mathrm{S}}=0 \mathrm{~V}, \mathrm{R}_{\mathrm{S}}=0 \Omega$ | TBD |  |  | pC |
| $\mathrm{O}_{\text {IS }}$ | Off Isolation | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega, \mathrm{f}=100 \mathrm{kHZ}$ | TBD |  |  | dB |
| Power Supplies |  |  |  |  |  |  |
| I+ | Positive Supply Current | $\mathrm{V}_{\mathrm{EN}}=\mathrm{V}_{\mathrm{A}}=0 \mathrm{~V}$ or 5 V | 50 |  | 250 | $\mu \mathrm{A}$ |
| I- | Negative Supply Current |  | -0.01 | -20 |  | $\mu \mathrm{A}$ |

(1) Typical operating conditions: $\mathrm{V}+=5 \mathrm{~V}, \mathrm{~V}-=-5 \mathrm{~V}, \mathrm{TA}=25^{\circ} \mathrm{C}$.
(2) Worst case operating conditions: $\mathrm{V}+=+5 \mathrm{~V} \pm 10 \%, \mathrm{~V}-=-5 \mathrm{~V} \pm 10 \%, \mathrm{TA}=-55$ to $125^{\circ} \mathrm{C}$.

## ELECTRICAL CHARACTERISTICS (Single Supply)

Temperature range -55 to $+225^{\circ} \mathrm{C}$, typical $@+25^{\circ} \mathrm{C}, \mathrm{V}+=+10 \mathrm{~V}, \mathrm{GND}=\mathrm{V}-=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0.8, \mathrm{~V}_{\mathrm{H}}=2.4 \mathrm{~V}$, unless otherwise specified

| Symbol | Parameters | Test Conditions | Typical <br> (1) | Worst Case (2) |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | MIN | MAX |  |
| Analog Switch |  |  |  |  |  |  |
| $\mathrm{V}_{\text {ANALOG }}$ | Analog Signal Range |  | 11 |  |  | V |
| $\mathrm{r}_{\text {DS(ON) }}$ | Drain-Source On-Resistance | $\mathrm{V}_{\mathrm{D}}=3 \mathrm{~V}, 10 \mathrm{~V}, \mathrm{I} \mathrm{S}=1 \mathrm{~mA}$ | 80 |  | 400 | $\Omega$ |
| $\Delta r_{\text {DS(ON) }}$ | $\mathrm{r}_{\mathrm{DS}(\text { ON })}$ Matching between Channels | Sequence Each Switch On | 2 |  |  | \% |
| $\mathrm{I}_{\text {S(OFF) }}$ | Source Off Leakage Current | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}$ | 0.01 |  | 200 | nA |
| $\mathrm{I}_{\text {( }(\text { OFF) }}$ | Drain Off Leakage Current | $\mathrm{V}_{\mathrm{S}}=0.5 \mathrm{~V}$ or 10 V | 0.04 | -2500 | 2500 | nA |
| $\mathrm{I}_{\mathrm{D}(\mathrm{ON})}$ | Drain On Leakage Current | $V_{S}=V_{D}=+10 \mathrm{~V}$ <br> Sequence Each Switch On | 0.04 | -2500 | 2500 | nA |
| Digital Control |  |  |  |  |  |  |
| $\mathrm{V}_{\text {IH }}$ | Logic High Input Voltage |  |  | 2.4 |  | V |
| $\mathrm{V}_{\text {IL }}$ | Logic Low Input Voltage |  | 0.8 |  |  | V |
| $\mathrm{I}_{\mathrm{IH}}$ | Logic High Input Current | $\mathrm{V}_{\mathrm{A}}=2.4 \mathrm{~V}, 10 \mathrm{~V}$ |  | -1 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {L }}$ | Logic Low Input Current | $\mathrm{V}_{\mathrm{EN}}=0 \mathrm{~V}, 2.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{A}}=0 \mathrm{~V}$ |  | -1 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{C}_{\text {IN }}$ | Logic Input Capacitance | $\mathrm{f}=1 \mathrm{MHz}$ | 7 |  |  | pF |
| Dynamic Characteristics |  |  |  |  |  |  |
| ton(EN) | Address/Enable Turn-On Time | trise/tfall<50ns |  | 100 | 400 | ns |
| toff(EN) | Address/Enable Turn-Off Time | trise/tall<50ns |  | 30 | 200 |  |
| Q | Charge Injection | $\mathrm{C}_{\mathrm{L}}=1 \mathrm{nF}, \mathrm{V}_{\mathrm{S}}=6, \mathrm{R}_{\mathrm{S}}=0$ | TBD |  |  | pC |
| Power Supplies |  |  |  |  |  |  |
| I+ | Positive Supply Current | = 0 V or 5V | 50 |  | 250 | $\mu \mathrm{A}$ |
| I- | Negative Supply Current | $=0 \mathrm{~V}$ or 5 | -0.01 | -20 |  | $\mu \mathrm{A}$ |

(1) Typical operating conditions: $\mathrm{V}+=10 \mathrm{~V}, \mathrm{~V}-=\mathrm{GND}=0 \mathrm{~V}, \mathrm{TA}=25^{\circ} \mathrm{C}$.
(2) Worst case operating conditions: $\mathrm{V}+=+10 \mathrm{~V} \pm 10 \%, \mathrm{~V}-=\mathrm{GND}=0 \mathrm{~V}, \mathrm{TA}=-55$ to $125^{\circ} \mathrm{C}$.

TRUTH TABLE - HT506

| A3 | A2 | A1 | A0 | EN | On Switch |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $X$ | $X$ | $X$ | $X$ | 0 | None |
| 0 | 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 0 | 1 | 1 | 2 |
| 0 | 0 | 1 | 0 | 1 | 3 |
| 0 | 0 | 1 | 1 | 1 | 4 |
| 0 | 1 | 0 | 0 | 1 | 5 |
| 0 | 1 | 0 | 1 | 1 | 6 |
| 0 | 1 | 1 | 0 | 1 | 7 |
| 0 | 1 | 1 | 1 | 1 | 8 |
| 1 | 0 | 0 | 0 | 1 | 9 |
| 1 | 0 | 0 | 1 | 1 | 10 |
| 1 | 0 | 1 | 0 | 1 | 11 |
| 1 | 0 | 1 | 1 | 1 | 12 |
| 1 | 1 | 0 | 0 | 1 | 13 |
| 1 | 1 | 0 | 1 | 1 | 14 |
| 1 | 1 | 1 | 0 | 1 | 15 |
| 1 | 1 | 1 | 1 | 1 | 16 |

TRUTH TABLE - HT507

| A2 | A1 | A0 | EN | On Switch |
| :---: | :---: | :---: | :---: | :---: |
| X | X | X | 0 | None |
| 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 1 | 1 | 2 |
| 0 | 1 | 0 | 1 | 3 |
| 0 | 1 | 1 | 1 | 4 |
| 1 | 0 | 0 | 1 | 5 |
| 1 | 0 | 1 | 1 | 6 |
| 1 | 1 | 0 | 1 | 7 |
| 1 | 1 | 1 | 1 | 8 |

Logic "0" = VAL $\leq 0.8 \mathrm{~V}$
Logic " 1 " = VAH $\geq 2.4 \mathrm{~V}$
$\mathrm{X}=$ Irrelevent

## ABSOLUTE MAXIMUM RATINGS

| Parameter | Value | Units |
| :--- | :---: | :---: |
| Voltages Referenced to V-, V+ | +15 | V |
| Digital Inputs VS, VD | -0.5 to VDD +0.5 | V |
| Current (any terminal) | 10 | mA |
| Peak Current, S or D, (Pulsed at 1ms, $10 \%$ Duty Cycle Max) | 15 | mA |
| Storage Temperature | -65 to +325 | ${ }^{\circ} \mathrm{C}$ |
| Power Dissipation (Package) | 500 | mW |
| ESD Protection | 1000 | V |

## 28-LEAD PACKAGE



ORDERING INFORMATION


## Find out more

For more information on Honeywell's High Temperature Electronics visit us online at www.honeywell.com/hightemp or contact us at 800-323-8295 or 763-954-2474. Customer Service Email: ps.customer.support@honeywell.com.

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