



General Description

The MAX7443/MAX7444 are low-cost, triple-channel video reconstruction filters for S-video and CVBS video signals. These devices are ideal for anti-aliasing and DAC smoothing video applications such as set-top boxes, DVD players, hard-disk recorders (HDRs), and personal video recorders. These filters operate from a single +5V supply. They are optimized for NTSC, PAL, and standard-definition digital TV (SDTV) video systems.

Each channel consists of a lowpass filter and an adjustable-gain output buffer capable of driving two standard 150Ω video loads. The output buffers can drive either an AC or a DC load such that the blanking level voltage after the backmatch resistor is less than 1V. The Y and C video inputs are summed to produce the composite video output. The MAX7444 offers high-frequency boost to improve picture sharpness. The MAX7443 has a maximum flat-frequency response.

The MAX7443/MAX7444 are available in a tiny 8-pin thin QFN and an 8-pin SO package with exposed pad, and are fully specified over the -40°C to +85°C extended temperature range.

Applications

Set-Top Boxes/HDR/DVD Security Cameras/Digital Cameras Game Consoles

Digital VCRs Every Composite, S-Video Output for NTSC, PAL, SDTV

Features

 Triple-Channel Filter and Buffer for CVBS and Y/C (S-Video)

MXXIM

- Filter Response Ideal for NTSC, PAL, and Interlaced SDTV Signals
- 41dB (typ) Stopband Attenuation at 27MHz
- -0.3dB (typ) Passband Response
- Low Blanking Level Voltage Allows DC-Coupled Output
- Each Channel Drives Two 150Ω Video Loads
- ♦ +5V Single-Supply Voltage
- Tiny 8-Pin Thin QFN or 8-Pin SO Packages

_Ordering Information

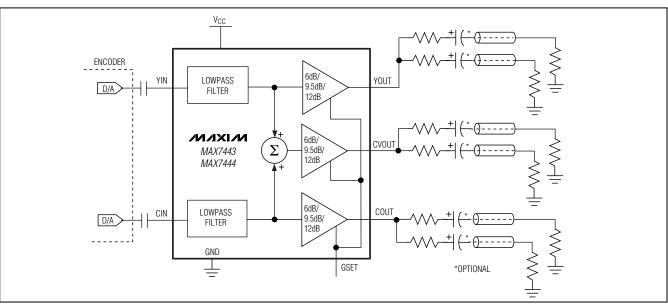
PART	PIN-PACKAGE	TOP MARK	PKG CODE
MAX7443ETA	8 Thin QFN-EP*	AFX	T833-1
MAX7443ESA	8 SO-EP*	_	S8E-12
MAX7444ETA	8 Thin QFN-EP*	AFY	T833-1
MAX7444ESA	8 SO-EP*	_	S8E-12

Note: All devices specified over -40°C to +85°C temperature range.

*EP = Exposed pad.

Selector Guide appears at end of data sheet.

Functional Diagram



Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +5V \pm 5\%, C_L = 0 \text{ to } 20\text{pF}, R_L = 75\Omega \text{ to GND} \text{ for DC-couple}, R_L = 75\Omega \text{ to } V_{CC}/2 \text{ for AC-couple}, C_{YIN} = C_{CIN} = 0.1\mu\text{F}, T_A = T_{MIN} \text{ to } T_{MAX}, \text{GSET} = \text{GND} (6\text{dB}) \text{ and } V_{CC} (9.5\text{dB}), \text{ unless otherwise noted}. Typical values are at T_A = +25^{\circ}\text{C}.)$

PARAMETER	SYMBOL	CONDITIO	ONS	MIN	ТҮР	MAX	UNITS	
Passband Response		f = 100kHz to 4.2MHz,	MAX7443	-0.8	-0.3	+0.1	dB	
rassualiu nespulise		relative to 100kHz	MAX7444	-0.1	+0.6	+0.92	uв	
Stopband Attenuation	A _{SB}	f = 27MHz		39	41		dB	
Boost Amplitude		f = 4.2MHz, MAX7444			0.6		dB	
Differential Gain	dG	5-step modulated staircase	Gain = 6dB, 9.5dB, 12dB		0.15	0.4	%	
Differential Phase	dθ	5-step modulated staircase	Gain = 6dB, 9.5dB, 12dB		0.15	0.4	Degrees	
Signal-to-Noise Ratio SNR		Peak signal (2V _{P-P}) to RMS noise, f = 100Hz to 50MHz	Gain = 6dB, 9.5dB, 12dB	67	73		dB	
	Δtg	Deviation from 100kHz to	MAX7443		8	20		
Group Delay Deviation		4.1MHz	MAX7444		12	30	ns	
Line-Time Distortion	HDIST	18µs, 100 IRE bar				0.3	%	
Field-Time Distortion	V _{DIST}	130 lines, 18µs, 100 IRE ba	ar			0.5	%	
Clamp Settling Time		To ±1%			430		Lines	
		YOUT		0.6	0.8	1.0		
Output DC Clamp Level		CVOUT		0.6	0.8	1.0	V	
		COUT			1.6	1.97		
Low-Frequency Gain Accuracy	Low-Frequency Gain Accuracy Av		f = 100kHz, relative to 6dB			+2	%	
Group Delay Matching	tg(MATCH)	Low-frequency channel-to-channel matching, f = 100kHz for YOUT and COUT			2		ns	
Channel-to-Channel Crosstalk	X _{TALK}	f = 100kHz to 3.58MHz, ga	ain = 6dB		61		dB	
Input Leakage Current	lin					10	μA	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +5V \pm 5\%, C_L = 0 \text{ to } 20\text{pF}, R_L = 75\Omega \text{ to GND} \text{ for DC-couple}, R_L = 75\Omega \text{ to } V_{CC}/2 \text{ for AC-couple}, C_{YIN} = C_{CIN} = 0.1\mu\text{F}, T_A = T_{MIN} \text{ to } T_{MAX}, \text{GSET} = \text{GND} \text{ (6dB) and } V_{CC} \text{ (9.5dB)}, \text{ unless otherwise noted}. Typical values are at T_A = +25^{\circ}\text{C}.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Input Dynamia Curing	YINP-P	GSET = GND (6dB)			1.2	
Input Dynamic Swing	CINP-P	GSET = GND (6dB)			0.9	VP-P
SUPPLY						
Supply Voltage Range	V _{CC}		4.75		5.25	V
Supply Current	Icc	$V_{CC} = +5.25V$, no load		88	110	mA
Power-Supply Rejection Ratio PSRR		$V_{IN} = 100mV_{P-P}$, f = 0 to 3.5MHz		33		dB

PASSBAND AMPLITUDE vs. FREQUENCY

 $(V_{CC} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$

AMPLITUDE vs. FREQUENCY

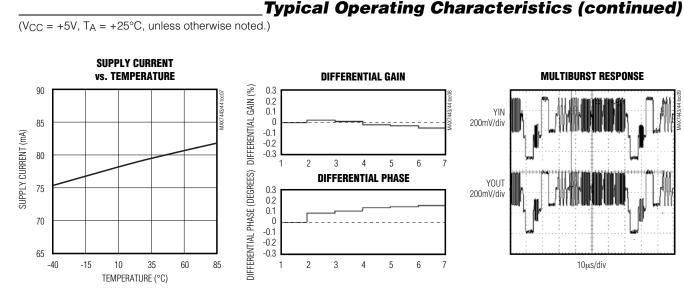
2.0 200 0 150 1.5 1.0 100 MAX7444 PHASE (DEGREES) 0 -0 0 -0 -20 (qb) 0.5 0 -0.5 AMPLITUDE (dB) -40 MAX7443 -100 -10 -60 NORMALIZED TO 0dB -1.5 -150 NORMALIZED TO 0dB -80 -2.0 -200 10 100 1 0.1 1 10 0.1 1 10 FREQUENCY (MHz) FREQUENCY (MHz) FREQUENCY (MHz) **MODULATED 12.5T RESPONSE GROUP DELAY vs. FREQUENCY** (1 IRE = 7.14 mV)2T RESPONSE (1 IRE = 7.14mV) 90 80 YIN 200mV/div **GROUP DELAY** (ns) Y_{IN} 70 200mV/div 60 Y_{OUT} 200mV/div 50 YOUT 400mV/div 40 0.1 10 400ns/div 1 200ns/div FREQUENCY (MHz) ///XI//

MAX7443/MAX7444

Typical Operating Characteristics

PHASE RESPONSE vs. FREQUENCY

MAX7443/MAX7444



Pin Description

PIN	NAME	FUNCTION
1	YIN	Luma (Y) Input
2	GSET	Gain-Setting Control Input. See Table 1 for values.
3	CIN	Chroma (C) Input
4	GND	Ground
5	V _{CC}	5V Power Supply
6	COUT	Chroma (C) Output
7	CVOUT	Composite Video Output
8	YOUT	Luma (Y) Output

Detailed Description

The primary function of the MAX7443/MAX7444 is to filter and buffer the encoder DAC outputs that contain digital video information in applications such as set-top boxes, hard-disk recorders, DVD players, and digital VCRs. These devices reconstruct and clean up analog video signals. Each MAX7443/MAX7444 consists of a lowpass filter and an adjustable-gain output video buffer that is capable of driving two standard 150 Ω video loads. The luma (YIN) and chroma (CIN) video inputs are summed internally to provide the composite (CVOUT) video output.

These devices operate from a single +5V supply. The filters have a nominal cutoff frequency optimized for NTSC, PAL, and SDTV.

Filter

Filter Response

The reconstruction filter consists of two 2nd-order Sallen-Key stages. The Butterworth-type response features a maximally flat passband for NTSC and PAL bandwidths. The stopband offers 41dB (typ) of attenuation at sampling frequencies of 27MHz and above (see *Typical Operating Characteristics*).

High-Frequency Boost

The high-frequency boost available in the MAX7444 compensates for signal degradation and rolloff in the video encoder, which increases the image sharpness. The MAX7443 has a flat response over the video bandwidth.

Output Buffer

///XI//

The output buffer drives two 150Ω video loads with a 2VP-P signal. The adjustable gain of the output buffer provides a gain of 6dB, 9.5dB, and 12dB, which are selected by tying the GSET pin to GND, V_{CC}, or floating, respectively. The MAX7443/MAX7444 can drive an AC load or drive the video load directly without using the large output capacitor. The output buffer drives a DC load with an output blanking level of less than 1V.

1

Table 1. Gain-Setting Control

GSET	GAIN (dB)
GND	6
Vcc	9.5
Open	12

12dB Gain Setting

The GSET pin is biased internally to V_{CC}/2 through two 100k Ω resistors from V_{CC} to GND. The internal impedance at the node is 50k Ω . No additional connection is necessary since the pin offers a minimum noise margin immunity of 1V_{P-P}.

Output Clamp Level

When sync pulses in the luma signal (Y) are detected, the DC restore loop is activated. The function of the loop is to set the sync tip of the video signal to the desired DC level of 0.8V for YOUT and COUT and the average DC voltage of COUT at 1.6V.

Applications Information

Input Considerations

Use a 0.1μ F ceramic capacitor to AC-couple the input to the MAX7443/MAX7444. This input capacitor stores a DC level such that the outputs are clamped to the appropriate DC voltage level.

Output Considerations

The outputs of the MAX7443/MAX7444 are typically connected to a 75 Ω series back-match resistor followed by the video cable. Because of the inherent divide by two of this configuration, the blanking level of the video cable is always less than 1V, which complies with industry-standard video requirements. The video buffer can also drive an AC-coupled video load. Good video performance is achieved with an output capacitor as low as 220µF.

Power-Supply Bypassing and Layout

The MAX7443/MAX7444 operate from a single +5V supply. Bypass V_{CC} to GND with a 0.1μ F capacitor. Place all external components as close to the device as possible. Refer to the MAX7443 evaluation kit for a proven PC board layout example.

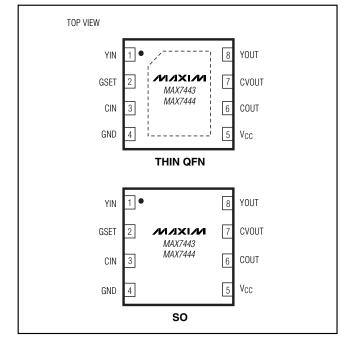
Exposed Pads

Both the SO and thin QFN packages of the MAX7443/ MAX7444 have exposed pads on the bottom of the packages. These pads are electrically connected to GND and should be connected to the ground plane for improved thermal conductivity. Do not route signals under these packages.

Selector Guide

PART	HIGH-FREQUENCY BOOST
MAX7443ETA	No
MAX7443ESA	No
MAX7444ETA	Yes
MAX7444ESA	Yes

Pin Configurations

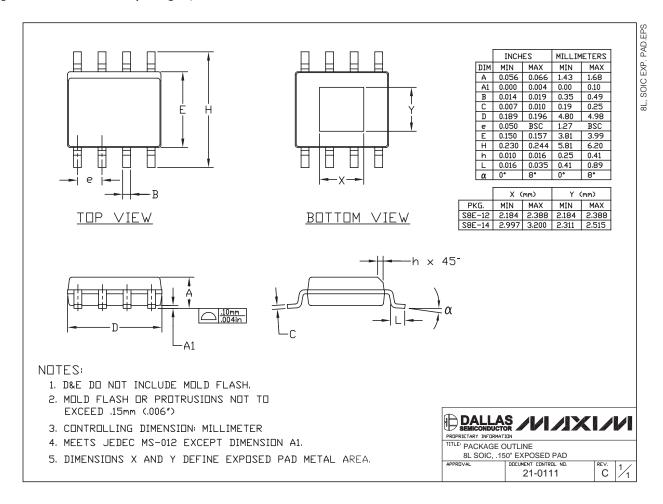


Chip Information

TRANSISTOR COUNT: 4252 PROCESS: BICMOS

Package Information

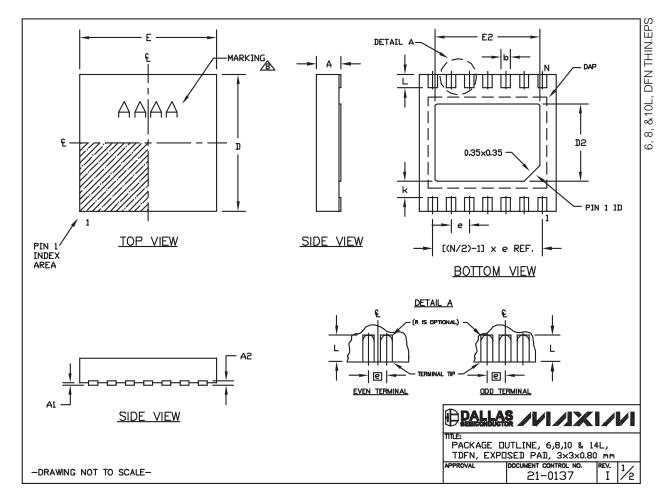
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



M/IXI/M

Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



_Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)

COMMON			1	ſ							
COMMON	DIMENS	SIONS		PACKAGE VA	RIATI	ONS					
SYMBOL	MIN.	MAX.]	PKG. CODE	Ν	D2	E2	е	JEDEC SPEC	b	[(N/2)-1] x e
А	0.70	0.80]	T633-2	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF
D	2.90	3.10	1	T833-2	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
E	2.90	3.10	1	T833-3	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
A1	0.00	0.05	1	T1033-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229/WEED-3	0.25±0.05	2.00 REF
L	0.20	0.40	1	T1033-2	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229/WEED-3	0.25±0.05	2.00 REF
k	0.25	MIN.	1	T1433-1	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
A2	0.20	REF.	1	T1433-2	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
2. COPL 3. WARF 4. PACK 5. DRAW 6. "N" 1 7. NUME	ANARITY AGE SH AGE LE ING CO S THE BER OF	′ Shall Iall NG NGTH/F NFORM TOTAL I LEADS	NOT EXC T EXCEEL PACKAGE N S TO JEDI NUMBER (SHOWN A	. ANGLES IN CEED 0.08 m 0.10 mm. WIDTH ARE CO EC M0229, E SF LEADS. ARE FOR REFI RIENTATION R	m. DNSID XCEP EREN	PERED AS S T DIMENSIO CE ONLY.	NS "D2" AN		C(S). ND T1433-1 & T	1433–2.	

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

0
ŏ

_____Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600

© 2006 Maxim Integrated Products

Printed USA

MAXIM is a registered trademark of Maxim Integrated Products.