VARIABLE Q FILTER

DESCRIPTION

The ZXF36L01 is a versatile analog high Q bandpass Many filter applications including: filter. The device contains two sections:

- 1 Variable Q bandpass filter.
- 2 Mixer block.

The basic filter section requires 2 resistors and 2 · capacitors to set the centre frequency. The filter . Instrumentation operates up to a frequency of 150kHz. Two external resistors control filter Q Factor. The Q can be varied up to 50.

The mixer is included to extend the frequency range up to 700kHz and to permit the centre frequency to be . tuned. The local oscillator can be any waveform, . making microprocessor control convenient.

APPLICATIONS

- Audio bandpass and notch •
- Micro controlled frequency •
- Adaptive filtering •
- Sonar and Ultrasonic Systems

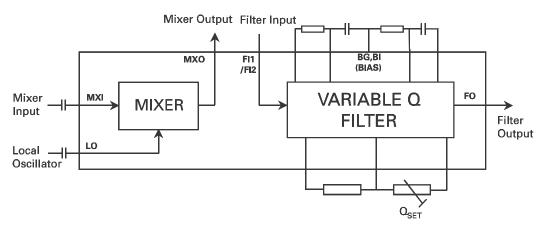
FEATURES AND BENEFITS

- Centre Frequency up to 700kHz ٠
- Tuneable centre frequency
- Variable Q
- Low power
- Standby mode for improved battery life •

ORDERING INFORMATION

PART NUMBER	PACKAGE	PART MARK
ZXF36L01W24	SO24W	ZXF36L01
PART NUMBER	CONTAINER	INCREMENT
ZXF36L01W24TC	Reel 13" 330mm	1000
ZXF36L01W24	Tube	31

SYSTEM DIAGRAM



1



ABSOLUTE MAXIMUM RATINGS

Voltage on any pin	7.0V (relative to Vss)
Operating temperature range	0 to 70°C (de-rated for -40 to 85°C)
Storage temperature	-55 to 125°C

ELECTRICAL CHARACTERISTICS

Test Covditions: Temperature =25°C, V_{DD} = 5.00V, V_{SS} = 0.00V

GENERAL CHARACTERISTICS					
Parameter	Conditions	Min.	Typical	Max.	Units
Operating current	PD= V _{DD}	2.2	3.4	4.5	mA
Shutdown current	$\overline{PD} = V_{SS}$		160	300	μA
IIH (PD)	VIH =5V (WRT V_{SS})			1.0	μA
IIL (PD)	VIL =0V (WRT V _{SS})	-1.0			μA
FILTER CHARACTERISTICS					
Max. operating frequency				150	kHz
Q usable range		0.5		50	
Centre frequency temperature coefficient	Q=30, fo = 1kHz Note 1		10		ppm/°C
Average Q temperature coefficient	Q=30, fo = 1kHz Note 2		0.1		% /°C
Voltage noise	1 – 100 kHz		20		nV/√ Hz
Input impedance		30		50	kΩ
Max. output swing	Output load ≥10 kΩ		1.6		V pk-pk
Output sink current			150		μΑ
Output source current			150		μA
MIXER CHARACTERISTICS					
Max. operating frequency			700		kHz
Maximum signal input			300		mV pk-pk
Maximum Local Oscillator input			100		mV pk-pk
Minimum Local Oscillator input			5		mV pk-pl
Local Oscillator input Impedance			60		Ω

NOTE 1

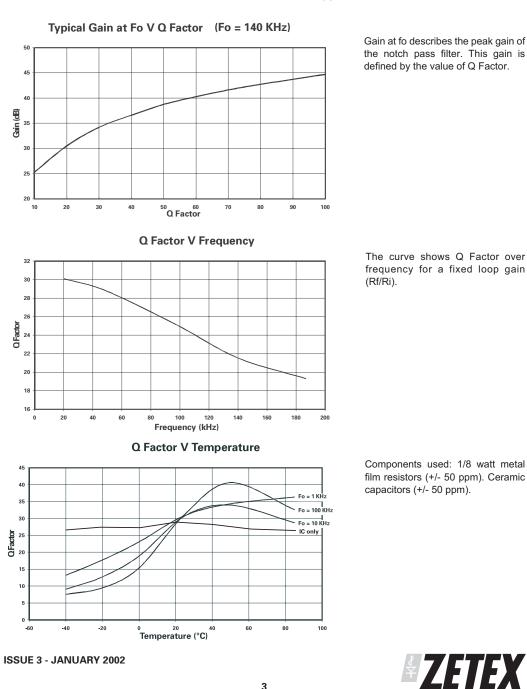
Centre frequency temperature coefficient is dominated by the external R & C components. On chip drift is negligable.

2

Note 2

Average Q temperature coefficient is dominated by the external R components.





TYPICAL ELECTRICAL CHARACTERISTICS Test Covditions: $V_{DD} = 5.00V$, $V_{SS} = 0.00V$

DESCRIPTION OF PIN FUNCTIONS

V _{DD}	Positive supply connection (5 volts). Both pins to be connected. To be decoupled with a 100nF capacitor to $V_{\rm SS}.$
V _{SS}	Negative supply connection; system ground (0 volts). Both pins to be connected.
BG	Bias Generator output. To be decoupled with a 100nF capacitor to $V_{SS}.$
BI	Bias inputs for internal circuitry, both to be connected to BG. (or external supply referenced to $V_{\mbox{SS}}$
PD	Active low. This feature can be used to reduce power consumption for applications that have a standby mode.
FI1,FI2	Filter input, FI1 or FI2 depending on filter configuration.
FO	Filter output for all configurations.
LO	Local Oscillator signal input.
MXI	Mixer signal input.
MXO	Mixer signal output.
C1, RC1	Phase advance network nodes. Values R and C set centre frequency, fo.
R2, RC2	Phase retard network nodes. Values R and C set centre frequency, fo.
GP1,2,3	Loop gain programming nodes.

4

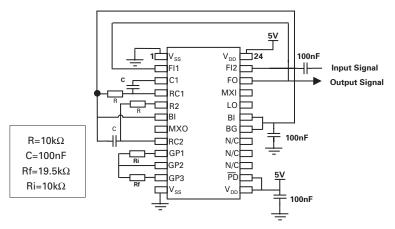
CONNECTION DIAGRAM

			1
1	V _{ss}	V _{DD}	
	FI1	FI2	
	C1	FO	
	RC1	MXI	
	R2	LO	
	BI	BI	
	мхо	BG	
	RC2	N/C	
	GP1	N/C	
	GP2	N/C	
	GP3	PD	
	V _{ss}	V _{DD}	Þ

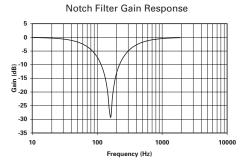


FILTER CONFIGURATIONS AND RESPONSES

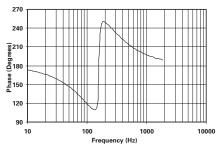
Notch Filter



Filter AC Performance







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 $f_o = \frac{1}{2\pi RC}$ $Q \propto (R_f / R_i)$

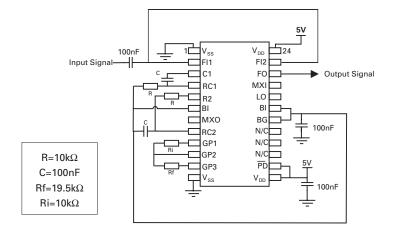
Where R, Ri and Rf ${\geq}10k\Omega$ and C ${\geq}$ 50 pF

See "Designing for a Value of Q" for more details.

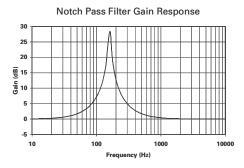
Typical responses for the circuit with component values shown in circuit diagram.

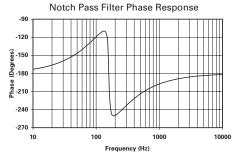


FILTER CONFIGURATIONS AND RESPONSES (continued)



Filter AC Performance





<u>_</u>	1
t _o	$= \frac{1}{2\pi RC}$
0	\propto (R_f / R_i)

6

Where R, Ri and Rf \geq 10k Ω and C \geq 50 pF

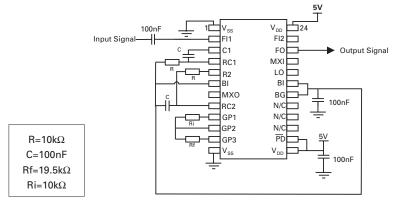
See "Designing for a Value of $\ensuremath{\mathsf{Q}}\xspace$ "for more details.

Typical responses for the circuit with component values shown in circuit diagram.

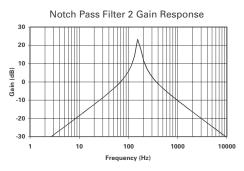


FILTER CONFIGURATIONS AND RESPONSES (continued)

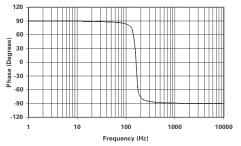
Notch Filter (with attenuating skirts)



Filter AC Performance



Notch Pass Filter 2 Phase Response



£	1
t _o	$= \frac{1}{2\pi RC}$
Q	$\propto (R_f / R_i)$

7

Where R, Ri and Rf \geq 10k Ω and C \geq 50 pF

See "Designing for a Value of $\ensuremath{\mathsf{Q}}\xspace$ for more details.

The skirt 'roll off' away from the peak is -20dB/decade regardless of chosen Q.

Typical responses for the circuit with component values shown in circuit diagram.



DESIGNING FOR A VALUE OF Q

As mentioned on the configuration pages, there is a proportional, but non-linear relationship between the ratio of Rf and Ri, and Ω .

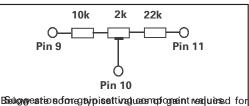
These resistors define the gain of an inverting amplifier that determines the peak value gain and therefore the Q of the filter,Q is defined as:

$$Q = \frac{f_o}{-3dB \ Bandwidth}$$

This value of required gain is critical. As the maximum value of Q is approached, too much gain will cause the filter to oscillate at the centre frequency, fo. A small reduction of gain will cause the value of Q to fall significantly. Therefore, for high values of Q or tight tolerances of lower values of Q, the resistor ratio must be trimmed as shown.

Frequency dependant effects must be accounted for in determining the appropriate gain. As the frequency increases because of internal phase shift effects the effective circuit gain reduces and thus Q Factor reduces.

The frequency effect is not a problem for circuits where the fo remains constant, as the phase shifts are accounted for permanently. For designs where Q is high and fo is to be 'swept', care must be taken that a gain appropriate at the highest frequency does not cause oscillation at the lowest.



several example conditions:

Example1

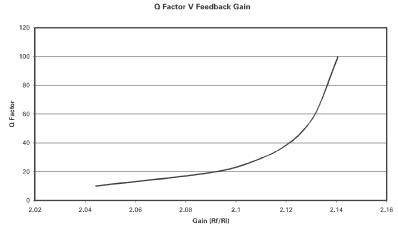
Example2

$$\begin{array}{ll} \mbox{fo} = 140 \mbox{kHz}, & R = 10 \mbox{k}\Omega, \mbox{ C} = 100 \mbox{pF} \\ \mbox{Q=15}, & R \mbox{f/Ri} = 37 \mbox{k}\Omega \mbox{ / } 18 \mbox{k}\Omega => 2.055 \end{array}$$

It can be seen from these examples that the higher Q example actually has a lower inverting amplifier gain. As mentioned before, the frequency will affect the value of gain. The Q Factor v Frequency graph illustrates this effect.

These examples show that the gain required is nominally 2. For the specified range of Q: 0.5 to 50 (values up to 250 are obtainable), the gain values vary from 1.9 to 2.5 correspondingly.

Due to internal gain errors, when the absolute value of Q is increased, the device to device variation in Q will also increase.



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This diagram shows the exponential relationship between gain and Q Factor. (fo = 140 kHz)



FILTERING HIGHER FREQUENCIES USING THE MIXER

Frequencies above 150 kHz cannot be filtered directly; the mixer enables the notch pass filter to function up to 700kHz.

The signal to be filtered is mixed with another frequency (local oscillator), chosen so that the difference (intermediate) frequency equals the filter's centre frequency, fo. The local oscillator signal waveform can be of any shape (sine, square, etc.) but must be approximately 50% duty cycle.

Example

Input frequency = 300 kHz, Local Oscillator (LO) frequency = 250 kHz,

Output (IF) Frequency = 50 kHz.

If the bandwidth of the 50 kHz filter were 1 kHz, the filter's Q factor would be:

50/1 = 50.

The bandwidth of the filter is still 1 kHz when 300 kHz is applied to the mixer's input, but now the Q factor is:

300/1 = 300.

The mixer provides a Q factor improvement equal to the ratio of the input frequency and the intermediate frequency.

The effective centre frequency can also be externally controlled by changing the LO frequency. This allows frequency tuning, trimming or sweeping while employing fixed resistors and capacitors for the filter.

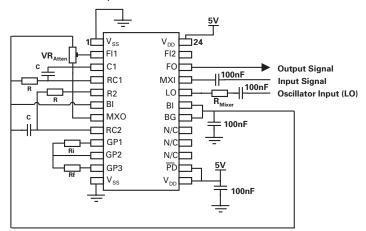
As the LO signal can be a square wave, this allows 'fo' to be controlled using a microcontroller or microprocessor.

MIXER CONFIGURATION WITH NOTCH PASS FILTER (with attenuating skirts)

The mixer can only be used with this filter configuration, as the other types have 0dB stop bands. The mixer output 'MXO' becomes the input of the filter.

As the gain of the notch filter changes with Q, the output of the mixer must be attenuated by some factor (VR_{Atten}). This will prevent the filter from being overdriven and allows the user to set the required output level.

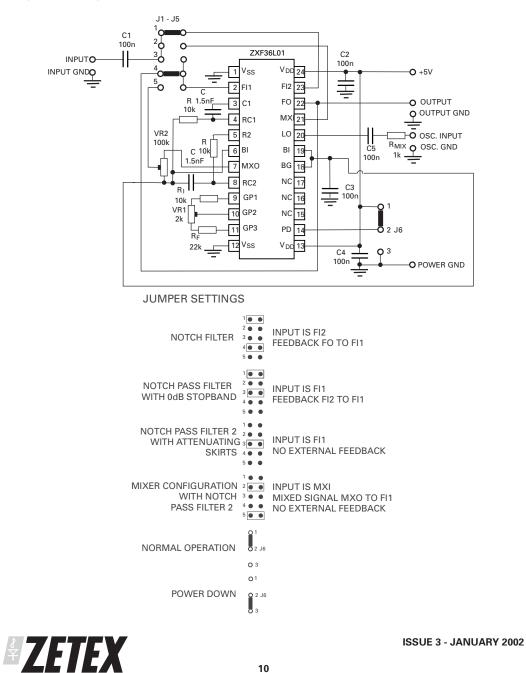
Note: As the local oscillator input, LO has a low input impedance (60 Ω), it will often be necessary to increase it for driving circuitry. As the input voltage required is low (around 5 mV pk-pk min.), a series resistor 'R_{Mitter}' can be inserted. A value of 1 k Ω per 100mV (pk) oscillator signal input will be suitable.

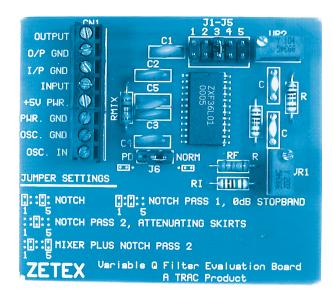


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Application Note

An assembled evaluation PCB is available from Zetex Plc, part code: ZXF36L01-EVB. It provides a fast and easy way of testing the filter configurations mentioned in this datasheet. This board is configured for 10kHz operation.





Evaluation

An evaluation board (ZXF36L01-EVB) is available to assist with in-system or stand-alone performance evaluation. The board can be set, by simple jumper links, to perform any of the filter characteristic responses. The mixer can be selected in conjunction with the notch pass filter 2 functions.

Evaluation boards can be purchased from our catalogue distributors.

Digi-Key North America (www.digikey.com) Tel:1-800344-4539

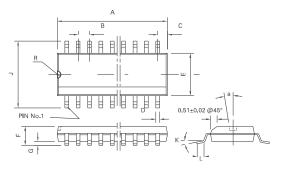
Europe - Farnell (www.farnell.com) Tel:44-113-263-6311





PACKAGE DIMENSION					
DIM	Millimetres		Inches	Inches	
	Min	Max	Min	Max	
А	15.20	15.40	0.598	0.606	
В	1.27	-	0.05	-	
С	0.66	-	0.026	-	
D	0.36	0.46	0.014	0.018	
E	7.40	7.60	0.291	0.299	
F	2.44	2.64	0.096	0.104	
G	0.10	0.30	0.004	0.012	
н	0°	7°	0°	7°	
I	0.23	0.28	0.009	0.011	
J	10.11	10.51	0.398	0.414	
к	0°	8°	0°	8°	
L	0.51	1.01	0.02	0.04	
R	0.63	0.89	0.025	0.035	
а	7°BSC		7°BSC		

PACKAGE OUTLINE



SOIC 24 LEAD

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