

PCN #: 08_0074 Rev. A**Title:**

ADV734x & ADV739x Feature Improvement

Updated Sep'08 - Customers can distinguish new material from old using a branding difference.

Date Published:

September 18, 2008

Type:

Device

Part Number:

ADV734x and ADV739x Video Encoders

Proposed Change:

Silicon Revision with following changes:

1) Improvement to allow correct operation of the part when using external HSYNC/VSYNC signals in interlaced HDTV modes.

Sep'08

Customers can distinguish new material from old using a branding difference.

A new brand designator will be added to all new material using the revised silicon as follows:

Old Material, Line 2: BSTZ (QFP package) or BCPZ (LFCSP package types)

New Material, Line 2: BSTZ-3 or BCPZ-3

The -3 corresponds to the revision of silicon in use. The silicon revcode can also be read back via software.

Reason for Change:

When using the ADV734x and ADV739x encoders in HD interlaced timing modes, with external HSYNC and VSYNC signals, the first active pixel of the video output is incorrectly blanked.

When using 4:2:2 input formats, the Pb and Pr output signals may also become inverted (swapped).

On the new version of silicon, by programming register 0x02, Bit 0, to high (Logic '1'), the user can enable the correct output waveforms without any side-effects.

This register write does not have to be made if the user does not use any of the modes described here.

Sep'08

To distinguish new material from old using a branding difference.

Summary of Supporting Information:

Electrical Characterisation has been performed and data is available upon request

Planned Date Change Effective:

December 16, 2008

EIA Date Code:

0851

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