

Features

Built-in input threshold hysteresis

Independent channel strobes

■ Single 5.0V supply operation

■ High speed ... typical propagation delay time 20 ns

■ Plug-in replacement for the SN75124 and the 8T24

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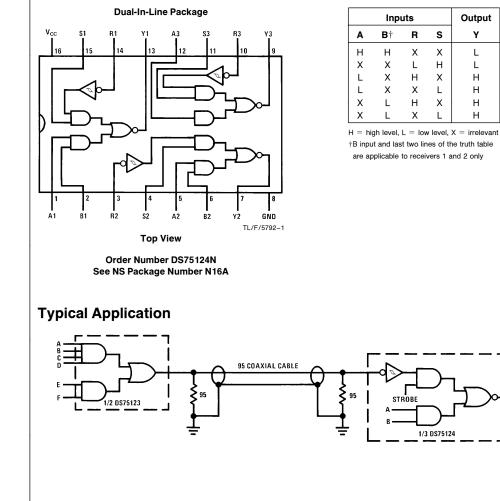
Input gating increases application flexibility

DS75124 Triple Line Receiver

General Description

The DS75124 is designed to meet the input/output interface specifications for IBM System 360. It has built-in hysteresis on one input on each of the three receivers to provide large noise margin. The other inputs on each receiver are in a standard TTL configuration. The DS75124 is compatible with standard TTL logic and supply voltage levels.

Connection Diagram and Truth Table



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TL/F/5792-2

DS75124 Triple Line Receiver

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Absolute Maximum Ratings (Note 1)

Input Voltage

R Input with V_{CC} Applied

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. Supply Voltage, V_{CC} 7.0V

 Operating Temperature Range
 0°C to +75°C

 Storage Temperature Range
 -65°C to +150°C

 Lead Temperature (Soldering, 4 seconds)
 260°C

 *Derate molded package 10.9 mW/°C above 25°C.
 260°C

Operating Conditions

R Input with V _{CC} not Applied	6.0V		Min	Мах	Units	
A, B, or S Input	5.5V	Supply Voltage, V _{CC}	4.75	5.25	V	
Output Voltage	7.0V	High Level Output Current,		-800	μÂ	
Output Current	\pm 100 mA	IOH				
Maximum Power Dissipation* at 25°C		Low Level Output Current,		16	mA	
Molded Package	1362 mW	IOL	_			
		Operating Temperature, T _A	0	+75	°C	

7.0V

Electrical Characteristics (Notes 2 and 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _{IH}	High Level Input Voltage	A, B, or S	2.0			V
		R	1.7			V
V _{IL}	Low Level Input Voltage	A, B, or S			0.8	V
		R			0.8	V
$v_{T+}\!-\!v_{T-}$	Hysteresis	V _{CC} = 5.0V, T _A = 25°C, R, (Note 6)		0.4		V
VI	Input Clamp Voltage	$V_{CC}=$ 5.0V, $I_{I}=-$ 12 mA, A, B, or S			-1.5	V
lj –	Input Current at Maximum	$V_{CC}=$ 5.25V, $V_{IN}=$ 5.5V, A, B, or S			1	mA
Input Voltage	Input Voltage	R V _I = 7.0V			5.0	mA
		$V_{I} = 6.0V, V_{CC} = 0V$			5.0	mA
V _{OH}	High Level Output Voltage	$V_{IH} = V_{IHMIN}, V_{IL} = V_{ILMAX},$ $I_{OH} = -800 \ \mu A$, (Note 4)	2.6			v
V _{OL}	Low Level Output Voltage	$V_{IH} = V_{IHMIN}, V_{IL} = V_{ILMAX}, I_{OL} = 16 \text{ mA}, \label{eq:VIH}$ (Note 4)			0.4	v
IIH	High Level Input Current	$V_{I} = 4.5V, A, B, or S$			40	μA
		V _I = 3.11V, R			170	μA
I _{IL}	Low Level Input Current	$V_{I} = 0.4V$, A, B, or S	-0.1		- 1.6	mA
I _{OS}	Short Circuit Output Current	$V_{CC} = 5.0V, T_A = 25^{\circ}C$, (Note 5)	-50		-100	mA
ICC	Supply Current	$V_{CC} = 5.25V$			72	mA

Switching Characteristics $T_A = 25^{\circ}C$, nominal power supplies unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{PLH}	Propagation Delay Time, Low-to-High Level Output from R Input	(See AC Test Circuit and Switching Time Waveforms)		20	30	ns
t _{PHL}	Propagation Delay Time, High-to-Low Level Output from R Input	(See AC Test Circuit and Switching Time Waveforms)		20	30	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

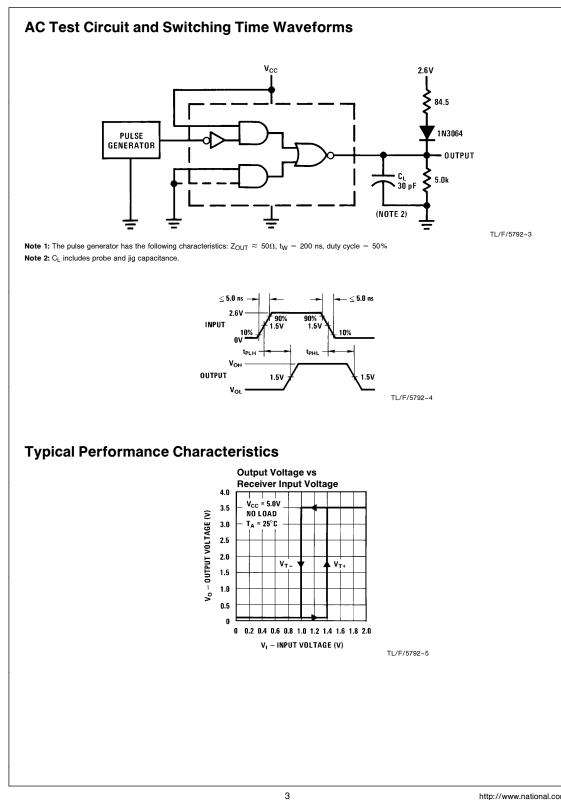
Note 2: All currents into device pins are shown as positive, currents out of device pins shown as negative, all voltage values are referenced with respect to network ground terminal, unless otherwise noted. All values shown as max or min on absolute value basis.

Note 3: Min/max limits apply across the guaranteed operating temperature range of 0°C to $+75^{\circ}$ C for DS75124, unless otherwise specified. Typicals are for V_{CC} = 5.0V, T_A = 25°C. Positive current is defined as current into the referenced pin.

Note 4: The output voltage and current limits are guaranteed for any appropriate combination of high and low inputs specified by the truth table for the desired output.

Note 5: Not more than one output should be shorted at a time.

Note 6: Hysteresis is the difference between the positive going input threshold voltage, V_{T+}, and the negative going input threshold voltage, V_{T-}.



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