

February 1995

# MM5369 17 Stage Oscillator/Divider

### **General Description**

The MM5369 is a CMOS integrated circuit with 17 binary divider stages that can be used to generate a precise reference from commonly available high frequency quartz crystals. An internal pulse is generated by mask programming the combinations of stages 1 through 4, 16 and 17 to set or reset the individual stages. The MM5369 is advanced one count on the positive transition of each clock pulse. Two buffered outputs are available: the cyrstal frequency for tuning purposes and the 17th stage output. The MM5369 is available in an 8-lead dual-in-line epoxy package.

#### **Features**

- Crystal oscillator
- Two buffered outputs Output 1 crystal frequency Output 2 full division
- High speed (4 MHz at V<sub>DD</sub> = 10V)
- Wide supply range 3V-15V
- Low power
- Fully static operation
- 8-lead dual-in-line package
- Low Current

# Option

■ MM5369AA

3.58 MHz to 60 Hz

### **Connection and Block Diagrams Dual-In-Line Package** OSC OUT TUNER OUTPUT OSC OUT OSC IN OSC IN DIVIDER 17 STAGE DIVIDER TUNER BUFFER OUTPUT RESET PULSE GENERATOR $\mathbf{v}_{\mathrm{SS}}$ $\Lambda^{DD}$ TL/F/10820-2 FIGURE 2 DIVIDER TUNER $V_{DD}$ OUTPUT OSC OUT OUTPUT DIVIDER TL/F/10820-1 **Top View** Order Number MM5369AA/N See NS Package Number N08Ĕ $v_{\rm SS}$ NC OSC IN TL/F/10820-8 Order Number MM5369AA/M See NS Package Number M14A

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TL/F/10820

RRD-B30M66/Printed in U. S. A.

# **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Voltage at Any Pin -0.3V to  $V_{DD} + 0.3$ V Operating Temperature  $0^{\circ}$ C to  $+70^{\circ}$ C Storage Temperature  $-65^{\circ}$ C to  $+150^{\circ}$ C

Package Dissipation 500 mW

Maximum V<sub>CC</sub> Voltage 16V

Operating V<sub>CC</sub> Range 3V to 15V

Lead Temperature (Soldering, 10 seconds) 300°C

#### **Electrical Characteristics**

 $T_A$  within operating temperature range,  $V_{SS} = GND$ ,  $3V \le V_{DD} \le 15V$  unless otherwise specified.

Parameter	Conditions	Min	Тур	Max	Units
Quiescent Current Drain	$V_{DD} = 15V$			10	μΑ
Operating Current Drain	$V_{DD} = 10V, f_{IN} = 4.19  MHz$		1.2	2.5	mA
Frequency of Oscillation	$V_{DD} = 10V$ $V_{DD} = 6V$	DC DC		4.5 2	MHz MHz
Output Current Levels	$V_{DD} = 10V$ $V_{O} = 5V$				
Logical "1" Source Logical "0" Sink	-	500 500			μA μA
Output Voltage Levels	$V_{DD} = 10V$ $I_{O} = 10 \mu A$				
Logical "1" Logical "0"		9.0		1.0	V V

Note: For 3.58 MHz operation,  $V_{DD}$  must be  $\geq$  10V.

## **Functional Description**

A connection diagram for the MM5369 is shown in *Figure 1* and a block diagram is shown in *Figure 2*.

#### TIME BASE

A precision time base is provided by the interconnection of a 3,579,545 Hz quartz crystal and the RC network shown in Figure 3 together with the CMOS inverter/amplifier provided between the OSC IN and the OSC OUT terminals. Resistor R1 is necessary to bias the inverter for class A amplifier operation. Capacitors C1 and C2 in series provide the parallel load capacitance required for precise tuning of the quartz crystal.

The network shown provides > 100 ppm tuning range when used with standard crystals trimmed for  $C_L=12$  pF. Tuning to better than  $\pm 2$  ppm is easily obtainable.

#### DIVIDER

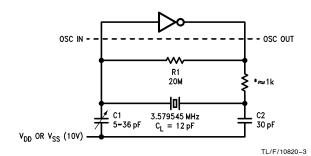
A pulse is genertaed when divider stages 1 through 4, 16 and 17 are in the correct state. By mask options, this pulse is used to set or reset individual stages of the counter. *Figure 4* shows the relationship between the duty cycle and the programmed modulus.

#### **OUTPUTS**

The Tuner Output is a buffered output at the crystal oscillator frequency. This output is provided so that the crystal frequency can be obtained without disturbing the crystal oscillator. The Divide Output is the input frequency divided by the mask programmed number. Both outputs are push-pull outputs.

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# Functional Description (Continued)



\*To be selected based on xtal used

FIGURE 3. Crystal Oscillator Network

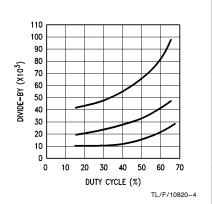


FIGURE 4. Plot of Divide-By vs Duty Cycle

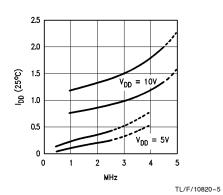
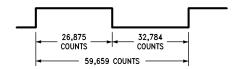
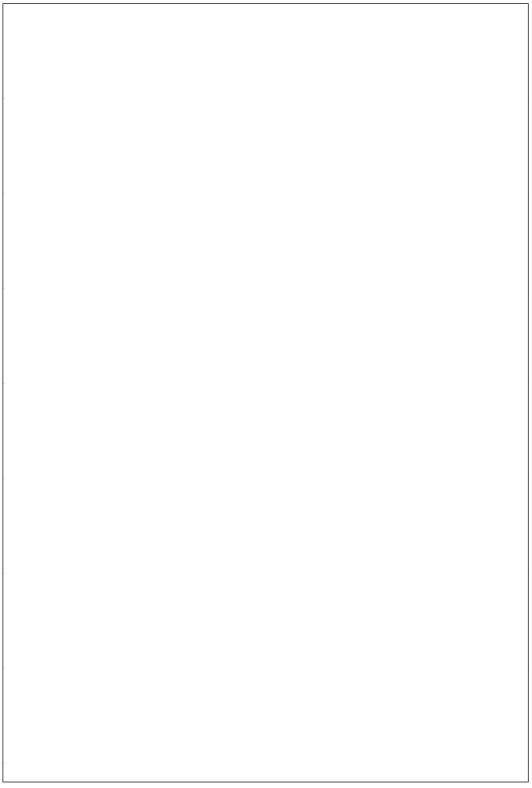


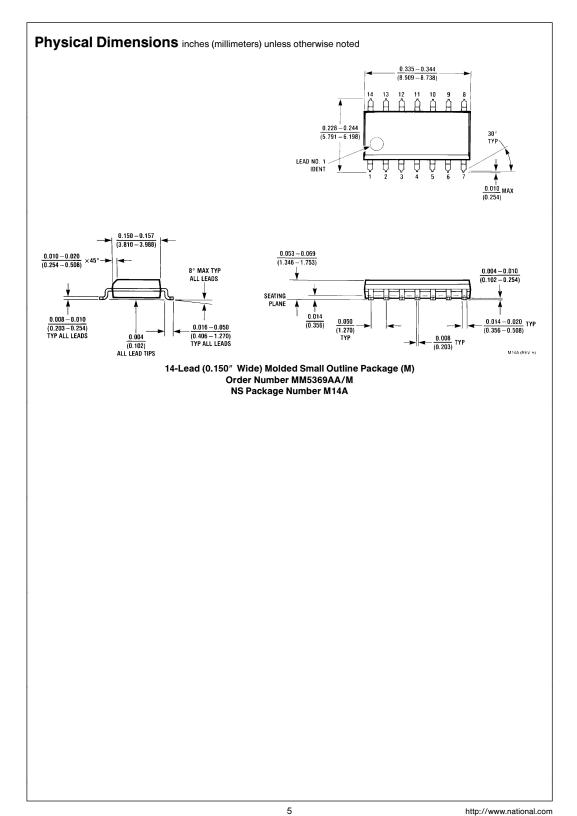
FIGURE 5. Typical Current Drain vs Oscillator Frequency



 $$^{\rm TL/F/10820-6}$$  FIGURE 6. Output Waveform for the MM5369AA

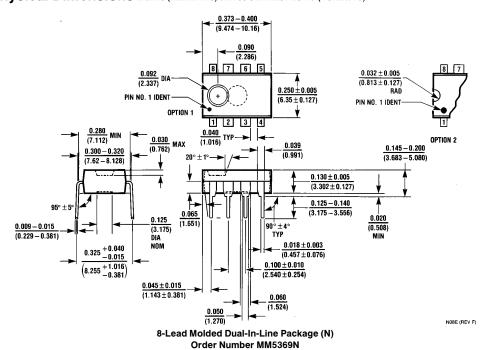


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# Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



NS Package Number N08E

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