

## Product/Process Change Notice - PCN 09\_0194 Rev. A

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

**PCN Title:** DAC8412/3 redesign and fab process change

**Publication Date:** 01-Oct-2009

Samples Available Date:

Effectivity Date: 01-Oct-2009 (the earliest date that a customer could expect to receive changed material)

## **Description Of Change**

In November 2006 ADI sent customers PCN 06\_0126 to advise of the redesign and transfer of the DAC8412/3 commercial devices from the LVCMOS wafer fab process at ADSiV to the H6 wafer fab process at Analog Devices, Limerick Ireland.

As a result of this redesign and process change the following datasheet specifications have changed:

From: For single supply operation only (Vrefl=0.0V, Vss=0.0V): Due to internal offset errors, INL and DNL are measured beginning at code 2 (002H).

To: For For single supply operation only (Vrefl=0.0V, Vss=0.0V): Due to internal offset errors, INL and DNL are measured beginning at code 5 (005H).

The maximum rating on the Vlogic pin is changed from +18V maximum to +7V maximum. A +7V maximum rating for this pin has been specified in the datasheet since Revision D (March 2000). Up until the redesign and process change the Vlogic pin could tolerate voltages up to +18V maximum. Since the redesign and process change, the Vlogic pin can only tolerate voltages up to +7V maximum.

The smaller geometry of the newer process onto which the DAC8412/3 were transferred means that digital interface propogation delays are greatly reduced allowing the devices to react to fast glitches, glitches which would have previous to the redesign and process change been ignored by the devices.

## Reason For Change

Specifications changed as a result of redesign and process transfer.

## Impact of the change (positive or negative) on fit, form, function & reliability

Some customers may need to revise their system for successful operation.

**Summary of Supporting Information** (View Qual Templates For PCNs)

These changes are reflected in Rev. F of the DAC8412/3 data sheet.

Supporting Documents None

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

 Americas:
 PCN\_Americas@analog.com
 Europe:
 PCN\_Europe@analog.com
 Japan:
 PCN\_Japan@analog.com

Rest of Asia: PCN\_ROA@analog.com

Appendix A - Affected ADI Models						
Existing Parts - Product Family / Model Number (17)						
DAC8412 / DAC8412EP	DAC8412 / DAC8412EPZ	DAC8412 / DAC8412FP	DAC8412 / DAC8412FPC	DAC8412 / DAC8412FPC-REEL	DAC8412 / DAC8412FPCZ	
DAC8412 / DAC8412FPCZ-REEL	DAC8412 / DAC8412FPZ	DAC8413 / DAC8413EP	DAC8413 / DAC8413EPZ	DAC8413 / DAC8413FP	DAC8413 / DAC8413FPC	
DAC8413 / DAC8413FPC-REEL	DAC8413 / DAC8413FPCZ	DAC8413 / DAC8413FPCZ-REEL	DAC8413 / DAC8413FPZ	DAC8413 / DAC8413GBC		

Appendix B - Revision History				
Rev	Publish Date	Rev Description		
Rev	07-Sep-2009	Initial Release		
Rev. A	01-Oct-2009	Revision A issued to remove 06_0126 PCN revision referenced incorrectly on original 09_0194 PCN		

Analog Devices, Inc.

DocId:869 Parent DocId:None