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953201BFLF

Category: Desktop Chipsets

Generic Part: 953201

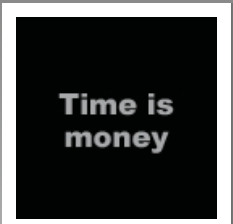
Market Group: PC CLOCK

Description: PC MAIN CLOCK

The ICS953201 is part of a whole new line of ICS clock generators and buffers called TCH₂ (Timing Control Hub). This part incorporates ICS's newest clock technology which offers more robust features and functionality. Employing the use of a serially programmable I2C interface, this device can adjust the output clocks by configuring the frequency setting, the output divider ratios, selecting the ideal spread percentage, the output skew, the output strength, and enabling/disabling each individual output clock. M/N control can configure output frequency with resolution up to 0.1MHz increment.

Output Features:

- 2 - Differential pair push-pull CPU clocks @ 3.3V
- 6 - PCI-Ex differential pairs
- 6 - PCICLK @ 3.3V
- 3 - AGPCLK @ 3.3V
- 1 - 48MHz @ 3.3V fixed.
- 1 - 24/48MHz @ 3.3V
- 2 - REF @ 3.3V, 14.318MHz



Parameters

| | |
|-------------------------|-----------------|
| Package | SSOP 56 (PVG56) |
| Voltage | 3.3 V |
| Package | SSOP 56 |
| Speed | NA |
| Temperature | C |
| Status | Active |
| Sample | Yes |
| Minimum Order Quantity | 130 |
| Factory Order Increment | 26 |

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

| Type | Title | Size | Revision Date |
|------|------------------------|-------|---------------|
| Misc | PC Clocks Contact Info | 61 KB | 05/29/2007 |

Package

| | |
|----------------------------------|--------------------------|
| Description | SSOP 300 MIL, 56LD |
| Class | PLASTIC |
| Moisture Sensitivity Level (MSL) | 1 |
| Category | Green |
| Moisture Exposure Floor Life | Unlimited @ <30°C/85% RH |
| Peak Reflow Temperature | 260°C |
| Rebake Conditions | N/A |
| Length | 18.4 |
| Mark | G |
| Width | 7.5 |
| Pitch | 0.64 |
| Thickness | 2.3 |
| Status | Active |