## omROn

## Digital Timer

## Easy-to-see and Easy-to-operate <br> DIN $48 \times 48$ mm Digital Timer with <br> IP66/NEMA 4 Protection

■ Water- and dust-protected for severe environments.

- Large, high-visibility LED displays with a height of 12 mm .
- Simple setting with Increment and Decrement Keys.
- Conforms to EMC standards.


■ Six-language instruction manual provided.

## Ordering Information

| Outputs | Supply voltage | Model |  |
| :--- | :--- | :--- | :--- |
|  |  | Without Finger Safe Terminal Cover | With Finger Safe Terminal Cover |
| Contact output | 100 to 240 VAC | H5CL-A | --- |
|  | 12 to 24 VDC | H5CL-AD | H5CL-AD-500 |
| Transistor output <br> (Photocoupler) | 100 to 240 VAC | H5CL-AS | --- |
|  | 12 to 24 VDC | H5CL-ADS | --- |

Model Number Legend:
H5CL-A $\frac{\square}{1} \frac{\square}{3}$

1. Fixed
2. D: DC Supply Voltage
3. S: Transistor output

## Accessories (Order Separately)

| Name |  | Model |
| :--- | :--- | :--- |
| Hard cover | --- | Y92A-48 |
| Soft cover |  | Finger safe type |
| Track Mounting/Front Connecting Socket (for AC <br> models only) | --- | P2CF-11 |
| Back Connecting Socket (for AC models only) | Finger safe type | P3CF-11-E |
| Finger Safe Terminal Cover for DC models |  | P3GA-11 with Y92A-48G (see note 1) |
| Flush Mounting Adapter (see note 2) | Y92A-48T |  |

Note: 1. Y92A-48G is a finger safe terminal cover which is attached to the P3GA-11 Socket.
2. Supplied with each Unit.

## Specifications

| Item | H5CL-A $\square$ (AC models) | H5CL-AD $\square$ (DC models) |
| :--- | :--- | :--- |
| Classification | Digital timer | Flush mounting |
| Mounting | DIN track, surface, and flush mounting <br> (common) |  |
| External connections | Socket | Screw terminals |
| Enclosure ratings | Panel surface: IEC IP66 and NEMA Type 4 (indoors) when Y92S-29 rubber packing is used. |  |
| Digits | 4 digits (zero suppress method) |  |
| Max. time settings | 9.999 s (0.001-s unit), 99.99 s (0.01-s unit), 999.9 s (0.1-s unit), 9999 s (1-s unit), <br> 99 min 59 s (1-s unit), 999.9 min (0.1-min unit), 99 h 59 min (1-min unit), 999.9 h (0.1-h unit) |  |
| Display modes | Up (increment) and Down (decrement) (selectable) |  |
| Input signals | Start, gate, reset, and key protection |  |
| Input method | No-voltage input: via NPN transistor or switching of contact |  |
| Operating modes | A (signal ON-delay), F (accumulative operation) (selectable) |  |
| Reset system | Power reset (A (signal ON-delay) mode only), external, manual resets |  |
| Sensor waiting time | $216 ~ m s ~ t y p ., ~ 250 ~ m s ~ m a x . ~(C o n t r o l ~ o u t p u t ~ i s ~ t u r n e d ~ O F F ~ a n d ~ n o ~ i n p u t ~ i s ~ a c c e p t e d ~ d u r i n g ~$ <br> sensor waiting time.) |  |
| External power supply | 50 mA at 12 VDC ( $\pm 10 \%)$ |  |
| Display | $7-s e g m e n t ~ L E D s ~(12 ~ m m ~ h i g h, ~ r e d ~ L E D s ~ f o r ~ t h e ~ p r e s e n t ~ v a l u e, ~ a n d ~ 8 ~ m m ~ h i g h, ~ g r e e n ~ L E D s ~ f o r ~$ <br> the set value) |  |
| Memory backup | EEP-ROM (overwritten 200,000 times min.), which can store data for 20 years min. |  |
| Mounting method | DIN track mounting, surface mounting, and <br> flush mounting | Flush mounting |
| Approved standards | UL 508, CSA C22.2 No.14 <br> Conforms to EN61010-1 |  |

## ■ Ratings

| Item | H5CL-A $\square$ (AC models) | H5CL-AD $\square$ (DC models) |
| :---: | :---: | :---: |
| Rated supply voltage | 100 to 240 VAC, $50 / 60 \mathrm{~Hz}$ | 12 to 24 VDC (permissible ripple: 20\% (p-p) max.) |
| Operating voltage range | 85 to 264 VAC, $50 / 60 \mathrm{~Hz}$ | 10.8 to 26.4 VDC |
| Power consumption | Approx. 10 VA | Approx. 3 W |
| Start, reset, gate inputs | Min. pulse width: $1 \mathrm{~ms} / 20 \mathrm{~ms}$ (selectable, same for all three inputs) |  |
| Key protection input | Response time: 1 s max. |  |
| Power reset | Min. power opening time: 0.5 s (excluding F (accumulative operation) mode) |  |
| Control output | Contact output: SPDT, 3 A at 250 VAC, resistive load ( $\cos \phi=1$ ) <br> Min. applicable load: 10 mA at $5 / 24 \mathrm{VDC} \mathrm{( } \mathrm{P}$ level, for reference value) <br> Transistor output: NPN open collector, 100 mA max. at 30 VDC max., <br> residual voltage 1.5 VDC max. |  |

## - Characteristics

| Item | H5CL-A $\square$ (AC models) | H5CL-AD $\square$ (DC models) |
| :---: | :---: | :---: |
| Deviation of operating time and setting error (including temperature and voltage influences) | Power start: $\pm 0.01 \% \pm 0.05 \mathrm{~s}$ max. (see note 1 ) <br> Signal start: $\pm 0.005 \% \pm 0.03$ s max. (see note 1) <br> Signal start, at transistor output model: $\pm 0.005 \% \pm 3 \mathrm{~ms}$ max. (see note 1 and 2) <br> If the set value is within the sensor waiting time ( 250 ms max.) in the case of power start, the control output of the H5CL will not be turned ON until the sensor waiting time passes. |  |
| Insulation resistance | $100 \mathrm{M} \Omega$ min. (at 500 VDC) (between current-carrying terminal and exposed non-current-carrying metal parts, and between non-continuous contacts) |  |
| Dielectric strength | 2,000 VAC, $50 / 60 \mathrm{~Hz}$ for 1 min (between current-carrying terminal and exposed non-current-carrying metal parts) $1,000 \mathrm{VAC}, 50 / 60 \mathrm{~Hz}$ for 1 min (between non-continuous contacts) | $1,000 \mathrm{VAC}, 50 / 60 \mathrm{~Hz}$ for 1 min (between current-carrying terminal and exposed non-current-carrying metal parts, and between non-continuous contacts) |
| Impulse withstand voltage | 3.0 kV (between power terminals) <br> 4.5 kV (between current-carrying terminal and <br> exposed non-current-carrying metal parts) | 1.0 kV (between power terminals) 1.5 kV (between current-carrying terminal and exposed non-current-carrying metal parts) |
| Noise immunity | $\pm 1.5 \mathrm{kV}$ (between power terminals) $\pm 600 \mathrm{~V}$ (between input terminals), square-wave noise by noise simulator (pulse width: $100 \mathrm{~ns} / 1 \mu \mathrm{~s}$, 1 -ns rise) | $\pm 480 \mathrm{kV}$ (between power terminals) $\pm 600 \mathrm{~V}$ (between input terminals), square-wave noise by noise simulator (pulse width: $100 \mathrm{~ns} / 1 \mu \mathrm{~s}, 1$-ns rise) |
| Static immunity | Destruction: 15 kV <br> Malfunction: 8 kV |  |
| Vibration resistance | Destruction: 10 to $55 \mathrm{~Hz}, 0.75-\mathrm{mm}$ single amplitude each in three directions Malfunction: 10 to $55 \mathrm{~Hz}, 0.5-\mathrm{mm}$ single amplitude each in three directions |  |
| Shock resistance | Destruction: $294 \mathrm{~m} / \mathrm{s}^{2}$ (30G) each in three directions Malfunction: $\quad 98 \mathrm{~m} / \mathrm{s}^{2}$ (10G) each in three directions |  |
| Ambient temperature | Operating: -10 to $55^{\circ} \mathrm{C}\left(-10\right.$ to $50^{\circ} \mathrm{C}$ if timers are mounted side by side) (with no icing) Storage: $\quad-25$ to $65^{\circ} \mathrm{C}$ (with no icing) |  |
| Ambient humidity | Operating: 35\% to 85\% |  |
| Life expectancy | Mechanical: $10,000,000$ operations min. <br> Electrical: 100,000 operations min. (3 at 250 VAC, resistive load) |  |
| EMC |  |  |
| Case color | Light gray (Munsell 5Y7/1) |  |
| Weight | Approx. 130 g | Approx. 110 g |

Note: 1. The values are based on the set value.
2. The value is applied for a minimum pulse width of 1 ms .

## Nomenclature

## Indicator

1. Present Value

Red LEDs with a character height of 12 mm
Note: The decimal point will flash on the present value during the timing operation in the following ranges: 0.1 to $999.9 \mathrm{~min}, 0 \mathrm{~h} 01 \mathrm{~min}$ to 99 h 59 min , and 0.1 to 999.9 h.
2. Preset Value

Green LEDs with a character height of 8 mm
3. Reset Indicator (orange)
4. Key Protection Indicator (orange)
5. Time Unit Display (orange)
6. Control Output Indicator (orange)

## Operation Key

7. Reset (RST) Key The RST Key initializes the present value and control output.
8. Increment Keys (1 to 4)

Up Keys 1 to 4 increment the preset value.
9. Decrement Keys (1 to 4)

Down Keys 1 to 4 decrement the preset value.

## Operation

## ■ DIP Switch Setting

| Pin no. | Item | OFF | ON |
| :---: | :--- | :--- | :--- |
| $\mathbf{1 , 2 , 3}$ | Time ranges | See table below. | Down (Decrement) |
| $\mathbf{4}$ | Display modes | Up (Increment) | 1 ms |
| $\mathbf{5}$ | Min. pulse width of inputs | 20 ms | F (accumulative operation) |
| $\mathbf{6}$ | Operating modes | A (signal ON-delay) |  |

Note: Set the DIP switch before installation and operation of the Unit. DIP switch setting changes are not effective while the power is on.
Time Ranges

| $\mathbf{1}$ | $\mathbf{2}$ | $\mathbf{3}$ |  |
| :--- | :--- | :--- | :--- |
| ON | ON | ON | 0.001 to 9.999 s |
| OFF | OFF | OFF | 0.01 to 99.99 s |
| ON | OFF | OFF | 0.1 to 999.9 s |
| OFF | ON | OFF | 1 to 9999 s |
| ON | ON | OFF | 0 min 01 s to 99 min 59 s |
| OFF | OFF | ON | 0.1 to 999.9 min |
| ON | OFF | ON | 0 h 01 min to 99 h 59 min |
| OFF | ON | ON | 0.1 to 999.9 h |

Note: Switches 1 to 6 are all set to OFF before shipping.

(The same switch settings apply to AC and DC models)

Timer Control with Power Start
When using the H5CL with power start, short-circuit the start input and input 0-V terminals.

## ■ Operating Modes

A Mode
Signal ON-delay


F Mode
Accumulative Operation


## Dimensions

Note: All units are in millimeters unless otherwise indicated.

```
H5CL-A}
```

DIN Track/Surface/Flush Mounting


H5CL-AD $\square$
Flush Mounting


H5CL-A $\square$
With Flush Mounting Adapter


H5CL-AD $\square$
With Flush Mounting Adapter


## Panel Cutouts

Panel cutouts are as shown below. (according to DIN43700).

$A=(48 n-2.5){ }_{-0}^{+1}$
Note: 1. The mounting panel thickness should be 1 to 4 mm .
2. It is possible to mount timers side by side, but only in one direction.
3. When the Timers are mounted closely side by side, the Timers will not be water-resistive.

DIN Track Mounting


H5CL-AD $\square$-500
The cover conforms to finger protection standard against electric shock. (VDE 0106/P100)


## ■ Accessories (Order Separately)

Track Mounting/

## Front Connecting Socket

## P2CF-11



P2CF-11-E (Finger Safe Terminal Type)
Conforming to VDE0106/P100


Back Connecting Socket P3GA-11


Terminal Arrangement/ Internal Connections (Bottom View)


Finger Safe Terminal Cover
Conforming to VDE0106/P100
Y92A-48G
(Attachment for P3GA-11 Socket)


Y92A-48T
(Attachment for H5CL DC models)


## Hard Cover Y92A-48

## Soft Cover <br> Y92A-48F1



## Installation

## ■ Terminal Arrangement

AC Models


DC Models


Note: 1 and 6 are connected to each other internally.

## - Input Circuits



## - Input Connections

Open Collector Output


Voltage Output


Contact input

Start, Reset, and Gate Input Specification
ON impedance:
ON residual voltage:
$500 \Omega \max$. (the leakage current is 5 to 20 mA when the impedance is $0 \Omega$.)
OFF impedance: 2 V max.
$100 \mathrm{k} \Omega \mathrm{min}$.
Maximum applicable voltage: 30 VDC max.

Two-wire Sensor


Applicable Two-wire Sensor
Leakage current: 1.5 mA max.
Switching capacity: 5 mA min.
Residual voltage: 3 V max.
Operating voltage: 10 VDC
Note: When connecting a two-wire sensor to a DC models, supply 24 VDC ( 21.6 to 26.4 VDC ) to the timer.

Key Protection Input


Key Protection Input
ON impedance: $\quad 1 \mathrm{k} \Omega$ max.
(the leakage current is approx. 1 mA when the impedance is $0 \Omega$.)
ON residual voltage: $\quad 0.5 \mathrm{~V}$ max.
OFF impedance: $\quad 100 \mathrm{k} \Omega \mathrm{min}$.
Maximum applicable voltage: 30 VDC max.
Note: The used contact should switch 1 mA at 5 V .

## Precautions

## Power Supplies

When turning the power ON and OFF, input signal reception is possible, unstable, or impossible as shown in the diagram below.


Apply the power supply voltage through a relay or switch in such a way that the voltage reaches a fixed value immediately.
Turn the power ON and OFF with relay with a rated capacity of 10 A minimum to prevent contact deterioration due to inrush current caused by turning the power ON and OFF.

## Timer Control with Power Start

The timer cannot take measurements until 160 to 250 ms have elapsed after the power is turned ON (refer to the above chart). The control output will be delayed for any set value less than 250 ms .
When the H5CL is used with power start in F mode (i.e., accumulative operation with output on hold), there will be a timer error (approximately 100 ms each time the H5CL is turned on) due to the characteristics of the internal circuitry.
Use the H5CL with signal start if timer accuracy is required.

## Transistor Output

The transistor output of the H 5 CL is insulated from the internal circuitry by a photocoupler, so the transistor output can be used as both NPN and PNP output.
The diode connected to the collector of the output transistor is used to absorb inverted voltage that is generated when an inductive load is connected to the H5CL.


Power for load


Power for load

## Self-diagnostic Function

The following displays will appear if an error occurs.

| Display | Error | Output <br> status | Correction | Set value <br> after <br> correction |
| :--- | :--- | :--- | :--- | :--- |
| $E i$ | CPU | OFF | Press RST <br> Key or turn <br> power off <br> and then <br> ON | No change |
| $E Z$ | Memory <br> (see note) |  |  | 0 |
|  |  |  |  |  |

Note: This includes times when the life of the EEPROM has expired.

## Operating Environment

When using the Timer in an area with excess electronic noise, separate the Timer, wiring, and the equipment which generates the input signals as far as possible from the noise sources. It is also recommended to shield the input signal wiring to prevent electronic interference.
Organic solvents (such as paint thinner), as well as very acidic or basic solutions can damage the outer casing of the Timer.

## Preset Value Change

When changing the preset value during a timing operation, output will turn ON if the preset value is changed as follows, since the constant read-in system is in use:
Display mode UP: Present value $\geq$ preset value
Display mode DOWN: Elapse time $\geq$ preset value
(Present value $=0$ )
Note: When in down mode, the changed amount of preset value is added to or subtracted from the present value.

## Reset with a Preset Value of 0

The output will go ON when the start signal is input. The output will be OFF while the reset key is pressed or the reset input is ON.

## Power Failure Backup

All data is stored in the EEPROM when there is power failure. The EEPROM can be overwritten more than 200,000 times.

| Operating <br> mode | Overwriting timing |
| :--- | :--- |
| A mode | When the H5CL is turned off after changing <br> the set value. |
| F mode | When the H5CL is turned off after changing <br> the set value, turning the start input, or the <br> reset input ON. |

## Flush Mounting

The H5CL's panel surface is water-resistive (conforming to NEMA 4 (indoors) and IP66). In order to prevent the internal circuit from water penetration through the space between the timer and operating panel, attach a rubber packing (provided with the H5CL) between the timer and operating panel and secure the rubber packing with the Y92F-30 flush-mounting adapter.


## Other

In case of performing a dielectric strength test, etc., on the H5CL mounted to a control panel, disconnect the H5CL from the connecting circuit, or short-circuit all the terminals of the H5CL.
Otherwise the H5CL may be damaged.
Terminal 1 (power supply terminal) and terminal 6 (input common: 0 V for input) of DC model H5CL are internally connected to each other.

## DIP Switch Selection

DIP switch setting while the H5CL is turned on will not be valid until the H 5 CL is turned off and on.

Cat. No. L085-E1-3 In the interest of product improvement, specifications are subject to change without notice.

## OMRON Corporation

Industrial Automation Company
Measuring and Supervisory Controls Division
28th Fl., Crystal Tower Bldg.,
Printed in Japan
1-2-27, Shiromi, Chuo-ku,
1099-0.1M (0696)
Osaka 540-6028 Japan
Tel: (81)6-6949-6035 Fax: (81)6-6949-6069

