

T2550H

High temperature 25 A Snubberless™ TRIAC

Features

- Medium current TRIAC
- 150 °C max. Tj turn-off commutation
- Low thermal resistance with clip bonding
- Very high 3 quadrant commutation capability
- RoHS (2002/95/EC) compliant package

Applications

Specifically designed for use in high temperature environment (found in hot appliances such as cookers, ovens, hobs, electric heaters, coffee machines).

Description

This 25 A T2550H TRIAC provides an enhanced performance in terms of reduced power loss and thermal dissipation. This allows for the optimization of the heatsinking dimensions, leading to space and cost effectivness when compared to electro-mechanical solutions.

Based on ST snubberless technology, the T2550H offers high commutation switching capabilities and high noise immunity levels. Thanks to the clip assembly technique, it provides a superior performance in surge current handling.

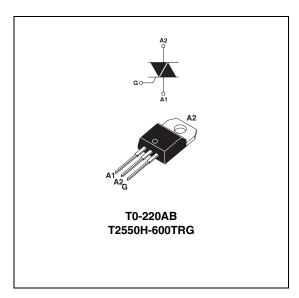


Table 1. Device summary

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Symbol	Value	Unit
I _{T(RMS)}	25	Α
V _{DRM} /V _{RRM}	600	V
I _{GT (Q₁)}	50	mA

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Table 2. Absolute maximum ratings

Symbol	Parameter			Value	Unit	
I _{T(RMS)}	RMS on-state current (full sine wave)		T _c = 125 °C	25	Α	
1 -	Non repetitive surge peak on-state current	F = 50 Hz	t = 20 ms	250	Α	
ITSM	(full cycle, T _j initial = 25° C)	F = 60 Hz	t = 16.7 ms	260	A	
l²t	I^2 t Value for fusing $t_p = 10 \text{ ms}$			340	A ² s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	F = 120 Hz	T _j = 150 °C	50	A/μs	
V _{DSM} /V _{RSM}	Non repetitive surge peak off-state voltage	$t_p = 10 \text{ ms}$	T _j = 25 °C	700	V	
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α	
P _{G(AV)}	Average gate power dissipation		T _j = 150 °C	1	W	
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 150	°C	

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test Conditions Quadrant			Value	Unit
I _{GT} ⁽¹⁾	V _D = 12 V R _L = 33 Ω	I - II - III	MAX.	50	mA
V _{GT}	AD = 15 A UF = 22.75	I - II - III	MAX.	1.3	V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_j = 150^{\circ} \text{ C}$ $I - II - III$		MIN.	0.15	V
I _H ⁽²⁾	I _T = 500 mA		MAX.	75	mA
ΙL	I _G = 1.2 I _{GT} I - II		MAX.	90	mA
dV/dt ⁽²⁾	V _D = 67% V _{DRM} gate open T _j = 150° C		MIN.	500	V/µs
(dl/dt)c ⁽²⁾	Without snubber $T_j = 150^{\circ} C$		MIN.	11.1	A/ms

^{1.} minimum I_{GT} is guaranted at 10% of I_{GT} max.

Table 4. Static Characteristics

Symbol	Test Conditions			Value	Unit
$V_T^{(1)}$	$I_{TM} = 35 \text{ A}$ $t_p = 380 \mu\text{s}$	T _j = 25 °C	MAX.	1.5	V
V _{to} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.80	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	19	mΩ
I_{DRM} $V_{DRM} = V_{RRM}$ $V_{DRM}/V_{RRM} = 400 \text{ V} (a)$	V - V	T _j = 25 °C		5	μΑ
	VDRM — VRRM	T _j = 150 °C	MAX.	8.5	mA
	V _{DRM} /V _{RRM} = 400 V (at mains peak voltage)	T _j = 150 °C		5.5	

^{1.} for both polarities of A2 referenced to A1.

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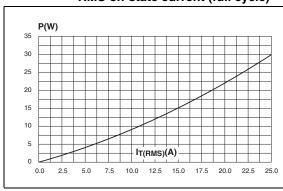
^{2.} for both polarities of A2 referenced to A1.

T2550H Characteristics

Table 5. Thermal resistance

Symbol	Parameter		Unit
R _{th(j-c)}	Junction to case (AC)		°C/W

Figure 1. Maximum power dissipation versus Figure 2. RMS on-state current versus case RMS on-state current (full cycle) temperature (full cycle)



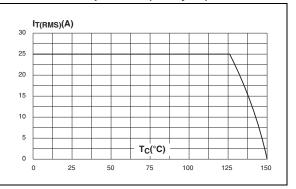
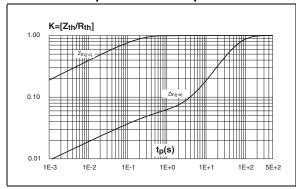


Figure 3. Relative variation of thermal impedance versus pulse duration

Figure 4. On-state characteristics (maximum values)



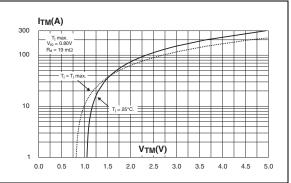
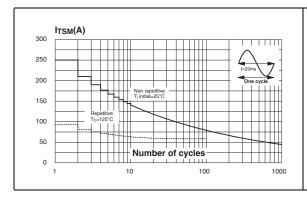
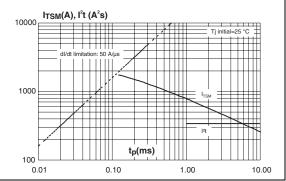


Figure 5. Surge peak on-state current versus Figure 6. number of cycles

Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10 \ \text{ms}$ and corresponding value of l^2t

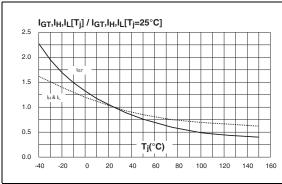




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Figure 7. Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)

Figure 8. Relative variation of critical rate of decrease of main current versus (dV/dt)c (typical values)



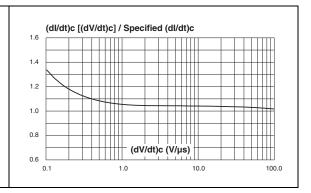
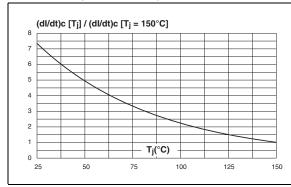


Figure 9. Relative variation of critical rate of decrease of main current versus junction temperature

Figure 10. Leakage current versus junction temperature for different values of blocking voltage (typical values)



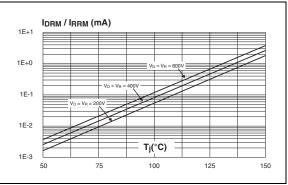
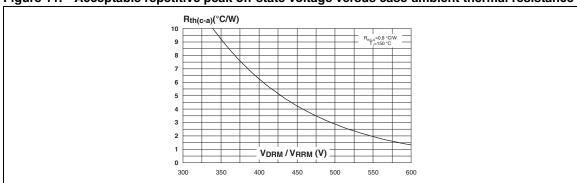
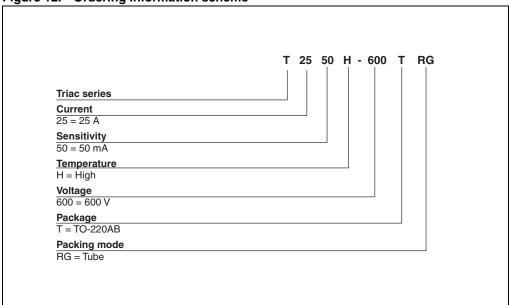


Figure 11. Acceptable repetitive peak off-state voltage versus case-ambient thermal resistance



2 Ordering information scheme

Figure 12. Ordering information scheme



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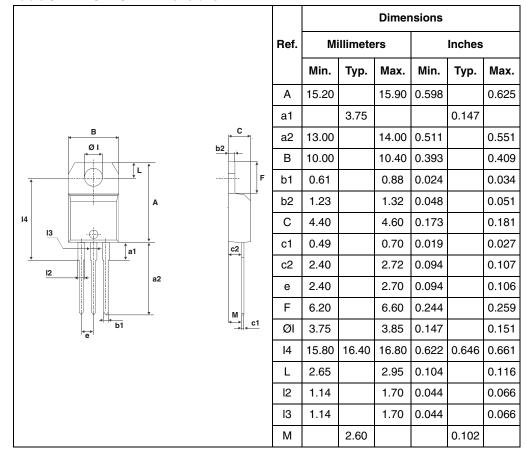
Package information T2550H

3 Package information

- Epoxy meets UL94, V0
- Recommended torque 0.4 to 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Table 6. TO-220AB Dimensions



T2550H Ordering information

4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T2550H-600TRG	T2550H600T	TO-220AB	2.3 g	50	Tube

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
Apr-2002	5A	Last update.
13-Feb-2006	6	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.
20-Jun-2006	7	Reformatted to current standards. Figures 6 and 11 replaced.
27-May-2008	8	Reformatted to current standards. ECOPACK statement updated.

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