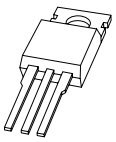


BTA225-600BT

Three quadrant triacs high commutation

Rev. 01 — 3 March 2005 Product data sheet

1. Product profile

1.1 General description

Passivated high commutation triac in a SOT78 (TO-220AB) plastic package. Intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. These devices will commutate the full rated RMS current at the maximum rated junction temperature, without the aid of a snubber.

1.2 Features

- High maximum junction temperature
- High commutation capability

1.3 Applications

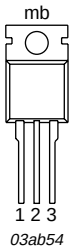
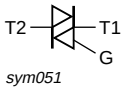
- Motor control
- Industrial and domestic heating

1.4 Quick reference data

- $V_{DRM} \leq 600$ V
- $I_{TSM} \leq 200$ A
- $I_{T(RMS)} \leq 25$ A
- $I_{GT} \leq 50$ mA (T2+ G+; T2+ G–; T2– G–)

2. Pinning information

Table 1: Pinning

Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		
2	main terminal 2 (T2)		
3	gate (G)		
mb	mounting base, connected to main terminal 2 (T2)		

SOT78 (TO-220AB)

PHILIPS

3. Ordering information

Table 2: Ordering information

Type number	Package		
	Name	Description	Version
BTA225-600BT	SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

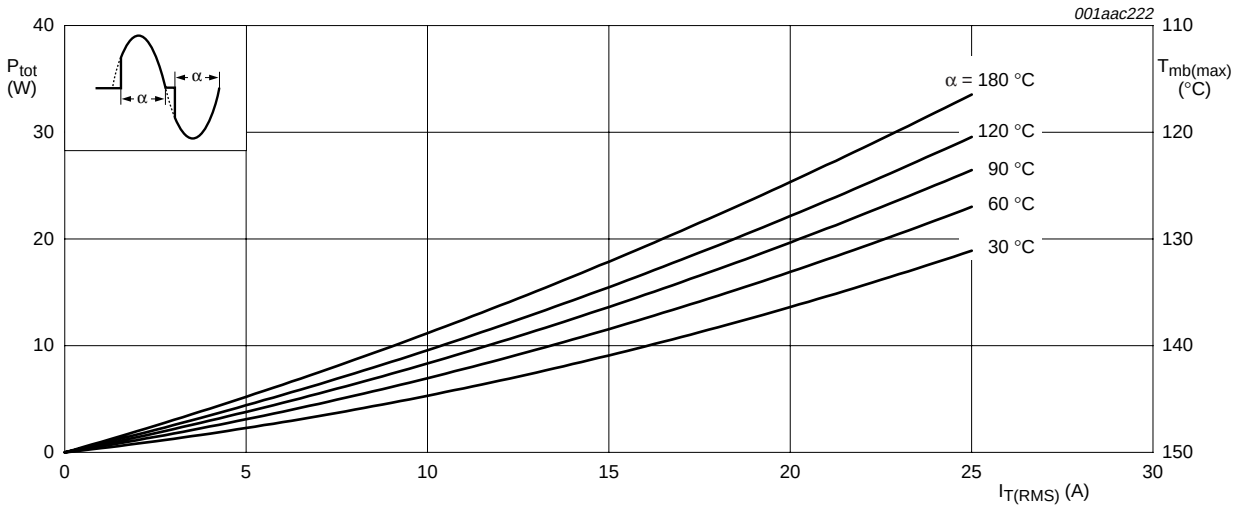
4. Limiting values

Table 3: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

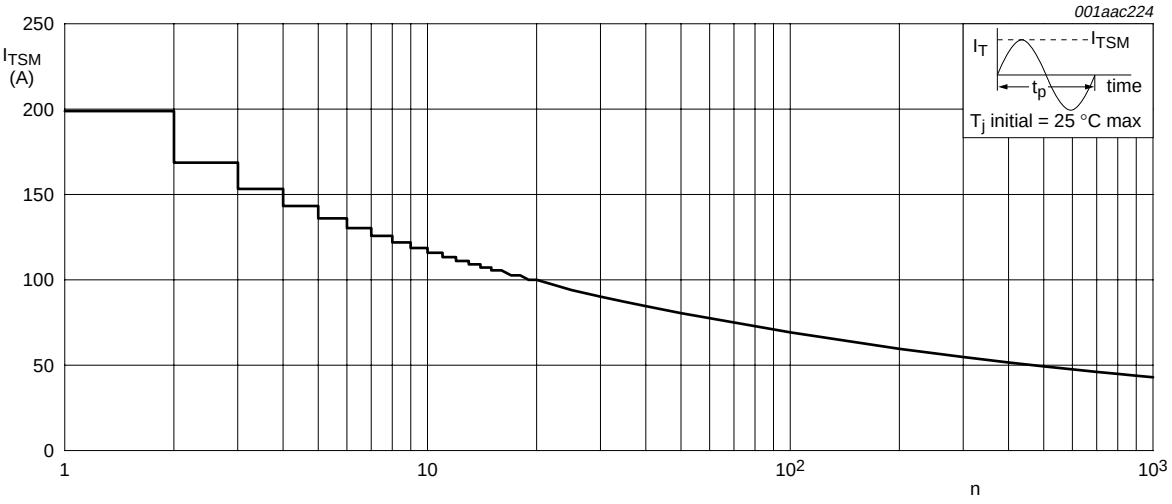
Symbol	Parameter	Conditions		Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		[1]	-	600	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 116$ °C; see Figure 4 and 5		-	25	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; T_j = 25 °C prior to surge; see Figure 2 and 3				
		t_p = 20 ms		-	200	A
		t_p = 16.7 ms		-	220	A
I^2t	I^2t for fusing	t = 10 ms		-	200	A^2s
dI_T/dt	repetitive rate of rise of on-state current after triggering	I_{TM} = 30 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs		-	100	A/µs
I_{GM}	peak gate current			-	2	A
V_{GM}	peak gate voltage			-	5	V
P_{GM}	peak gate power			-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period		-	0.5	W
T_{stg}	storage temperature			–40	+150	°C
T_j	junction temperature			-	150	°C

[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/µs.



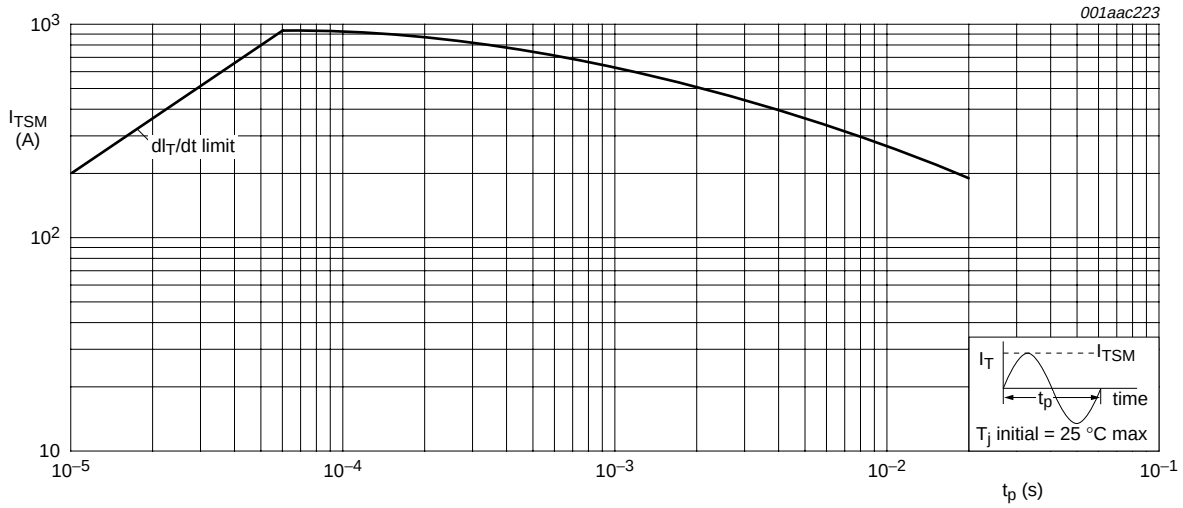
α = conduction angle

Fig 1. Total power dissipation as a function of RMS on-state current; maximum values



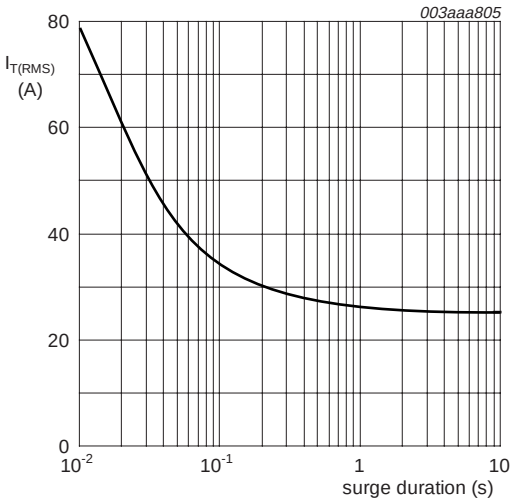
f = 50 Hz

Fig 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



$t_p \leq 20$ ms

Fig 3. Non-repetitive peak on-state current as a function of pulse width (t_p) for sinusoidal currents; maximum values



f = 50 Hz; $T_{mb} \leq 116$ °C

Fig 4. RMS on-state current as a function of surge duration for sinusoidal currents

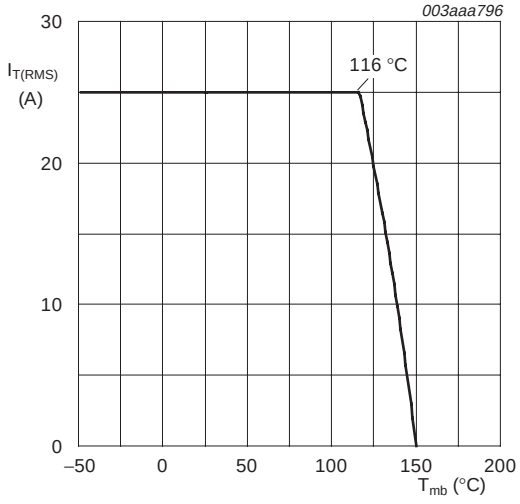
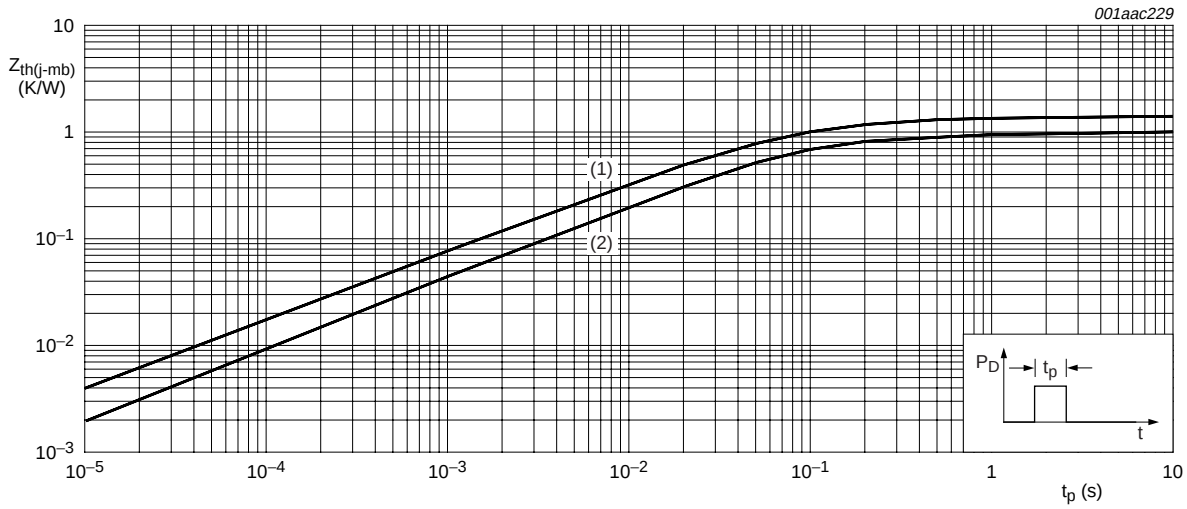


Fig 5. RMS on-state current as a function of mounting base temperature; maximum values

5. Thermal characteristics

Table 4: Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle	-	-	1.0	K/W
		half cycle	-	-	1.4	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



(1) Unidirectional

(2) Bidirectional

Fig 6. Transient thermal impedance as a function of pulse width

6. Characteristics

Table 5: Characteristics

T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I_{GT}	gate trigger current	V_D = 12 V; I_T = 0.1 A; see Figure 8	[1]			
		T2+ G+	2	18	50	mA
		T2+ G–	2	21	50	mA
		T2– G–	2	34	50	mA
I_L	latching current	V_D = 12 V; I_{GT} = 0.1 A; see Figure 10				
		T2+ G+	-	31	60	mA
		T2+ G–	-	34	90	mA
		T2– G–	-	30	60	mA
I_H	holding current	V_D = 12 V; I_{GT} = 0.1 A; see Figure 11	-	31	60	mA
V_T	on-state voltage	I_T = 30 A; see Figure 9	-	1.3	1.55	V
V_{GT}	gate trigger voltage	V_D = 12 V; I_T = 0.1 A; see Figure 7	-	0.7	1.5	V
		V_D = 400 V; I_T = 0.1 A; T_j = 150 °C	0.25	0.4	-	V
I_D	off-state leakage current	$V_D = V_{DRM(max)}$; T_j = 150 °C	-	1	5	mA
Dynamic characteristics						
dV_D/dt	critical rate of rise of off-state voltage	V_{DM} = 67 % $V_{DRM(max)}$; T_j = 150 °C; exponential waveform; gate open circuit	1000	4000	-	V/µs
dI_{com}/dt	critical rate of change of commutating current	V_{DM} = 400 V; T_j = 150 °C; $I_{T(RMS)}$ = 25 A; without snubber; gate open circuit; see Figure 12	9	20	-	A/ms
t_{gt}	gate controlled turn-on time	I_{TM} = 30 A; $V_D = V_{DRM(max)}$; I_G = 0.1 A; dI_G/dt = 5 A/µs	-	2	-	µs

[1] Device does not trigger in the T2–, G+ quadrant.

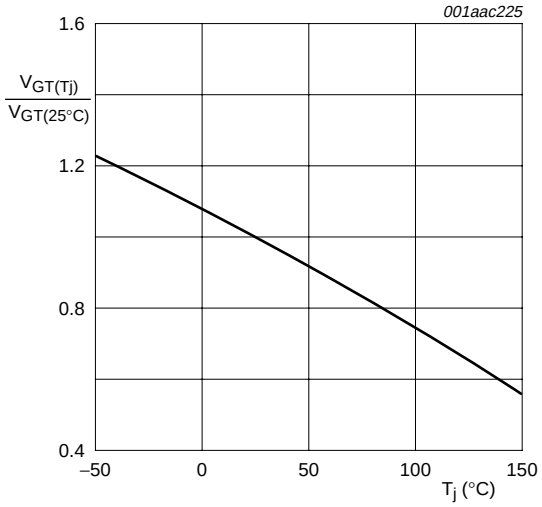
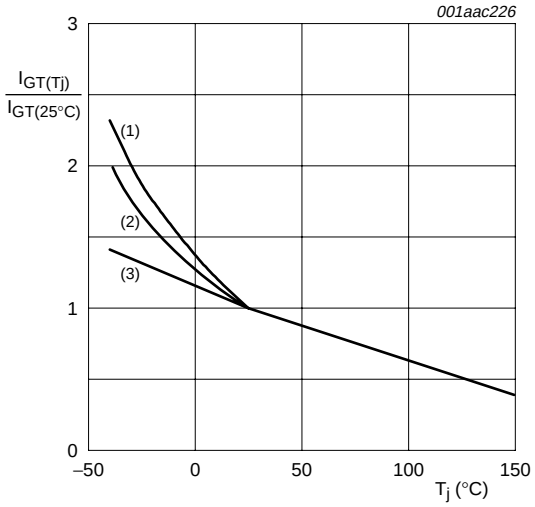


Fig 7. Normalized gate trigger voltage as a function of junction temperature

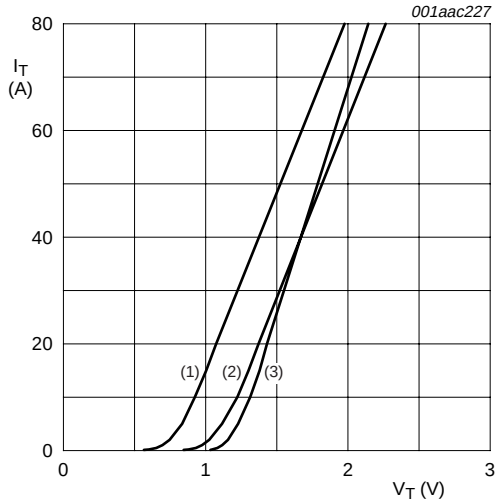


(1) T2– G–

(2) T2+ G–

(3) T2+ G+

Fig 8. Normalized gate trigger current as a function of junction temperature



V_O = 1.073 V

R_S = 0.015 Ω

(1) T_j = 150 °C; typical values

(2) T_j = 150 °C; maximum values

(3) T_j = 25 °C; maximum values

Fig 9. On-state current characteristics

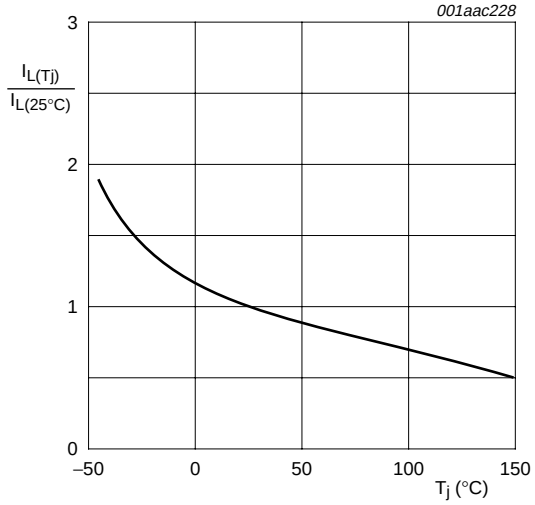


Fig 10. Normalized latching current as a function of junction temperature

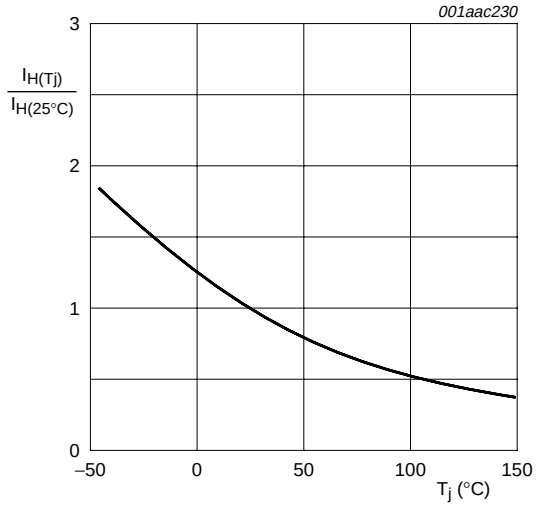


Fig 11. Normalized holding current as a function of junction temperature

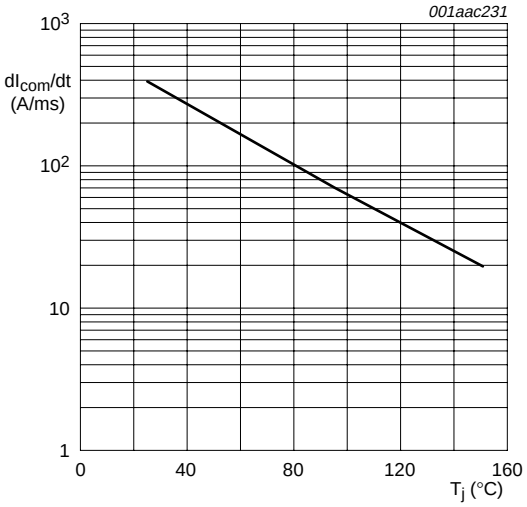


Fig 12. Critical rate of change of commutating current as a function of junction temperature; typical values

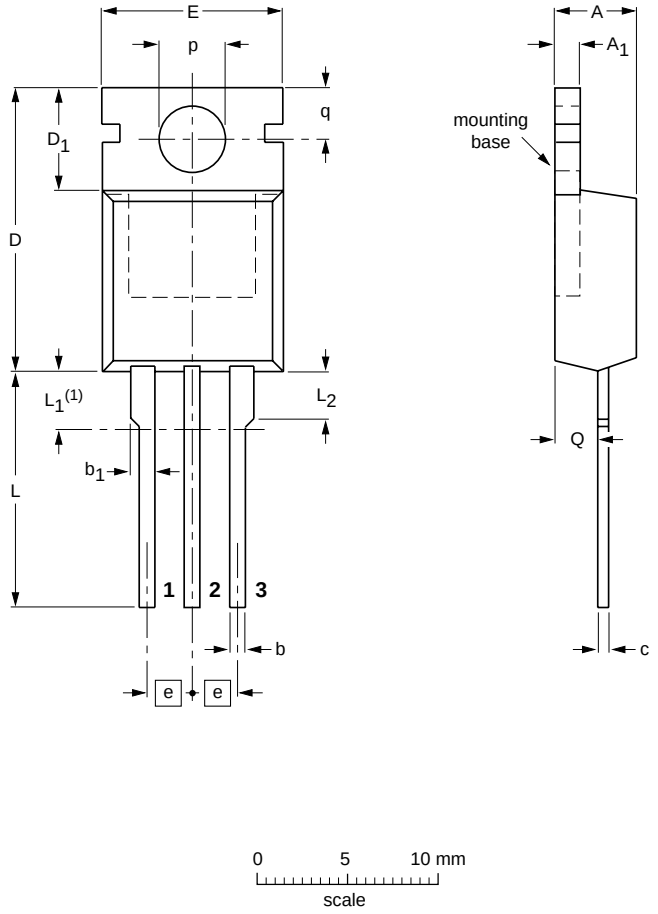
7. Package information

Refer to mounting instructions for SOT78 (TO-220AB) package.

Epoxy meets requirements of UL94 V-0 at $\frac{1}{8}$ inch.

8. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB **SOT78**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1	b	b_1	c	D	D_1	E	e	L	L_1(1)	L_2 max.	p	q	Q
mm	4.5 4.1	1.39 1.27	0.9 0.6	1.3 1.0	0.7 0.4	15.8 15.2	6.4 5.9	10.3 9.7	2.54	15.0 13.5	3.30 2.79	3.0	3.8 3.6	3.0 2.7	2.6 2.2

Note

1. Terminals in this zone are not tinned.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT78		3-lead TO-220AB	SC-46			~~01-02-16~~ 03-01-22

Fig 13. Package outline SOT78 (TO-220AB)

9. Revision history

Table 6: Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BTA225-600BT_1	20050303	Product data sheet	-	9397 750 14379	-

10. Data sheet status

Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

11. Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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