

T1210T-6I, T1220T-6I T1225T-6I, T1235T-6I

Entry level Snubberless™, logic level and standard 12 A TRIACs

Features

- Medium current TRIAC
- High static and dynamic commutation
- Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- V_{RM} = 600 V
- Insulated packaged rated at 2500 V rms

Applications

- Entry-level and value-sensitive applications
- General purpose AC line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

Description

Available in through-hole, the T12T series of TRIACs can be used as on/off or phase angle control function in general purpose AC switching where high commutation capability is required.

Initially planned for power conic (T series) equipment, this series can be used in many value sensitive appliances 'hanks to the guidance on parameters provided in the following pages.

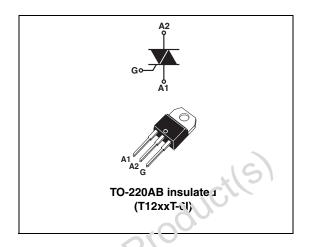


Table 1. Device summary

Order code	Symbol	Value
T1220 7-61 11435T-61	I _{GT} 3Q Snubberless	20 / 35 mA
T1225T-6I	I _{GT} 4Q standard	25 mA
T1210T-6I	I _{GT} 3Q logic level	10 mA

TM: Snubberless is a trademark of STMicroelectronics

March 2009 Rev 1 1/9

1 Characteristics

Table 2. Absolute maximum ratings (limiting values; $T_j = 25$ °C, unless otherwise specified)

Symbol	Parameter			Value	Uni
I _{T(RMS)}	On-state rms current (full sine wave) $T_c = 88 ^{\circ}\text{C}$			12	Α
1	Non repetitive surge peak on-state current (full	F = 50 Hz	t _p = 20 ms	90	Λ.
I _{TSM}	cycle, T _j initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	95	Α
l ² t	I ² t Value for fusing	t _p = 10 ms		60	A ² s
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 60 \text{ Hz}$ $I_{T} \leq 100 \text{ ns}$		T _j = 125 °C	50	A/µ
V _{DSM} / V _{RSM}	Non repetitive surge peak off-state voltage	t _p = 10 ms	T _j = 25 °C	V _{DRM} /V _{RRM} + 100	V
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 125 °C	4	A
P _{G(AV)}	Average gate power dissipation	-1	T _j = 125 °C	1 (W
T _{stg}	Storage junction temperature range			- 40 to + 150	°(
T _i	Operating junction temperature range			- 40 to + 125	°(
,		50let	ePro		
,	Operating junction temperature range)50 ^{[8}	ePro		

T12xxT Symbol **Test conditions** Quadrant Unit T1210T T1220T T1225T T1235T 1 - 11 - 111 10 20 25 35 I_{GT} ⁽¹⁾ $V_D = 12 \text{ V}$ $R_L = 30 \Omega$ MAX. mΑ IV 40 $V_D = V_{DRM}$, $R_L = 3.3 \text{ k}\Omega$, ALL MAX. ٧ V_{GT} 1.3 $T_i = 25 \degree C$ $\begin{aligned} &V_D = V_{DRM}, \ R_L = 3.3 \ k\Omega, \\ &T_j = 125 \ ^{\circ}C \end{aligned}$ ALL MIN. 0.2 V_{GD} I_H ⁽²⁾ $I_{T} = 500 \text{ mA}$ MAX. 10 15 20 30 mA I - III 20 35 40 50 IV I_{L} $I_{G} = 1.2 I_{GT}$ MAX. 40 mΑ Ш 30 40 60 80 T_j = 125 °C 100 1000 2000 100 dV/dt (2) $V_D = 67\% V_{DRM,}$ gate open MIN. V/µs $T_i = 150 \, {}^{\circ}C^{(3)}$ 50 500 1000 50 $(dV/dt)c = 0.1 V/\mu s$ 7 7 T_i = 125 °C $(dV/dt)c = 10 V/\mu s$ 3 3 6 Without snubber 12 (di/dt)c (2) MIN. A/ms $(dV/dt)c = 0.1 V/\mu s$ 3 3 1 $T_j = 150 \, {}^{\circ}C^{(3)}$ $(dV/dt)c = 10 V/\mu s$ 1

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Static characteristics Table 4.

Symbol	Test conditions	Val	Unit		
V _T ⁽¹⁾	$I_{TM} = 17 \text{ A}, t_p = 380 \mu\text{s}$	T _j = 25 °C	Max.	1.55	V
V _{TO} (1)	Threshold voltage	T _j = 125 °C	Max.	0.85	V
R _D ⁽¹⁾	Dynamic resistance	T _j = 125 °C	Max.	35	mΩ
I _{BBM}	$V_{DRM} = V_{RRM}$	T _j = 25 °C	- Max.	5	μΑ
		T _j = 125 °C		1	A
	$V_D = 0.9 \times V_{DRM}$	T _j = 150 °C ⁽²⁾	Тур.	1.9	mA

3

Without snubber 1. Minimum I_{GT} is guaranted at 5% of I_{GT} max.

^{2.} For both polarities of A2 referenced to A1.

^{3.} Derating information for excess temperature above T_i max.

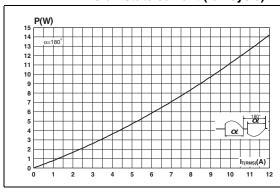
^{1.} For both polarities of A2 referenced to A1.

^{2.} Derating information for excess temperature above Ti max.

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.6	°C/W
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case rms on-state current (full cycle) temperature (full cycle)



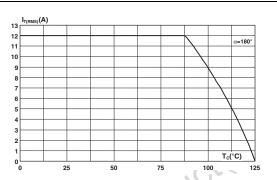


Figure 3. On-state rms current versus ambient temperature

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Figure 4. Relative variation of thermal impedance versus pulse duration

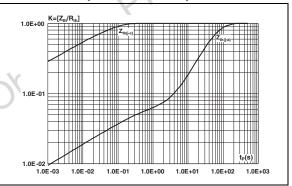
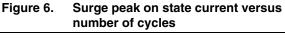
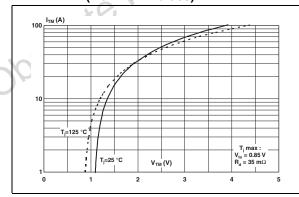
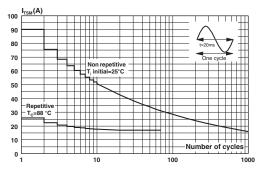


Figure 5. On state characteristics (maximum values)







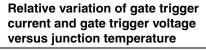
4/9

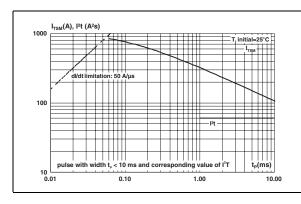
2.5

1.0

0.0

Figure 7. Non repetitive surge peak on state Figure 8. current for a sinusoidal





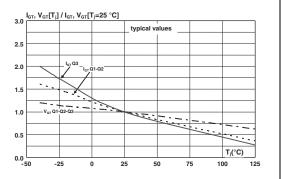
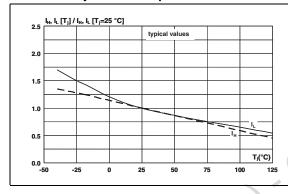


Figure 9. Relative variation of holding current and latching current versus junction temperature

Figure 10. Relative variation of critical rate of decrease of main current versus (dV/dt)c



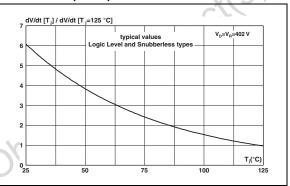
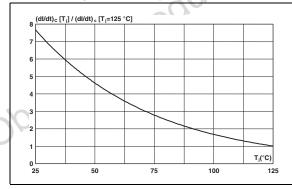
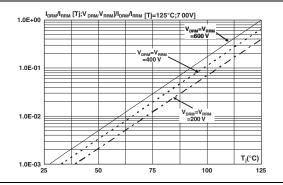


Figure 11. Relative variation of critical rate of Figure 12. Leakage current versus junction decrease of main current versus junction temperature

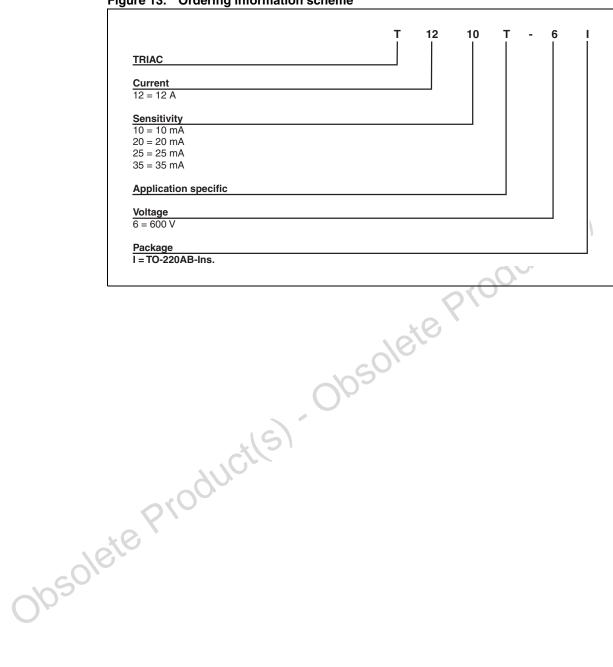
temperature for different values of blocking voltage (typical values)





2 Ordering information scheme

Figure 13. Ordering information scheme



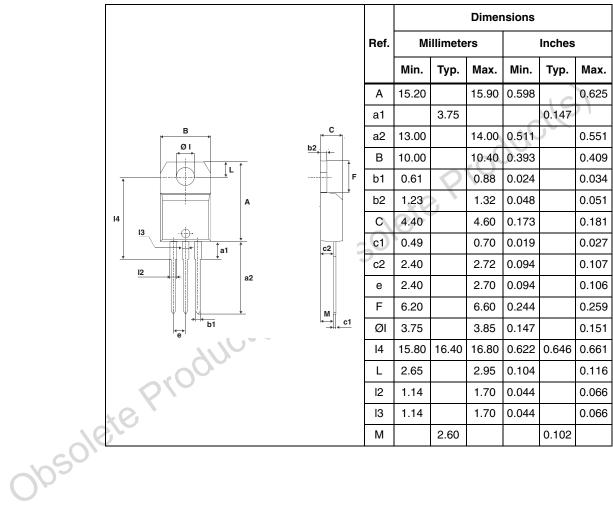
6/9

3 Package mechanical data

- Epoxy meets UL94, V0
- Recommended torque: 0.4 to 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 6. TO-220AB insulated dimensions



4 Ordering Information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1210T-6I	T1210T-6I				
T1220T-6I	T1220T-6I	TO-220AB-ins.	2.3 g	50	Tube
T1225T-6I	T1225T-6I	10-220AB-IIIS.	2.5 g	50	Tube
T1235T-6I	T1235T-6I				

5 Revision history

Table 8. Document revision history

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	Date	Revision	Changes
	23-Mar-2009	1	Initial release.
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			leite
			60/0
			ObsoletePle
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	- *(70,	
	61,		
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Obsole	3		
-1050			
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57/

9/9