

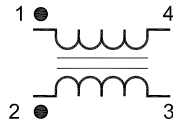
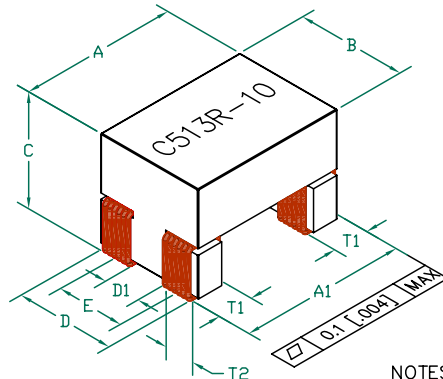


CC1812C513R-10

UNCONTROLLED DOCUMENT

ELECTRICAL SPECIFICATION @ 25°C
OPERATING TEMPERATURE RANGE: -40°C TO +125°C

PARAMETER SYMBOL	TEST LEADS	CONDITIONS	REQUIREMENTS		
			MIN	TYP	MAX
Lp (μH)	1-4	@ 100KHz/ 10mV	35.7	51	66.3
L LEAKAGE (μH)	1-4 (s' 2-3)	@ 100KHz/ 10mV			2.6
TRP	1-4/2-3		1 : 1 +/- 5%		
DCR (Ω)	1-4 2-3				0.50
Hi-pot (Vac)	Primary Secondary	0.5mA, 2sec.	250		
I (mA)					200



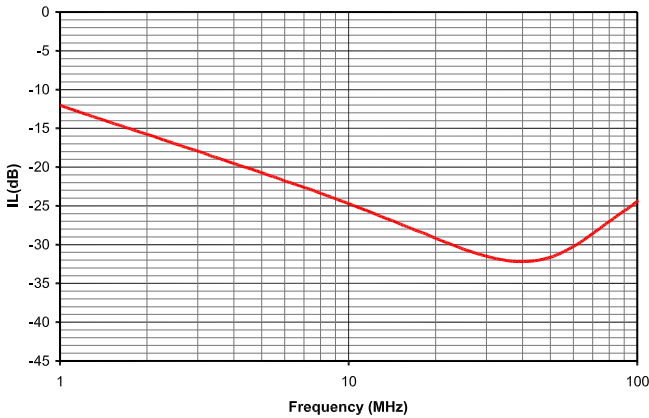
PHYSICAL DIMENSIONS:

A	5.00 [.197]	+ .005	MAX.
A ₁	5.20 [.205]	+ .005	MAX.
B	3.50 [.138]	+ .005	MAX.
C	3.55 [.140]	+ .005	MAX.
D	1.95 [.077]	+0.55 [.022]	
D1	1.00 [.039]	+0.90 [.035]	
E	1.65 [.065]	+0.55 [.022]	
T1	1.00 [.039]	+ .005	TYP.
T2	0.50 [.020]	+0.20 [.008]	

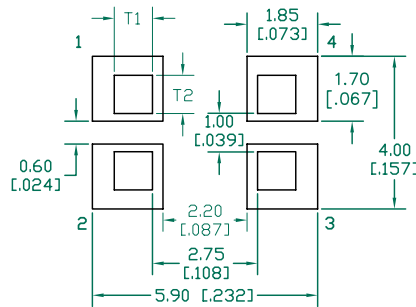
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED ON 13" REELS, 2000 PCS/REEL.
2. OPEN BOTTOM CONSTRUCTION.
3. PRE-TINNED DIMENSIONS AND TOLERANCES.

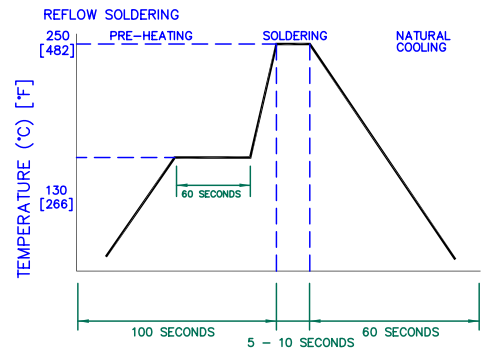
Typical Insertion Loss @ 50 ohm



LAND PATTERNS FOR REFLOW SOLDERING



RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
D	UPDATE TERMINAL DIMS, TEST VOLTAGE	03/11/10	JUN	PROJECT/PART NUMBER:	CC1812C513R-10	REV	D
C	UPDATE COMPANY LOGO	04/07/09	JRK	PART TYPE:	ASSEMBLY	DRAWN BY:	JRK
B	UPDATE COMPANY LOGO	05/08/07	JRK	DATE:	06/09/05	SCALE:	NTS
A	ORIGINAL DRAFT	06/09/05	JRK	CAD #	CC1812C513R-10-D	TOOL #	-
REV	DESCRIPTION	DATE	INT			SHEET: 2 of 2	

HP 8753E Network Analyzer
Intercontinental Microwave Test Fixture

