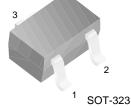


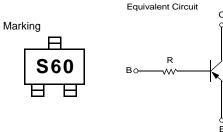
FJX4010R

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=10KΩ)
- Complement to FJX3010R



1. Base 2. Emitter 3. Collector



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	-40	V
V _{CEO}	Collector-Emitter Voltage	-40	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-100	mA
P _C	Collector Power Dissipation	200	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu A, I_E = 0$	-40			V
BV_{CEO}	Collector-Emitter Breakdown Voltage	I_E = -1mA, I_B =0	-40			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = -30V, I_{E} = 0$			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -5V, I_{C} = -1mA	100		600	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I_C = -10mA, I_B = -1mA			-0.3	V
C _{ob}	Output Capacitance	V_{CB} = -10V, I_{E} =0 f=1MHz		5.5		pF
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -5mA$		200		MHz
R	Input Resistor		7	10	13	ΚΩ

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Typical Characteristics

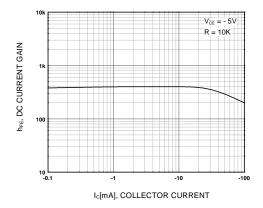


Figure 1. DC current Gain

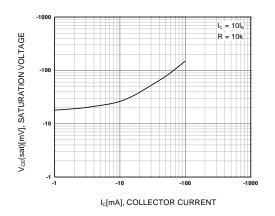


Figure 2. Collector-Emitter Saturation

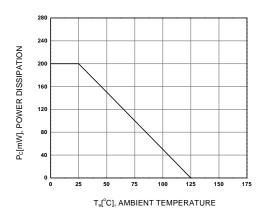
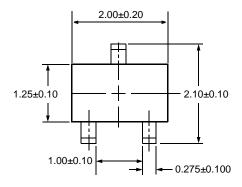


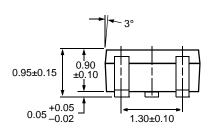
Figure 3. Power Derating

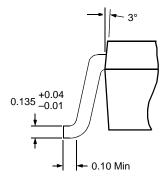
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Package Dimensions

SOT-323







Dimensions in Millimeters

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Across the board.	. Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
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Programmable Active Droop™		OPTOPLANAR™	SMART START™	

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