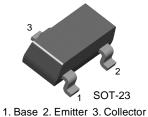
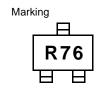


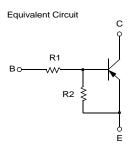
FJV4106R

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R_1=10K\Omega$, $R_2=47K\Omega$)
- Complement to FJV3106R







PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V_{CBO}	Collector-Base Voltage	-50	V	
V_{CEO}	Collector-Emitter Voltage	-50	V	
V _{EBO}	Emitter-Base Voltage	-10	V	
I _C	Collector Current	-100	mA	
P _C	Collector Power Dissipation	200	mW	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 ~ 150	°C	

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -10\mu A, I_E = 0$	-50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -100 \mu A, I_B = 0$	-50			V
I _{CBO}	Collector Cut-off Current	V _{CB} = -40V, I _E =0			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -5V, I_{C} = -5mA	68			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10mA, I _B = -0.5mA			-0.3	V
C _{ob}	Output Capacitance	V_{CB} = -10V, I_{E} =0 f=1.0MHz		5.5		pF
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -5mA$		200		MHz
V _I (off)	Input Off Voltage	V_{CE} = -5V, I_{C} = -100 μ A	-0.3			V
V _I (on)	Input On Voltage	$V_{CE} = -0.3V, I_{C} = -1mA$			-1.4	V
R ₁	Input Resistor		7	10	13	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.19	0.21	0.24	

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Typical Characteristics

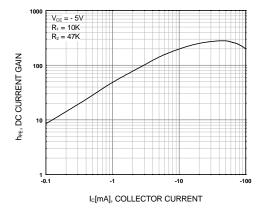


Figure 1. DC current Gain

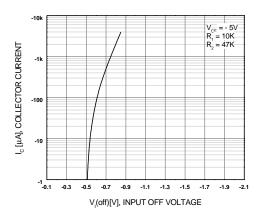


Figure 3. Input Off Voltage

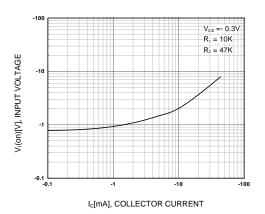


Figure 2. Input On Voltage

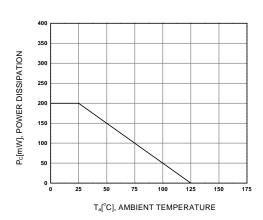
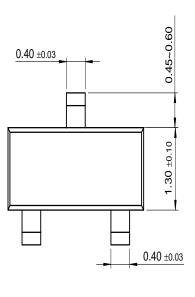


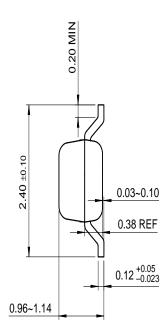
Figure 4. Power Derating

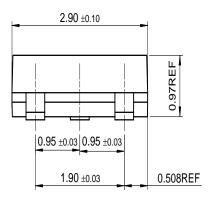
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Package Dimensions

SOT-23







Dimensions in Millimeters

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EnSigna™	I ² C TM	OCX™	RapidConfigure™	UHC™
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
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Programmable Ac	tive Droop™	OPTOPLANAR™	SMART START™	

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