

CMRDM3575

**SURFACE MOUNT  
N-CHANNEL AND P-CHANNEL  
ENHANCEMENT-MODE  
COMPLEMENTARY SILICON MOSFETS**

ATTOmini™



**SOT-963 CASE**

• Device is **Halogen Free** by design

**APPLICATIONS:**

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Powered Portable Devices

**MAXIMUM RATINGS:** (T<sub>A</sub>=25°C)

Drain-Source Voltage  
 Gate-Source Voltage  
 Continuous Drain Current (Steady State)  
 Continuous Drain Current, t<sub>p</sub>≤5.0s  
 Power Dissipation  
 Operating and Storage Junction Temperature  
 Thermal Resistance

**ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C)

SYMBOL	TEST CONDITIONS
I <sub>GSSF</sub> , I <sub>GSSR</sub>	V <sub>GS</sub> =5.0V, V <sub>DS</sub> =0
I <sub>DSS</sub>	V <sub>DS</sub> =5.0V, V <sub>GS</sub> =0
I <sub>DSS</sub>	V <sub>DS</sub> =16V, V <sub>GS</sub> =0
BV <sub>DSS</sub>	V <sub>GS</sub> =0, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
r <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =100mA
r <sub>DS(ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =50mA
r <sub>DS(ON)</sub>	V <sub>GS</sub> =1.8V, I <sub>D</sub> =20mA
r <sub>DS(ON)</sub>	V <sub>GS</sub> =1.5V, I <sub>D</sub> =10mA
r <sub>DS(ON)</sub>	V <sub>GS</sub> =1.2V, I <sub>D</sub> =1.0mA
g <sub>FS</sub>	V <sub>DS</sub> =5.0V, I <sub>D</sub> =125mA
C <sub>rss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0, f=1.0MHz
C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0, f=1.0MHz
C <sub>oss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0, f=1.0MHz
t <sub>on</sub>	V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA
t <sub>off</sub>	V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA



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**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMRDM3575 consists of complementary N-Channel and P-Channel Enhancement-mode silicon MOSFETs designed for high speed pulsed amplifier and driver applications. These MOSFETs offer Low r<sub>DS(ON)</sub> and Low Threshold Voltage.

**MARKING CODE: CT**

**FEATURES:**

- Power Dissipation: 125mW
- Low Package Profile: 0.5mm (MAX)
- Low r<sub>DS(ON)</sub>
- Low Threshold Voltage
- Logic Level Compatible
- Small SOT-963 Surface Mount Package

SYMBOL	N-CH (Q1)	P-CH (Q2)	UNITS
V <sub>DS</sub>		20	V
V <sub>GS</sub>		8.0	V
I <sub>D</sub>	160	140	mA
I <sub>D</sub>	200	180	mA
P <sub>D</sub>		125	mW
T <sub>J</sub> , T <sub>stg</sub>		-65 to +150	°C
θ <sub>JA</sub>		1000	°C/W

SYMBOL	N-CH (Q1)			P-CH (Q2)			UNITS
	MIN	TYP	MAX	MIN	TYP	MAX	
I <sub>GSSF</sub> , I <sub>GSSR</sub>	-	-	100	-	-	100	nA
I <sub>DSS</sub>	-	-	50	-	-	50	nA
I <sub>DSS</sub>	-	-	100	-	-	100	nA
BV <sub>DSS</sub>	20	-	-	20	-	-	V
V <sub>GS(th)</sub>	0.4	-	1.0	0.4	-	1.0	V
r <sub>DS(ON)</sub>	-	1.5	3.0	-	4.0	5.0	Ω
r <sub>DS(ON)</sub>	-	2.0	4.0	-	5.5	7.0	Ω
r <sub>DS(ON)</sub>	-	3.0	6.0	-	8.0	10	Ω
r <sub>DS(ON)</sub>	-	4.0	10	-	11	17	Ω
r <sub>DS(ON)</sub>	-	7.0	-	-	20	-	Ω
g <sub>FS</sub>	-	1.3	-	-	1.3	-	S
C <sub>rss</sub>	-	2.2	-	-	1.0	-	pF
C <sub>iss</sub>	-	9.0	-	-	12	-	pF
C <sub>oss</sub>	-	3.0	-	-	2.7	-	pF
t <sub>on</sub>	-	40	-	-	60	-	ns
t <sub>off</sub>	-	150	-	-	210	-	ns

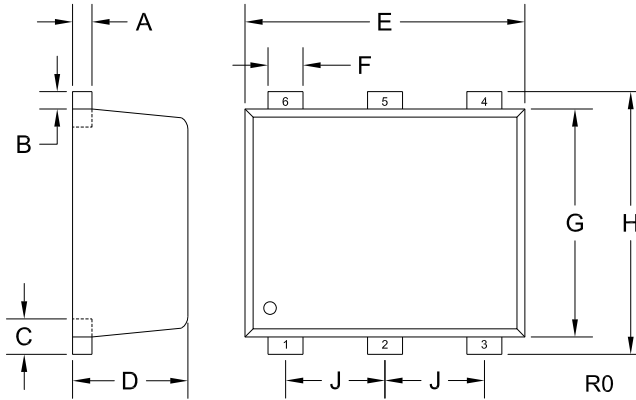
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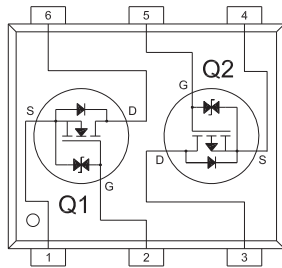
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**SOT-963 CASE - MECHANICAL OUTLINE**



**PIN CONFIGURATION**



**LEAD CODE:**

- 1) Source Q1
- 2) Gate Q1
- 3) Drain Q2
- 4) Source Q2
- 5) Gate Q2
- 6) Drain Q1

**MARKING CODE: CT**

**DIMENSIONS**

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.002	0.006	0.050	0.150
B	0.002	0.006	0.050	0.150
C	0.005	0.007	0.125	0.175
D	0.016	0.020	0.400	0.500
E	0.037	0.041	0.950	1.050
F	0.004	0.008	0.100	0.200
G	0.030	0.033	0.750	0.850
H	0.037	0.041	0.950	1.050
J	0.014		0.350	

SOT-963 (REV: R0)

R1 (8-February 2010)