

New Product

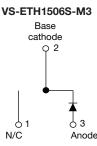
VS-ETH1506S-M3, VS-ETH1506-1-M3

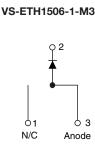
Vishay Semiconductors

Hyperfast Rectifier, 15 A FRED Pt®









D²PAK

TO-262

PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK), TO-262AA
I _{F(AV)}	15 A
V _R	600 V
V _F at I _F	2.45 V
t _{rr} (typ.)	21 ns
T _J max.	175 °C
Diode variation	Single die

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition



HALOGEN

FREE

Designed and qualified according to JEDEC-JESD47

DESCRIPTION/APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Repetitive peak reverse voltage	V _{RRM}		600	V					
Average rectified forward current	I _{F(AV)}	T _C = 139 °C	15	٨					
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	160	A					
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C					

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-				
Forward voltage	V _F	I _F = 15A	-	1.8	2.45	V			
r orward voltage		I _F = 15 A, T _J = 150 °C	-	1.25	1.6				
Reverse leakage current	I _R	$V_R = V_R$ rated	-	0.01	15				
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	200	μA			
Junction capacitance	CT	V _R = 600 V	-	12	-	pF			
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8.0	-	nH			

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VS-ETH1506S-M3, VS-ETH1506-1-M3

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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	100 A/µs, V _R = 30 V	-	21	26				
	+	I _F = 1.5 A, dI _F /dt =	100 A/µs, V _R = 30 V	-	25	36				
	t _{rr}	T _J = 25 °C		-	29	-	ns			
		T _J = 125 °C	l _F = 15 A dl _F /dt = 200 A/μs V _R = 390 V	-	65	-				
De als me a sur a surrant	I _{RRM}	T _J = 25 °C		-	3.9	-	A			
Peak recovery current		T _J = 125 °C		•	7.0	-				
Poverae receivery charge	Q _{rr}	T _J = 25 °C		-	60	-	nC			
Reverse recovery charge		T _J = 125 °C		-	240	-	nc			
Reverse recovery time	t _{rr}		I _F = 15 A	-	42	-	ns			
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 800 A/µs	-	21	-	А			
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	480	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C			
Thermal resistance, junction to case	R _{thJC}		-	1.3	1.51	°C/W			
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70				
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-				
Woight			-	2.0	-	g			
Weight			-	0.07	-	oz.			
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)			
Marking daying		Case style D ² PAK modified	ETH1506S						
Marking device		Case style TO-262		ETH1506-1					

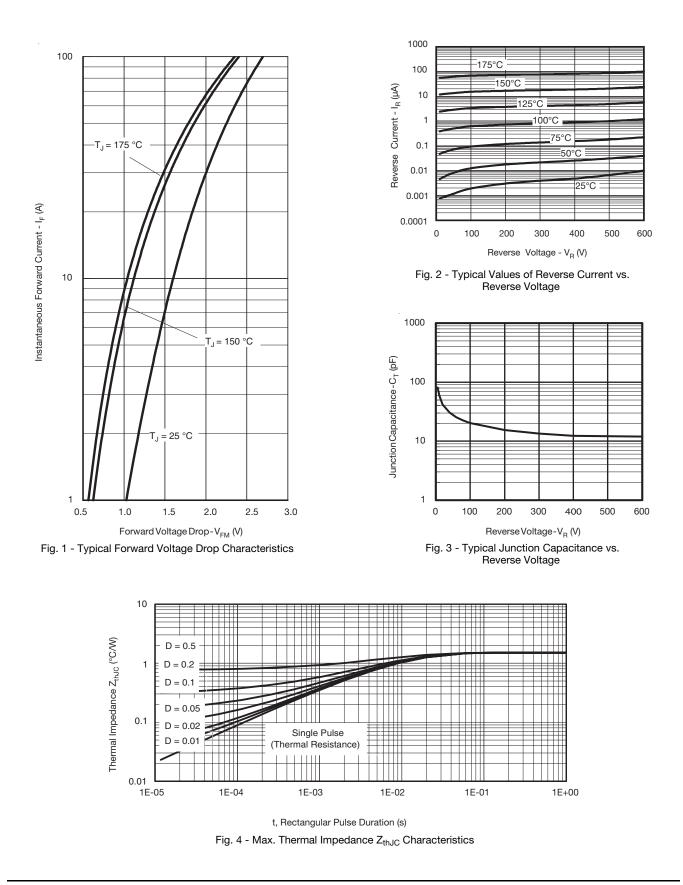
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New Product

VS-ETH1506S-M3, VS-ETH1506-1-M3

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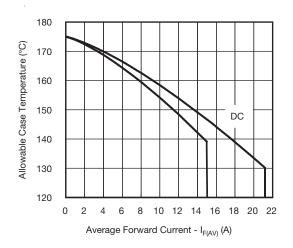


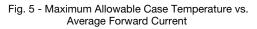
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VS-ETH1506S-M3, VS-ETH1506-1-M3

Vishay Semiconductors Hyperfast Rectifier, 15 A FRED Pt®







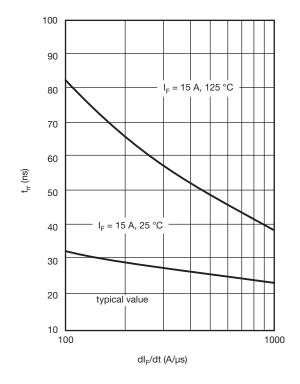


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

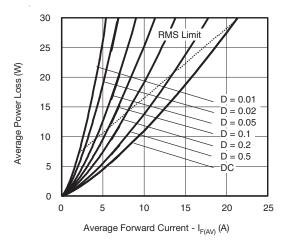


Fig. 6 - Forward Power Loss Characteristics

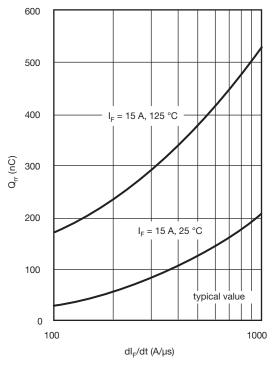


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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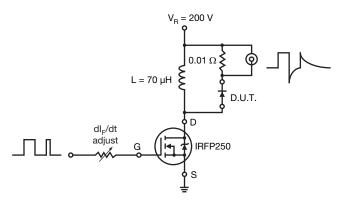


Fig. 9 - Reverse Recovery Parameter Test Circuit

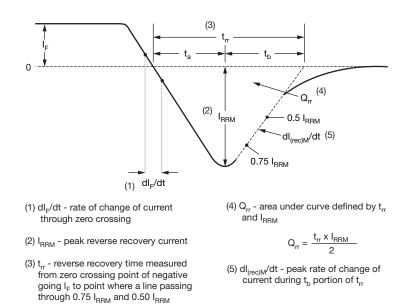


Fig. 10 - Reverse Recovery Waveform and Definitions

extrapolated to zero current.

VS-ETH1506S-M3, VS-ETH1506-1-M3

Vishay Semiconductors Hyperfast Rectifier, 15 A FRED Pt®



ORDERING INFORMATION TABLE

Device code	VS-	Е	т	н	15	06	s	TRL	-M3
	(1)	(2)	(3)	(4)	(5)	(6)	()	(8)	(9)
	1 -	- Visł	nay Sem	niconduc	ctors pro	oduct			
	2 -		uit conf	-	ו				
	3 -		Single (TO-220						
	4 -		Hyperfa		very tim	0			
	5 -		rent cod			6			
	6 -		age coo						
	7 -		= D ² PAI		000 ()				
	<u> </u>	• -1	= TO-2	62					
	8 -	• No	one = Tu	ube (50	pieces)				
		• TF	RL = Tap	be and r	eel (left	oriente	d, for D	² PAK p	ackage
	-	• TF	RR = Ta	pe and ı	eel (rigl	nt orient	ted, for	D ² PAK	packag
	9 -	-M3	= Halo	gen-free	, RoHS	complia	ant, and	l termin	ations I

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-ETH1506S-M3	50	1000	Antistatic plastic tube						
VS-ETH1506-1-M3	50	1000	Antistatic plastic tube						
VS-ETH1506STRR-M3	800	800	13" diameter reel						
VS-ETH1506STRL-M3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046				
Dimensions	TO-262AA	www.vishay.com/doc?95419				
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444				
	TO-262AA	www.vishay.com/doc?95443				
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032				

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Outline Dimensions

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MIN.

0.270

0.380

0.311

0.575

0.070

0.050

0.188

0.100 BSC

0.010 BSC

MAX.

0.315

0.420

0.346

0.625

0.110

0.070

0.208

3

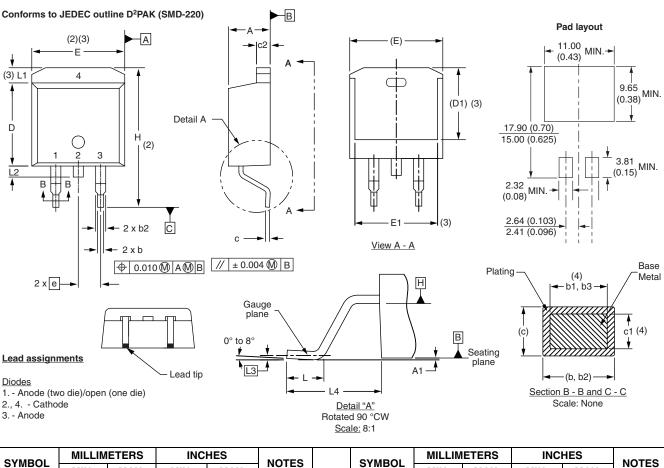
2, 3

З

3

D²PAK





SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		
	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	
С	0.38	0.74	0.015	0.029		L1	-	1.65	
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing per ASME Y14.5 M-1994 $\,$

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

 $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Document Number: 95046
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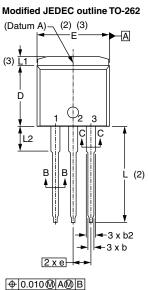


Outline Dimensions

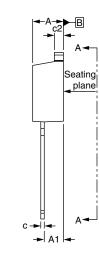
Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



Lead tip



Lead assignments

2., 4. - Cathode

1. - Anode (two die)/open (one die)

Diodes

3. - Anode

D1(3) (3) E1 Section A - A Base (4)Plating b1, b3 metal Ā ///// (4)<--(b, b2)-►

Е

Section B - B and C - C Scale: None

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 0.160 0.190 А 4.06 4.83 0.080 A1 2.03 3.02 0.119 0.51 0.99 0.020 0.039 b b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 b3 1.14 1.73 0.045 0.068 4 0.38 0.74 0.015 0.029 с 0.38 0.015 0.023 c1 0.58 4 0.045 0.065 c2 1.14 1.65 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2.3 E1 7.90 8.80 0.311 0.346 3 2.54 BSC 0.100 BSC е L 13.46 0.530 0.555 14.10 L1 1.65 0.065 3 L2 3.56 3.71 0.140 0.146

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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