

Isolated, High-Density, Eighth-Brick 100W DC/DC Converters



#### **FEATURES**

- RoHS compliant
- Industry standard eighth-brick pinout and package
- Outputs from 1.5V to 12V up to 100W
- Low profile 0.4" height with 0.9" x 2.3" outline dimensions
- 36 to 75 Vdc nominal input
- Fully isolated, 2250 Vdc (BASIC) insulation
- Outstanding thermal performance and derating
- Extensive self-protection and short circuit features with no output reverse conduction
- On/Off control, trim and sense functions
- Interleaved synchronous rectification yields high efficiency over 90%
- Fully protected against temperature and voltage limits
- UL/EN/IEC 60950-1 safety approvals
- Qual/HALT/EMI testing is scheduled

For efficient, fully isolated DC power in the smallest space, the UCE open frame DC/DC converter series fit in industry-standard "eighth brick" outline dimensions and mounting pins (on quarter-brick pinout).

#### PRODUCT OVERVIEW

Units are offered with fixed output voltages from 1.5 to 12 Volts and currents up to 40 Amps. UCEs operate over a wide temperature range (up to +85 degrees Celsius at moderate airflow) with full rated power. Interleaved synchronous rectifier topology yields excellent efficiency over 90% and no reverse output conduction.

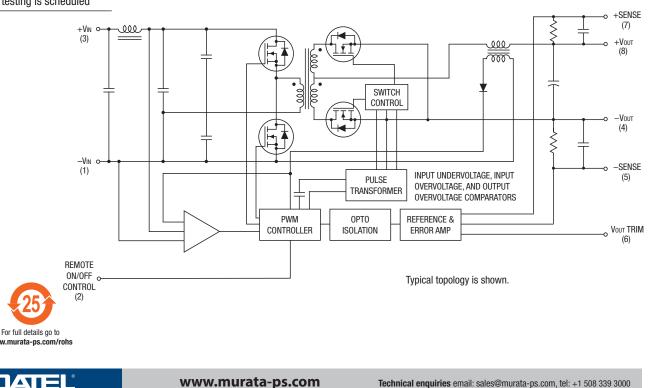
UCE's achieve these impressive mechanical and environmental specs while delivering excellent electrical performance in a through-hole package. Overall noise is typically 50 mV pk-pk (low voltage models) with fast step response. These converters offer tight output regulation and high stability even with no load. The unit is fully protected against input undervoltage, output overcurrent and short circuit. An on-board temperature sensor shuts

#### SIMPLIFIED BLOCK DIAGRAM

down the converter if thermal limits are reached. "Hiccup" output protection automatically restarts the converter when the fault is removed.

A convenient remote On/Off control input enables phased startup and shutdown in multi-voltage applications. To compensate for longer wiring and to retain output voltage accuracy at the load, UCEs employ a Sense input to dynamically correct for ohmic losses. A trim input may be connected to a user's adjustment potentiometer or trim resistors for output voltage calibration. The UCE will tolerate substantial capacitive loading for bypass-cap applications.

UCEs include industry-standard safety certifications and BASIC I/O insulation provides input/output isolation to 2250V. Radiation emission testing is performed to widely-accepted EMC standards.

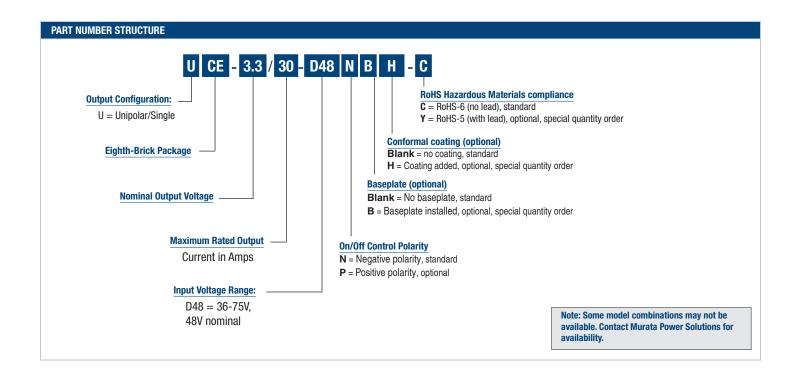


## **UCE Series**

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PERFORMANCE SPECI	FICATIONS	AND ORI	DERING G	UIDE											
			Output Input							out	ıt				
	Vout lout Po		Power	Ripple & Noise (mVp-p)		Regulation		VIN Nom.	Range	lın, no load	lın, full load	Efficiency		Pac	kage
Model Family	(V)	(A)	(W)	Тур.	Max.	Line	Load	(V)	(V)	(mA)	(A)	Min.	Тур.	Case	Pinout
UCE-1.2/40-D48N-C	1.2	40	48			I	Please contac	t Murata P	ower Solut	ions for fu	rther inforn	nation.			
UCE-1.5/20-D48N-C	1.5	20	30	50	100	±0.15%	±0.3%	48	36-75	50	0.72	85%	87%	C56	P32
UCE-1.5/40-D48N-C	1.5	40	60				Please contac	t Murata P	ower Solut	ions for fu	rther inforn	nation.			
UCE-1.8/30-D48N-C	1.8	30	54	30	80	±0.125%	±0.25%	48	36-75	45	1.28	87%	88%	C56	P32
UCE-2.5/20-D48N-C	2.5	20	50				Please contac	t Murata D	owor Colut	iono for fu	rthor inform	action			
UCE-2.5/40-D48N-C	2.5	40	100				riease contat	, i iviui ala Fi	ower Solut	10115 101 10		lauon.			
UCE-3.3/15-D48N-C	3.3	15	49.5	50	100	±0.125%	±0.25%	48	26.75	60	1.15	86%	90%	C56	P32
UCE-3.3/30-D48N-C	3.3	30	99	50	100	±0.1%	±0.2%	48	36-75	60	2.27	89%	91%	000	FJZ
UCE-5/10-D48N-C	5	10	50				Please contac	t Murata D	owor Solut	ione for fu	rthor inform	nation			
UCE-5/20-D48N-C	5	20	100				TEASE CUIILAL	r iviui ala Fi		10115 101 10		iauvil.			
UCE-12/4.2-D48N-C	12	4.2	50.4	150	300	±0.125%	±0.25%	48	48 36-75	5 50	1.14	86%	92%	C56	P32
UCE-12/8.3-D48N-C	12	8.3	99.6	200	500	±0.125%	±0.23%				2.31	0070	90%	030	1.52

① Please refer to the model number structure for additional ordering part numbers and options





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INPUT CHARACTER	RISTICS														
		Start-up	Under-	Reflected			Inpu	t Current					Re	mote On/Off	Control
Model Family	Vin (Volts)	thresh- old Min. (A)	voltage Shut- down (V)	(back) Ripple Current (mA)	Full Load Condi- tions	Inrush Tran- sient A <sup>2</sup> sec	Output Short Circuit (mA)	No Load (mA)	Low Line (Vin=min.) (A)	Standby Mode (mA)		Reverse Polarity Protec- tion		Positive Logic "P" Model Suffix	Negative Logic "N" Model Suffix
-	(10110)	(1)		(IIIA)		A 000	(IIIA)	(IIIA)		(IIIA)	Type	uon	(IIIA)	ounix	ounix
UCE-1.5/20-D48	_		32						0.97					OFF=Ground	OFF=open or
UCE-1.8/30-D48	32.5						1.72					pin to	+2.5V to		
UCE-3.3/15-D48	48	34	32	10-30, model	See	0.05	50-150,	45-60,	1.54	1-10, model L-C dependent		See notes	1.0	+1V max.	+15V max.
UCE-3.3/30-D48	40	40 54	32	dependent	ordering nt quide	A <sup>2</sup> sec	model dependent	model dependent	3.06					ON=open or	ON=Ground
UCE-12/4.2-D48			32	dopondont	guiuo	guide	aoponaona	aoponaone	1.52					+3.5 to +15V	
UCE-12/8.3-D48			32						3.07					max.	max.

OUTPUT CHARACT	ERISTIC	s									
Model Family	Vout V	Vout Accuracy 50% Load % of Vnom	Capacitive Loading Max. Low ESR <0.02Ω Max. resistive load μF	Adjustment Range	Temperature Coefficient	Minimum Loading	Remote Sense Compen- sation	Ripple/ Noise (20 MHz bandwidth)	Line/Load Regulation	Efficiency	Current Limit Inception 98% of Vout, after warmup A
UCE-1.5/20-D48	1.5		10,000		±0.02% of	No minimum	mum . 10%		24.5		
UCE-1.8/30-D48	1.8		10,000								36
UCE-3.3/15-D48	3.3	. 10/	10,000 max.	-10 to +10% of				See ordering guide		do	24
UCE-3.3/30-D48	3.3	±1%	10,000	Vnom.	Vout range per °C	load	+10%			ue	35
UCE-12/4.2-D48	12		1000	viioiii.	por o						5.5
UCE-12/8.3-D48	12		1000								12

#### ISOLATION CHARACTERISTICS

Model Family	Input to Output Min. V	Input to baseplate Min. V	Baseplate to output Min. V	lsolation Resistance MΩ	Isolation Capacitance pF	Isolation Safety Rating	
UCE-1.5/20-D48		100					
UCE-1.8/30-D48				10	1000	Basic Insulation	
UCE-3.3/15-D48	0050	0050 1500	1500				
UCE-3.3/30-D48	2250	1500	1500	100	1000		
UCE-12/4.2-D48				100			
UCE-12/8.3-D48							

MISCELLANEOUS CHARACTERISTICS											
Model Family	Calculated MTBF <sup>4</sup>	Operating Temperature Range with derating (°C)	Operating Case Temperature (no derating)	Storage Temperature Range (°C)	Thermal Protection/ Shutdown (°C)	Short Circuit Current (A)	Overvoltage Protection <sup>12</sup> (V) Via magnetic feedback (V)	Short Circuit Protection Method	Short Circuit Duration <sup>16</sup>	Relative Humidity (non-condensing)	
UCE-1.5/20-D48					120		1.95	Current	Continuous,	to +85°C/85%	
UCE-1.8/30-D48							2.8 V. max	limiting, hiccup autorestart. Remove overload for			
UCE-3.3/15-D48	твс	-40 to +85	-40 to +120	-55 to	120	5	4.25		output shorted to		
UCE-3.3/30-D48	IBC	-40 10 +05	-40 10 +120	+125	125	5	4.20		ground. No	10 +05 0/05 /0	
UCE-12/4.2-D48							14.5		damage.		
UCE-12/8.3-D48					120		14.5	recovery.		·	

**DATEL** 

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DYNAMIC CHARACTERISTICS					
		Start-			
	Dynamic Load Response (50-75-50%	VIN to VOUT regulated (Max.)	Remote On/ Off to Vout regulated (Max.)	Switching Frequency	
Model Family	load step)	m	KHz		
UCE-1.5/20-D48				480	
UCE-1.8/30-D48	30-200 µSec			400 ±40	
UCE-3.3/15-D48	to $\pm 1\%$ of final	5-50, model	5-50, model	480 ±50	
UCE-3.3/30-D48	value, model	dependent	dependent	380 ±40	
UCE-12/4.2-D48	dependent			200 10	
UCE-12/8.3-D48				200 ±10	

#### ABSOLUTE MAXIMUM RATINGS

Input Voltage:	
Continuous: 48 Volt input models	75 Volts
Transient (100 mSec. Max.) 48 Volt input models	100 Volts
On/Off Control	+15 Volts
Input Reverse Polarity Protection	5 Amps, 10 sec. max.
Output Overvoltage Protection	Magnetic feedback. See specifications.
Output Current *	Current-limited. Devices can withstand sustained short circuit without damage.
Storage Temperature	-40 to +125°C.
Lead Temperature	+280°C, 10 seconds max.
Absolute maximums are stress ratings. Exposu	-

Absolute maximums are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended.

Note: Not all model combinations are available.

#### PERFORMANCE SPECIFICATION NOTES

(1) All models are tested and specified with external 1||10  $\mu$ F ceramic/tantalum output capacitors and no external input capacitor. All capacitors are low ESR types. These capacitors are necessary to accommodate our test equipment and may not be required to achieve specified performance in your applications. All models are stable and regulate within spec under no-load conditions.

General conditions for Specifications are +25 deg.C,  $V_{IN}$  = nominal,  $V_{OUT}$  = nominal, full load. Adequate airflow must be supplied for extended testing under power.

(2) Input Ripple Current is tested and specified over a 5 Hz to 20 MHz bandwidth. Input filtering is  $C_{IN} = 33 \mu$ F, 100V tantalum,  $C_{BUS} = 220 \mu$ F, 100V electrolytic,  $L_{BUS} = 12 \mu$ H.

(3) Note that Maximum Power Derating curves indicate an average current at nominal input voltage. At higher temperatures and/or lower airflow, the DC/DC converter will tolerate brief full current outputs if the total RMS current over time does not exceed the Derating curve. All Derating curves are presented at sea level altitude. Be aware of reduced power dissipation with increasing density altitude.

(4) Mean Time Before Failure is calculated using the Telcordia (Belcore) SR-332 Method 1, Case 3, ground fixed conditions, Tpcboard=+25 deg.C, full output load, natural air convection.

(5) The On/Off Control is normally controlled by a switch. But it may also be driven with external logic or by applying appropriate external voltages which are referenced to Input Common. The On/Off Control Input should use either an open collector or open drain transistor.

(6) Short circuit shutdown begins when the output voltage degrades approximately 2% from the selected setting.

(7) The outputs are not intended to sink appreciable reverse current. This may damage the outputs.

(8) Output noise may be further reduced by adding an external filter. See I/O Filtering and Noise Reduction.

(9) All models are fully operational and meet published specifications, including "cold start" at  $-40^{\circ}$ C.

(10) Regulation specifications describe the deviation as the line input voltage or output load current is varied from a nominal midpoint value to either extreme.

(11) Alternate pin length and/or other output voltages are available under special quantity order.

(12) Output current limit is non-latching. When the overcurrent fault is removed, the converter will immediately recover.

(13) Do not exceed maximum power specifications when adjusting the output trim.

(14) At zero output current, the output may contain low frequency components which exceed the ripple specification. The output may be operated indefinitely with no load.

(15) If reverse polarity is accidentally applied to the input, a body diode will become forward biased and will conduct considerable current. To ensure reverse input protection with full output load, always connect an external input fuse in series with the  $+V_{\rm IN}$  input. Use approximately twice the full input current rating with nominal input voltage.



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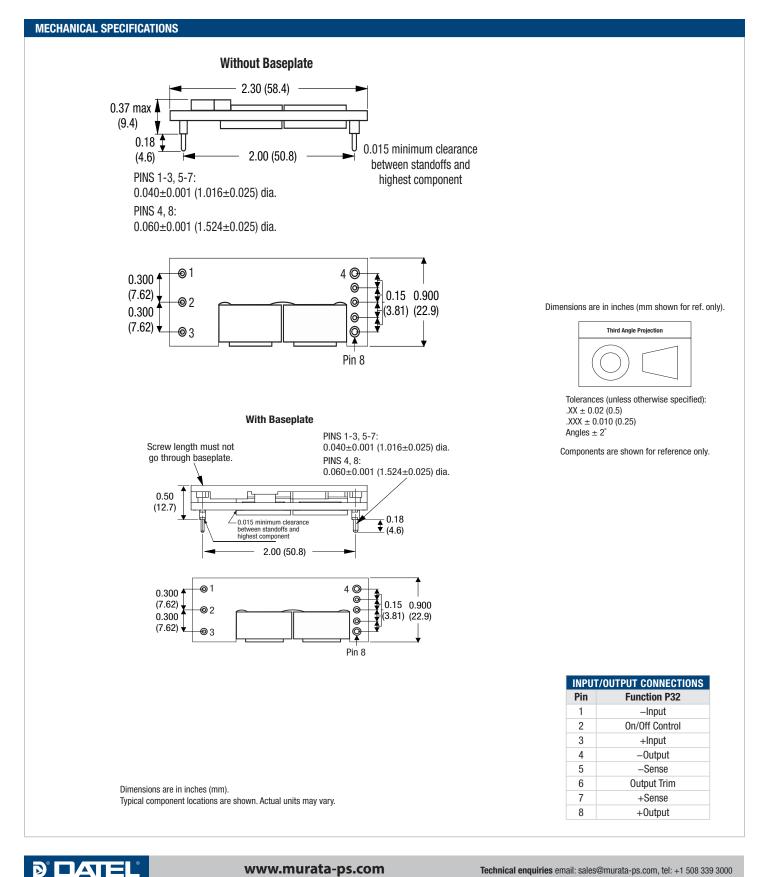
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PHYSICAL CH	IARACTERISTICS	
Outline dimension	ons	See mechanical specs (below)
Pin material		Copper alloy
Pin diameter		0.04/0.062" (1.016/1.524mm)
Pin finish		Nickel underplate with gold overplate
	UCE-1.5/20-D48	0.67 ounces (19 grams)
Woight	UCE-1.8/30-D48, UCE-12/4.2-D48	0.71 ounces (20 grams)
Weight	UCE-3.3/15-D48	1 ounce (28 grams)
	UCE-3.3/30-D48, UCE-12/8.3-D48	0.81 ounces (23 grams)
Electromagnetic interference (conducted and radiated) (external filter required)		FCC part 15, class B, EN55022
Safety		UL/cUL 60950-1, CSA-C22.2 No. 60950-1, IEC/EN 60950-1





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### Isolated, High-Density, Eighth-Brick 100W DC/DC Converters

#### **Trim Equations**

Trim Down Connect trim resistor between trim pin and –Sense Trim Up Connect trim resistor between trim pin and +Sense

$$\mathsf{R}_{\mathsf{TrimDn}}\left(\mathsf{k}\;\Omega\right) = \frac{5.11}{\Delta} - 10.22$$

 $R_{\text{TrimUp}}\left(k\;\Omega\right)=\frac{5.11\times\text{V}_{\text{NOM}}\times(1\!+\!\Delta)}{1.225\times\Delta}-\frac{5.11}{\Delta}\;-10.22$ 

#### Where,

$$\label{eq:linear} \begin{split} \Delta &= \mid (V_{\text{NOM}} - V_{\text{OUT}}) / V_{\text{NOM}} \mid \\ V_{\text{NOM}} \text{ is the nominal, untrimmed output voltage.} \\ V_{\text{OUT}} \text{ is the desired new output voltage.} \end{split}$$

Do not exceed the specified trim range or maximum power ratings when adjusting trim. Use 1% precision resistors mounted close to the converter on short leads.

#### **Trim Circuits**

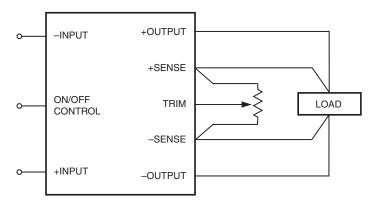


Figure A. Trim Connections Using A Trimpot

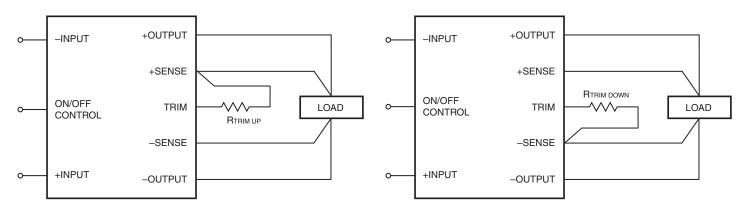


Figure B. Trim Connections To Increase Output Voltages

Figure C. Trim Connections To Decrease Output Voltages

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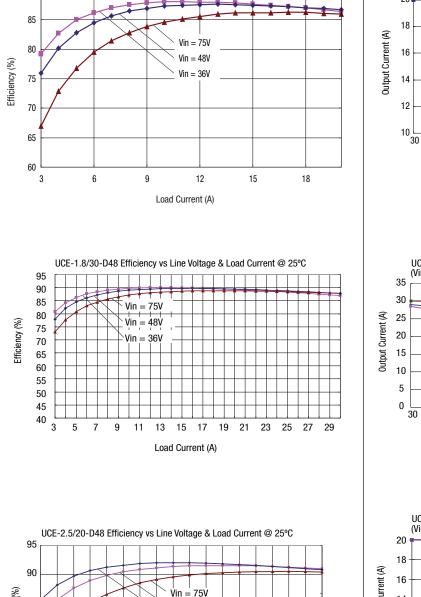


UCE-1.5/20-D48 Efficiency vs Line Voltage & Load Current @ 25°C

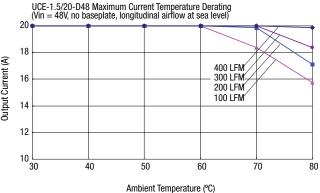
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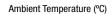
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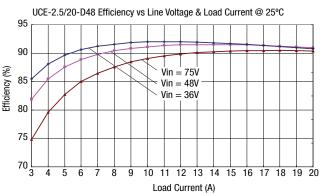
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#### **Typical Performance Curves**







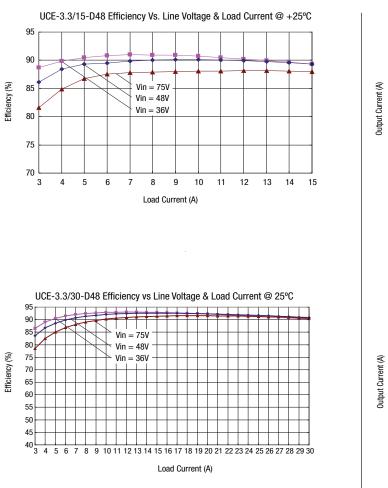
UCE-2.5/20-D48 Maximum Current Temperature Derating (Vin = 48V, with baseplate, longitudinal airflow at sea level) 300 LFM Output Current (A) 200 LFM 100 LFM 14 Natural Convection 12 10 30 40 50 60 70 80 Ambient Temperature (°C)

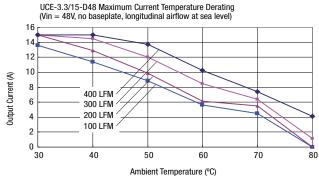
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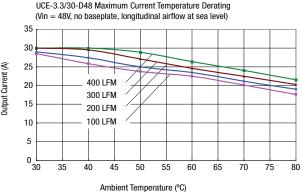
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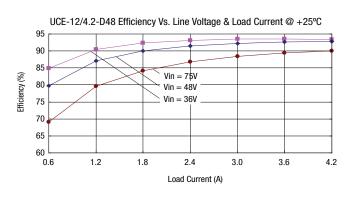
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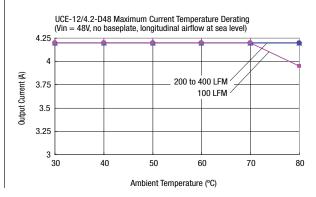










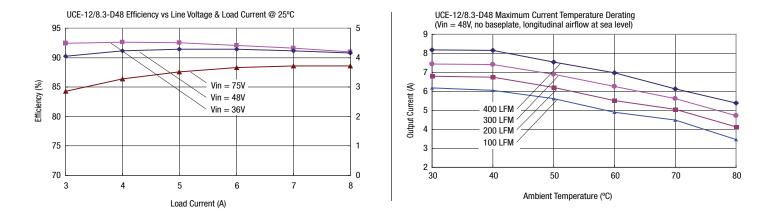


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### muRata Ps Murata Power Solutions

Murata Power Solutions, Inc.

11 Cabot Boulevard, Mansfield, MA 02048-1151 U.S.A.

Tel: (508) 339-3000 (800) 233-2765 Fax: (508) 339-6356

www.murata-ps.com email: sales@murata-ps.com ISO 9001 REGISTERED 07/03/08

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USA:	Mansfield (MA), Tel: (508) 339-3000, email: sales@murata-ps.com
Canada:	Toronto, Tel: (866) 740-1232, email: toronto@murata-ps.com
UK:	Milton Keynes, Tel: +44 (0)1908 615232, email: mk@murata-ps.com
France:	Montigny Le Bretonneux, Tel: +33 (0)1 34 60 01 01, email: france@murata-ps.com
Germany:	München, Tel: +49 (0)89-544334-0, email: munich@murata-ps.com
Japan:	Tokyo, Tel: 3-3779-1031, email: sales_tokyo@murata-ps.com Osaka, Tel: 6-6354-2025, email: sales_osaka@murata-ps.com Website: www.murata-ps.jp
China:	Shanghai, Tel: +86 215 027 3678, email: shanghai@murata-ps.com Guangzhou, Tel: +86 208 221 8066, email: guangzhou@murata-ps.com

Technical enquiries email: sales@murata-ps.com, tel: +1 508 339 3000

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