

# Low Cost, Low Power Video Op Amp

**AD818** 

#### **FEATURES**

**Low Cost** 

**Excellent Video Performance** 

55 MHz 0.1 dB Bandwidth (Gain = +2)

0.01% and 0.05° Differential Gain and Phase Errors High Speed

130 MHz Bandwidth (3 dB, G = +2)

100 MHz Bandwidth (3 dB, G+ = -1)

500 V/μs Slew Rate

80 ns Settling Time to 0.01% ( $V_0 = 10 \text{ V Step}$ )

**High Output Drive Capability** 

50 mA Minimum Output Current

**Ideal for Driving Back Terminated Cables** 

Flexible Power Supply

Specified for Single (+5 V) and Dual ( $\pm 5$  V to  $\pm 15$  V) Power Supplies

Low Power: 7.5 mA Max Supply Current Available in 8-Lead SOIC and 8-Lead PDIP

#### GENERAL DESCRIPTION

The AD818 is a low cost video op amp optimized for use in video applications that require gains equal to or greater than +2 or -1. The AD818's low differential gain and phase errors, single supply functionality, low power, and high output drive make it ideal for cable driving applications such as video cameras and professional video equipment.

With video specs like 0.1 dB flatness to 55 MHz and low differential gain and phase errors of 0.01% and 0.05°, along with 50 mA of output current, the AD818 is an excellent choice for

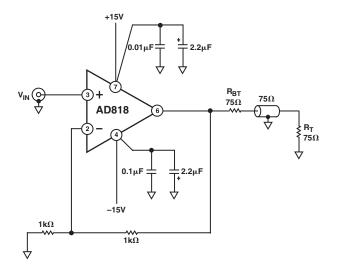
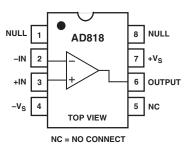


Figure 1. Video Line Driver

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#### CONNECTION DIAGRAM

8-Lead Plastic Mini-DIP (N) and SOIC (R) Packages



any video application. The 130 MHz 3 dB bandwidth (G = +2) and 500 V/ $\mu$ s slew rate make the AD818 useful in many high speed applications including video monitors, CATV, color copiers, image scanners, and fax machines.

The AD818 is fully specified for operation with a single +5 V power supply and with dual supplies from  $\pm 5$  V to  $\pm 15$  V. This power supply flexibility, coupled with a very low supply current of 7.5 mA and excellent ac characteristics under all power supply conditions, make the AD818 the ideal choice for many demanding yet power sensitive applications.

The AD818 is a voltage feedback op amp and excels as a gain stage in high speed and video systems (gain  $\geq 2$ , or gain  $\leq -1$ ). It achieves a settling time of 45 ns to 0.1%, with a low input offset voltage of 2 mV max.

The AD818 is available in low cost, small 8-lead PDIP and SOIC packages.

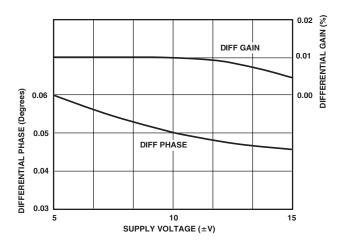


Figure 2. Differential Gain and Phase vs. Supply

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## $\begin{tabular}{ll} AD818-SPECIFICATIONS & (@T_A=25^\circ C, unless otherwise noted.) \end{tabular}$

				<b>AD818A</b>		
Parameter	Conditions	V <sub>S</sub>	Min	Typ	Max	Unit
DYNAMIC PERFORMANCE						
−3 dB Bandwidth	Gain = +2	±5 V	70	95		MHz
5 dD Dandwiddi		±15 V	100	130		MHz
		0 V, +5 V	40	55		MHz
	Gain = -1	±5 V	50	70		MHz
	Gaiii = -1		70			MHz
		±15 V		100		
		0 V, +5 V	30	50		MHz
Bandwidth for 0.1 dB Flatness	Gain = +2	±5 V	20	43		MHz
	$C_C = 2 pF$	±15 V	40	55		MHz
		0 V, +5 V	10	18		MHz
	Gain = −1	±5 V	18	34		MHz
	$C_C = 2 pF$	±15 V	40	72		MHz
	-C -F-	0 V, +5 V	10	19		MHz
Full Power Bandwidth*	$V_{OUT} = 5 \text{ V p-p}$	0 1, 13 1	10	17		11112
Tuli I owel Balldwidth		+ <b>5</b> 37		25.5		MHz
	$R_{LOAD} = 500 \Omega$	±5 V		25.5		MHZ
	$V_{OUT} = 20 \text{ V p-p}$					
	$R_{LOAD} = 1 \text{ k}\Omega$	±15 V		8.0		MHz
Slew Rate	$R_{LOAD} = 1 k\Omega$	±5 V	350	400		V/µs
	Gain = -1	±15 V	450	500		V/µs
		0 V, +5 V	250	300		V/μs
Settling Time to 0.1%	-2.5 V to +2.5 V	±5 V		45		ns
	$0 \text{ V}-10 \text{ V Step, } A_{\text{V}} = -1$	±15 V		45		ns
011 FET 0.010/	- ,					
Settling Time to 0.01%	-2.5 V to +2.5 V	±5 V		80		ns
	$0 \text{ V}-10 \text{ V Step, } A_{\text{V}} = -1$	±15 V		80		ns
Total Harmonic Distortion	$F_C = 1 \text{ MHz}$	±15 V		63		dB
Differential Gain Error	NTSC	±15 V		0.005	0.01	%
$(R_L = 150 \Omega)$	Gain = +2	±5 V		0.01	0.02	%
(14L 130 11)		0 V, +5 V		0.08		%
Differential Phase Error	NTSC	±15 V		0.045	0.09	Degrees
	Gain = +2	±5 V		0.045	0.09	Degrees
$(R_L = 150 \Omega)$	Gain = +2				0.09	_
		0 V, +5 V		0.1		Degrees
Cap Load Drive				10		pF
INPUT OFFSET VOLTAGE		±5 V to ±15 V		0.5	2	mV
	$T_{MIN}$ to $T_{MAX}$				3	mV
Offset Drift	1 MIN to 1 MAX			10		μV/°C
		1537 13537				
INPUT BIAS CURRENT		±5 V, ±15 V		3.3	6.6	μA
	$T_{MIN}$				10	μA
	$T_{MAX}$				4.4	μA
INPUT OFFSET CURRENT		±5 V, ±15 V		25	300	nA
INI OT OFFSET CORRENT	T to T	± , , , ⊥ 1 )		43		
25.6	$T_{MIN}$ to $T_{MAX}$			0.0	500	nA
Offset Current Drift				0.3		nA/°C
OPEN-LOOP GAIN	$V_{OUT} = \pm 2.5 \text{ V}$	±5 V				
OPEN-LOUP GAIN	$R_{LOAD} = 500 \Omega$		3	5		V/mV
			3	J		
	$T_{MIN}$ to $T_{MAX}$		2	4		V/mV
	$R_{LOAD} = 150 \Omega$		2	4		V/mV
	$V_{OUT} = \pm 10 \text{ V}$	±15 V				
	$R_{LOAD} = 1 k\Omega$		6	9		V/mV
	$T_{MIN}$ to $T_{MAX}$		3			V/mV
	$V_{OUT} = \pm 7.5 \text{ V}$	±15 V				
	$R_{LOAD} = 150 \Omega$					
	10AD - 100 22		2	5		V/mV
	(50 m A Output)					V/IIIV
	(50 mA Output)		3			V/111 V
COMMON-MODE REJECTION		±5 V		100		
COMMON-MODE REJECTION	(50 mA Output) $V_{CM} = \pm 2.5 \text{ V}$ $V_{CM} = \pm 12 \text{ V}$	±5 V ±15 V	82 86			dB dB

			AD818A			
Parameter	Conditions	$V_{S}$	Min	Typ	Max	Unit
POWER SUPPLY REJECTION	$V_S = \pm 5 \text{ V to } \pm 15 \text{ V}$ $T_{MIN} \text{ to } T_{MAX}$		80 80	90		dB dB
INPUT VOLTAGE NOISE	f = 10 kHz	±5 V, ±15 V		10		$nV/\sqrt{Hz}$
INPUT CURRENT NOISE	f = 10 kHz	±5 V, ±15 V		1.5		pA/√ <del>Hz</del>
INPUT COMMON-MODE VOLTAGE RANGE		±5 V ±15 V 0 V, +5 V	+3.8 -2.7 +13 -12 +3.8 +1.2	+4.3 -3.4 +14.3 -13.4 +4.3 +0.9		V V V V
OUTPUT VOLTAGE SWING Output Current	$R_{LOAD} = 500 \Omega$ $R_{LOAD} = 150 \Omega$ $R_{LOAD} = 1 k\Omega$ $R_{LOAD} = 500 \Omega$ $R_{LOAD} = 500 \Omega$	±5 V ±5 V ±15 V ±15 V 0 V, +5 V ±15 V ±5 V 0 V, +5 V	3.3 3.2 13.3 12.8 1.5, 3.5 50 50 30	3.8 3.6 13.7 13.4		±V ±V ±V V mA mA mA
Short-Circuit Current		±15 V		90		mA
INPUT RESISTANCE				300		kΩ
INPUT CAPACITANCE				1.5		pF
OUTPUT RESISTANCE	Open Loop			8		Ω
POWER SUPPLY Operating Range Quiescent Current	Dual Supply Single Supply $T_{MIN}$ to $T_{MAX}$ $T_{MIN}$ to $T_{MAX}$	±5 V ±5 V ±15 V ±15 V	±2.5 +5	7.0 7.0	±18 +36 7.5 7.5 7.5 7.5	V V mA mA mA

<sup>\*</sup>Full power bandwidth = slew rate/( $2\pi V_{PEAK}$ ).

Specifications subject to change without notice.

### **AD818**

#### ABSOLUTE MAXIMUM RATINGS1

Supply Voltage
Internal Power Dissipation <sup>2</sup>
Plastic (N) See Derating Curves
Small Outline (R) See Derating Curves
Input Voltage (Common Mode) $\dots \pm V_S$
Differential Input Voltage ±6 V
Output Short-Circuit Duration See Derating Curves
Storage Temperature Range (N, R)65°C to +125°C
Operating Temperature Range40°C to +85°C
Lead Temperature Range (Soldering 10 sec) 300°C
NOTES

<sup>1</sup>Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

<sup>2</sup>Specification is for device in free air: 8-lead plastic package,  $\theta_{JA}$  = 90°C/W; 8-lead SOIC package,  $\theta_{JA}$  = 155°C/W.

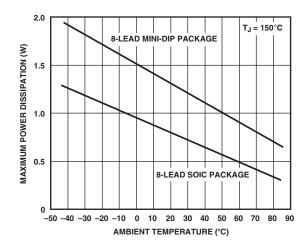


Figure 3. Maximum Power Dissipation vs. Temperature for Different Package Types

#### **ORDERING GUIDE**

Model	Temperature Range	Package Description	Package Option
AD818AN	–40°C to +85°C	8-Lead Plastic PDIP	N-8
AD818AR	−40°C to +85°C	8-Lead Plastic SOIC	R-8
AD818AR-REEL	−40°C to +85°C	13" Tape and Reel	R-8
AD818AR-REEL7	−40°C to +85°C	7" Tape and Reel	R-8

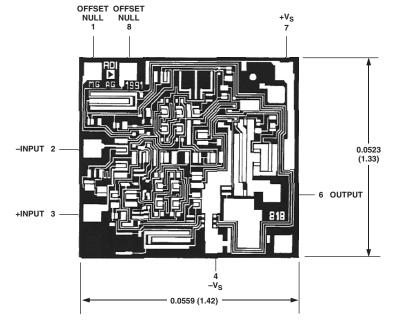
#### CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the AD818 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



#### **METALLIZATION PHOTOGRAPH**

Dimensions shown in inches and (mm)

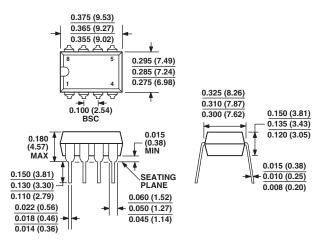


#### **OUTLINE DIMENSIONS**

#### 8-Lead Plastic Dual In-Line Package [PDIP]

(N-8)

Dimensions shown in inches and (millimeters)

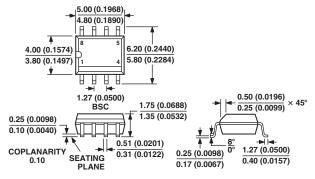


COMPLIANT TO JEDEC STANDARDS MO-095AA
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS
(IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR
REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN

#### 8-Lead Standard Small Outline Package [SOIC]

(R-8)

Dimensions shown in millimeters and (inches)



COMPLIANT TO JEDEC STANDARDS MS-012AA
CONTROLLING DIMENSIONS ARE IN MILLIMETERS; INCH DIMENSIONS
(IN PARENTHESES) ARE ROUNDED-OFF MILLIMETER EQUIVALENTS FOR
REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN