

LM5574 SIMPLE SWITCHER® 75V, 0.5A Step-Down Switching Regulator

General Description

The LM5574 is an easy to use SIMPLE SWITCHER® buck regulator which allows design engineers to design and optimize a robust power supply using a minimum set of components. Operating with an input voltage range of 6 - 75V, the LM5574 delivers 0.5A of continuous output current with an integrated 750m $\!\Omega$ N-Channel MOSFET. The regulator utilizes an Emulated Current Mode architecture which provides inherent line regulation, tight load transient response, and ease of loop compensation without the usual limitation of lowduty cycles associated with current mode regulators. The operating frequency is adjustable from 50kHz to 500kHz to allow optimization of size and efficiency. To reduce EMI, a frequency synchronization pin allows multiple IC's from the LM(2)557x family to self-synchronize or to synchronize to an external clock. The LM5574 guarantees robustness with cycle-by-cycle current limit, short-circuit protection, thermal shut-down, and remote shut-down. The device is available in a TSSOP-16 package. The LM5574 is supported by the full suite of WEBENCH® On-Line design tools.

Simplified Application Schematic

Features

- Integrated 75V, 750mΩ N-channel MOSFET
- Ultra-wide input voltage range from 6V to 75V
- Adjustable output voltage as low as 1.225V
- 1.5% feedback reference accuracy
- Operating frequency adjustable between 50kHz and 500kHz with single resistor
- Master or slave frequency synchronization
- Adjustable soft-start
- Emulated current mode control architecture
- Wide bandwidth error amplifier
- Built-in protection

Package

TSSOP-16



Connection Diagram

LM5574



Ordering Information

Order Number	Package Type	NSC Package Drawing	Supplied As
LM5574MT	TSSOP-16	MTC16	92 Units in Rail
LM5574MTX	TSSOP-16	MTC16	2500 Units on Tape and Reel

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

V _{IN} to GND	76V
BST to GND	90V
PRE to GND	76V
SW to GND (Steady State)	-1.5V
BST to V _{CC}	76V
SD, V _{CC} to GND	14V

BST to SW14VOUT to GNDLimited to VinSYNC, SS, FB, RAMP to GND7VESD Rating (Note 2)Human Body ModelStorage Temperature Range-65°C to +150°C

Operating Ratings (Note 1)

V _{IN}	6V to 75V
Operation Junction Temperature	-40°C to + 125°C

Electrical Characteristics Specifications with standard typeface are for $T_J = 25^{\circ}$ C, and those with **boldface** type apply over full **Operating Junction Temperature range**. $V_{IN} = 48V$, $R_T = 32.4k\Omega$ unless otherwise stated. (Note 3)

STARTUP REGUL VccReg V V	ATOR /cc Regulator Output		-			
VccReg V	/cc Regulator Output					
\ \ \			6.85	7.15	7.45	V
V	/cc LDO Mode turn-off			9		V
	/cc Current Limit	Vcc = 0V		25		mA
VCC SUPPLY						
V	/cc UVLO Threshold	(Vcc increasing)	5.03	5.35	5.67	V
V	/cc Undervoltage Hysteresis			0.35		V
E	Bias Current (lin)	FB = 1.3V		3.7	4.5	mA
5	Shutdown Current (lin)	SD = 0V		57	85	μA
SHUTDOWN THRE	ESHOLDS					
5	Shutdown Threshold	(SD Increasing)	0.47	0.7	0.9	V
5	Shutdown Hysteresis			0.1		V
5	Standby Threshold	(Standby Increasing)	1.17	1.225	1.28	V
5	Standby Hysteresis			0.1		V
5	SD Pull-up Current Source			5		μA
SWITCH CHARAC	TERSICS					
E	Buck Switch Rds(on)			750	1500	mΩ
E	BOOST UVLO			4		V
E	300ST UVLO Hysteresis			0.56		V
F	Pre-charge Switch Rds(on)			70		Ω
F	Pre-charge Switch on-time			250		ns
CURRENT LIMIT						
C	Cycle by Cycle Current Limit	RAMP = 0V	0.6	0.7	0.8	A
C	Cycle by Cycle Current Limit Delay	RAMP = 2.5V		75		ns
SOFT-START						
5	SS Current Source		7	10	14	μA
OSCILLATOR						
F	Frequency1		180	200	220	kHz
F	Frequency2	$R_T = 11k\Omega$	425	485	545	kHz
5	SYNC Source Impedance			11		kΩ
5	SYNC Sink Impedance			110		Ω
5	SYNC Threshold (falling)			1.3		V
5	SYNC Frequency	$R_T = 11k\Omega$	550			kHz
5	SYNC Pulse Width Minimum		15			ns
	-	<u> </u> !	-			-

4

Symbol	Parameter	Conditions	Min	Tvp	Мах	Units
RAMP GENER	ATOR		_!	71	_	
	Ramp Current 1	Vin = 60V, Vout=10V	467	550	633	μA
	Ramp Current 2	Vin = 10V, Vout=10V	36	50	64	μA
PWM COMPAR	ATOR		-!			
	Forced Off-time		416	500	575	ns
	Min On-time			80		ns
	COMP to PWM Comparator Offset			0.7		V
ERROR AMPLI	FIER	·	•		•	
	Feedback Voltage	Vfb = COMP	1.207	1.225	1.243	V
	FB Bias Current			17		nA
	DC Gain			70		dB
	COMP Sink / Source Current		3			mA
	Unity Gain Bandwidth			3		MHz
DIODE SENSE	RESISTANCE					
D _{SENSE}				250		mΩ
THERMAL SHU	ITDOWN		_!			
Tsd	Thermal Shutdown Threshold			165		°C
	Thermal Shutdown Hysteresis			25		°C
THERMAL RES	SISTANCE		-1	1	•	1
θ _{JC}	Junction to Case			30		°C/W
θ,JA	Junction to Ambient			90		°C/W
					1	

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Ratings are conditions under which operation of the device is intended to be functional. For guaranteed specifications and test conditions, see the Electrical Characteristics.

Note 2: The human body model is a 100pF capacitor discharged through a $1.5k\Omega$ resistor into each pin.

Note 3: Min and Max limits are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. Limits are used to calculate National's Average Outgoing Quality Level (AOQL).

Typical Performance Characteristics





0

25

50

TEMPERATURE (°C)

75

100 125



LM5574

0.990 -50 -25



Downloaded from **Elcodis.com** electronic components distributor