## LM2596

## SIMPLE SWITCHER ${ }^{\circledR}$ Power Converter 150 kHz

 3A Step-Down Voltage Regulator
## General Description

The LM2596 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving a 3A load with excellent line and load regulation. These devices are available in fixed output voltages of $3.3 \mathrm{~V}, 5 \mathrm{~V}, 12 \mathrm{~V}$, and an adjustable output version.
Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensationt, and a fixed-frequency oscillator.
The LM2596 series operates at a switching frequency of 150 kHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Available in a standard 5-lead TO-220 package with several different lead bend options, and a 5-lead TO-263 surface mount package.
A standard series of inductors are available from several different manufacturers optimized for use with the LM2596 series. This feature greatly simplifies the design of switch-mode power supplies.
Other features include a guaranteed $\pm 4 \%$ tolerance on output voltage under specified input voltage and output load conditions, and $\pm 15 \%$ on the oscillator frequency. External shutdown is included, featuring typically $80 \mu \mathrm{~A}$ standby current. Self protection features include a two stage frequency reducing current limit for the output switch and an over temperature shutdown for complete protection under fault conditions.

## Typical Application (Fixed Output Voltage <br> Versions)



01258301

## Features

- $3.3 \mathrm{~V}, 5 \mathrm{~V}$, 12 V , and adjustable output versions
- Adjustable version output voltage range, 1.2 V to 37 V $\pm 4 \%$ max over line and load conditions
- Available in TO-220 and TO-263 packages
- Guaranteed 3A output load current
- Input voltage range up to 40 V
- Requires only 4 external components
- Excellent line and load regulation specifications
- 150 kHz fixed frequency internal oscillator
- TTL shutdown capability
- Low power standby mode, $\mathrm{I}_{\mathrm{Q}}$ typically $80 \mu \mathrm{~A}$
- High efficiency
- Uses readily available standard inductors
- Thermal shutdown and current limit protection


## Applications

- Simple high-efficiency step-down (buck) regulator
- On-card switching regulators
- Positive to negative converter

Note: †Patent Number 5,382,918.

## Connection Diagrams and Ordering Information

Bent and Staggered Leads, Through Hole Package
5-Lead TO-220 (T)


01258302
Order Number LM2596T-3.3, LM2596T-5.0,
LM2596T-12 or LM2596T-ADJ
See NS Package Number T05D


Order Number LM2596S-3.3, LM2596S-5.0,
LM2596S-12 or LM2596S-ADJ
See NS Package Number TS5B

| Absolute Maximum Ratings (Note 1) |  |  | Human Body Model (Note 2) |  |  | 2 kV |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. |  |  | Lead Temperature S Package |  |  |  |
|  |  |  | Vapor Phase (60 sec.) |  |  | $+215^{\circ} \mathrm{C}$ |
| Maximum Supply Voltage |  | 45 V | Infrared (10 sec.) |  |  | $+245^{\circ} \mathrm{C}$ |
| $\overline{\text { ON /OFF Pin Input Voltage }}$ |  | $-0.3 \leq \mathrm{V} \leq+25 \mathrm{~V}$ | T Package (Soldering, 10 sec.) |  |  | $+260^{\circ} \mathrm{C}$ |
| Feedback Pin Voltage |  | $-0.3 \leq \mathrm{V} \leq+25 \mathrm{~V}$ | Maximum Junction Temperature |  |  | $+150^{\circ} \mathrm{C}$ |
| (Steady State) | Output Voltage to Ground |  |  |  |  |  |
| Power Dissipation |  | Internally limited | Temperature Range |  | $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+125^{\circ} \mathrm{C}$ |  |
|  |  | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |  |  |  |  |
|  | ESD Susceptibility |  | Supply Voltage |  | 4.5 V to 40 V |  |
| LM2596-3.3 |  |  |  |  |  |  |
| Electrical Characteristics |  |  |  |  |  |  |
| Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range |  |  |  |  |  |  |
| Symbol | Parameter | Conditions |  | LM2596-3.3 |  | Units(Limits) |
|  |  |  |  | Typ (Note 3) | Limit (Note 4) |  |
| SYSTEM PARAMETERS (Note 5) Test Circuit Figure 1 |  |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $4.75 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq$ | $\leq 3 \mathrm{~A}$ | 3.3 |  | V |
|  |  |  |  |  | 3.168/3.135 | $V(\min )$ |
|  |  |  |  |  | 3.432/3.465 | V (max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ |  | 73 |  | \% |

## LM2596-5.0

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range

| Symbol | Parameter | Conditions | LM2596-5.0 |  | Units <br> (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{gathered} \text { Typ } \\ \text { (Note 3) } \end{gathered}$ | Limit (Note 4) |  |
| SYSTEM PARAMETERS (Note 5) Test Circuit Figure 1 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ | 5.0 | $\begin{aligned} & \text { 4.800/4.750 } \\ & 5.200 / 5.250 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\min ) \\ \mathrm{V}(\max ) \end{gathered}$ |
| $\eta$ | Efficiency | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 80 |  | \% |

## LM2596-12

## Electrical Characteristics

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range

| Symbol | Parameter | Conditions | LM2596-12 |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{aligned} & \text { Typ } \\ & \text { (Note 3) } \end{aligned}$ | Limit (Note 4) |  |
| SYSTEM PARAMETERS (Note 5) Test Circuit Figure 1 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $15 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ | 12.0 | $\begin{aligned} & 11.52 / 11.40 \\ & 12.48 / 12.60 \end{aligned}$ |  |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=25 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 90 |  | \% |

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range

| Symbol | Parameter | Conditions | LM2596-ADJ |  | Units(Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{gathered} \text { Typ } \\ \text { (Note 3) } \end{gathered}$ | Limit (Note 4) |  |
| SYSTEM PARAMETERS (Note 5) Test Circuit Figure 1 |  |  |  |  |  |
| $\mathrm{V}_{\text {FB }}$ | Feedback Voltage | $4.5 \mathrm{~V} \leq \mathrm{V}_{\mathrm{IN}} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\mathrm{LOAD}} \leq 3 \mathrm{~A}$ <br> $\mathrm{V}_{\text {OUt }}$ programmed for 3 V . Circuit of Figure 1 | 1.230 | $\begin{aligned} & 1.193 / 1.180 \\ & 1.267 / 1.280 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\min ) \\ \mathrm{V}(\max ) \end{gathered}$ |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=3 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 73 |  | \% |

## All Output Voltage Versions

## Electrical Characteristics

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable version and $\mathrm{V}_{\mathrm{IN}}=24 \mathrm{~V}$ for the 12 V version. $\mathrm{I}_{\text {LOAD }}=500 \mathrm{~mA}$

| Symbol | Parameter | Conditions | LM2596-XX |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{gathered} \text { Typ } \\ \text { (Note 3) } \end{gathered}$ | Limit (Note 4) |  |
| DEVICE PARAMETERS |  |  |  |  |  |
| $\mathrm{I}_{\mathrm{b}}$ | Feedback Bias Current | Adjustable Version Only, $\mathrm{V}_{\text {FB }}=1.3 \mathrm{~V}$ | 10 | 50/100 | nA nA (max) |
| $\mathrm{f}_{0}$ | Oscillator Frequency | (Note 6) | 150 | $\begin{aligned} & 127 / 110 \\ & 173 / 173 \end{aligned}$ | $\begin{gathered} \hline \mathrm{kHz} \\ \mathrm{kHz}(\min ) \\ \mathrm{kHz}(\max ) \\ \hline \end{gathered}$ |
| $\mathrm{V}_{\text {SAT }}$ | Saturation Voltage | $\mathrm{l}_{\text {Out }}=3 \mathrm{~A}($ Notes 7, 8) | 1.16 | 1.4/1.5 | $\begin{gathered} \mathrm{V} \\ \mathrm{~V}(\max ) \end{gathered}$ |
| DC | Max Duty Cycle (ON) Min Duty Cycle (OFF) | (Note 8) (Note 9) | $\begin{gathered} 100 \\ 0 \end{gathered}$ |  | \% |
| $\overline{I_{C L}}$ | Current Limit | Peak Current (Notes 7, 8) | 4.5 | $\begin{aligned} & 3.6 / 3.4 \\ & 6.9 / 7.5 \end{aligned}$ | A A(min) A(max) |
| $\overline{\mathrm{I}}$ | Output Leakage Current | Output = 0V (Notes 7, 9) |  | 50 | $\mu \mathrm{A}(\mathrm{max})$ |
|  |  | Output $=-1 \mathrm{~V}$ ( Note 10) | 2 | 30 | $\begin{gathered} \mathrm{mA} \\ \mathrm{~mA}(\max ) \end{gathered}$ |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current | (Note 9) | 5 | 10 | $\begin{gathered} \mathrm{mA} \\ \mathrm{~mA}(\max ) \end{gathered}$ |
| $\mathrm{I}_{\text {STBY }}$ | Standby Quiescent Current | ON/OFF pin $=5 \mathrm{~V}$ (OFF) (Note 10) | 80 | 200/250 | $\begin{gathered} \mu \mathrm{A} \\ \mu \mathrm{~A}(\max ) \end{gathered}$ |
| $\begin{aligned} & \hline \theta_{\mathrm{JC}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \hline \end{aligned}$ | Thermal Resistance | TO-220 or TO-263 Package, Junction to Case TO-220 Package, Junction to Ambient (Note 11) TO-263 Package, Junction to Ambient (Note 12) TO-263 Package, Junction to Ambient (Note 13) TO-263 Package, Junction to Ambient (Note 14) | $\begin{gathered} 2 \\ 50 \\ 50 \\ 30 \\ 20 \end{gathered}$ |  | $\begin{aligned} & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \end{aligned}$ |
| ON/OFF CONTROL Test Circuit Figure 1 |  |  |  |  |  |
| $\begin{aligned} & \mathrm{V}_{\mathrm{IH}} \\ & \mathrm{~V}_{\mathrm{IL}} \end{aligned}$ | $\overline{\mathrm{ON}}$ /OFF Pin Logic Input Threshold Voltage | Low (Regulator ON) <br> High (Regulator OFF) | 1.3 | $\begin{aligned} & 0.6 \\ & 2.0 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\max ) \\ \mathrm{V}(\min ) \end{gathered}$ |

## All Output Voltage Versions <br> Electrical Characteristics (Continued)

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable version and $\mathrm{V}_{\mathrm{IN}}=24 \mathrm{~V}$ for the 12 V version. $\mathrm{I}_{\text {LOAD }}=500 \mathrm{~mA}$

| Symbol | Parameter | Conditions | LM2596-XX |  | Units <br> (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ (Note 3) | Limit (Note 4) |  |
| $\mathrm{I}_{\mathrm{H}}$ | $\overline{\text { ON }}$ /OFF Pin Input Current | $\mathrm{V}_{\text {LOGIC }}=2.5 \mathrm{~V}$ (Regulator OFF) | 5 | 15 |  |
| $\mathrm{I}_{\mathrm{L}}$ |  | $\mathrm{V}_{\text {LOGIC }}=0.5 \mathrm{~V}$ (Regulator ON) | 0.02 | 5 |  |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.
Note 2: The human body model is a 100 pF capacitor discharged through a 1.5 k resistor into each pin.
Note 3: Typical numbers are at $25^{\circ} \mathrm{C}$ and represent the most likely norm.
Note 4: All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are $100 \%$ production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
Note 5: External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the LM2596 is used as shown in the Figure 1 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.
Note 6: The switching frequency is reduced when the second stage current limit is activated.
Note 7: No diode, inductor or capacitor connected to output pin.
Note 8: Feedback pin removed from output and connected to 0 V to force the output transistor switch ON.
Note 9: Feedback pin removed from output and connected to 12 V for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and the ADJ. version, and 15 V for the 12 V version, to force the output transistor switch OFF.
Note 10: $\mathrm{V}_{\mathrm{IN}}=40 \mathrm{~V}$.
Note 11: Junction to ambient thermal resistance (no external heat sink) for the TO-220 package mounted vertically, with the leads soldered to a printed circuit board with ( 1 oz .) copper area of approximately $1 \mathrm{in}^{2}$.
Note 12: Junction to ambient thermal resistance with the TO-263 package tab soldered to a single printed circuit board with $0.5 \mathrm{in}^{2}$ of ( 1 oz .) copper area.
Note 13: Junction to ambient thermal resistance with the TO-263 package tab soldered to a single sided printed circuit board with $2.5 \mathrm{in}^{2}$ of ( 1 oz .) copper area.
Note 14: Junction to ambient thermal resistance with the TO-263 package tab soldered to a double sided printed circuit board with $3 \mathrm{in}^{2}$ of ( 1 oz .) copper area on the LM2596S side of the board, and approximately $16 \mathrm{in}^{2}$ of copper on the other side of the p-c board. See Application Information in this data sheet and the thermal model in Switchers Made Simple ${ }^{\text {TM }}$ version 4.3 software.

## Typical Performance Characteristics (Circuit of Figure 1)





Physical Dimensions inches (millimeters) unless otherwise noted (Continued)


LEAD POSITION OVERLAY


5-Lead TO-263 Surface Mount Package (S)
Order Number LM2596S-3.3, LM2596S-5.0,
LM2596S-12 or LM2596S-ADJ
NS Package Number TS5B

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