

LM1876 - Overture Audio Power Amplifier Series Dual 20-Watt Audio Power Amplifier with Mute and Standby Modes

Typical Application



Features

SPiKe protection

- Minimal amount of external components necessary
- Quiet fade-in/out mute mode
- Standby-mode
- Isolated 15-lead TO-220 package
- Non-Isolated 15-lead TO-220 package
- Wide supply range 20V 64V

Key Specification

THD+N at 1kHz at 2 x 15W continuous average	
output power into 4Ω or 8Ω :	0.1% (max)
THD+N at 1kHz at continuous average	
output power of 2 x 20W into 8Ω :	0.009% (typ)
Standby current:	4.2mA (typ)



Connection Diagram



Parametric Table

Power@ 4Ohms, 1% THD	23 Watt
Power@ 80hms, 1% THD	20 Watt
Power@ 4Ohms, 10% THD	29 Watt
Power@ 80hms, 10% THD	26 Watt
THD	0.08 %
Channels	2 Channels
THD Conditions	Po=15W @ Vs=20V
PSRR Conditions	Undefined

SNR Conditions	Undefined	

Typical Performance



Datasheet



Package Availability, Models

	Package			Factory Lead Time						Std	Package						
Part Number	Туре	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report	CAD Symbols	Weeks	Qty	Models				Pack Size	Marking Format		
LM1876T	TO-220	TO-220 15	876T TO-220	15	STD	1	NA RoHS N/A Full production	on	N/A		rail of	il NSUZXYTTE#					
			NOPB	1	NA			6 weeks	500					20	, LM1876T		
LM1876TF	ISOLATED TO220	ISOLATED TO220				STD	STD 1 NA	NA	Dello		Full producti	on	N1/0			rail	NSUZXYTTE#
			15	NOPB	1	NA	KOHS	N/A	6 weeks	1000	N/A				or 20	LM1876TF	

General Description

The LM1876 is a stereo audio amplifier capable of delivering typically 20W per channel of continuous average output power into a 4Ω or 8Ω load with less than 0.1% THD+N.

Each amplifier has an independent smooth transition fade-in/out mute and a power conserving standby mode which can be controlled by external logic.

The performance of the LM1876, utilizing its Self Peak Instantaneous Temperature (°Ke) (SPiKeTM) protection circuitry, places it in a class above discrete and hybrid amplifiers by providing an inherently, dynamically protected Safe Operating Area (SOA). SPiKe protection means that these parts are safeguarded at the output against overvoltage, undervoltage, overloads, including thermal runaway and instantaneous temperature peaks.

Reliability Metrics

Part Number	Process	EFR Reject	EFR Sample Size	PPM *	LTA Rejects	LTA Device Hours	FITS	MTTF (Hours)
LM1876T	HV700	0	13580	0	0	1222500	3	346887713
LM1876TF	HV700	0	13580	0	0	1222500	3	346887713



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THD+N at 1kHz at 2 x 15W continuous average output power into 4Ω or 8Ω: 0.1% (max)
THD N at 1kHz at continuous average

THD+N at TKHZ at continuous average	
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Features

- SPiKe protection
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- Quiet fade-in/out mute mode
- Standby-mode
- Isolated 15-lead TO-220 package
- Non-Isolated 15-lead TO-220 package
- Wide supply range 20V 64V

Applications

- High-end stereo TVs
- Component stereo
- Compact stereo

Connection Diagram



Top View Isolated Package Order Number LM1876TF See NS Package Number TF15B Non-Isolated Package Order Number LM1876T See NS Package Number TA15A

SPiKe[™] Protection and Overture[™] are trademarks of National Semiconductor Corporation.

Absolute Maximum Ratings (Notes 4,

5)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage V _{CC} + V _{EE}	
(No Input)	64V
Supply Voltage $ V_{CC} + V_{EE} $	
(with Input)	64V
Common Mode Input Voltage	$(V_{CC} \text{ or } V_{EE})$ and
	$ V_{CC} + V_{EE} \le$
	54V
Differential Input Voltage	54V
Output Current	Internally Limited
Power Dissipation (Note 6)	62.5W
ESD Susceptability (Note 7)	2000V
Junction Temperature (Note 8)	150°C

Thermal Resistance	
Isolated TF-Package	
θ_{JC}	2°C/W
Non-Isolated T-Package	
θ_{JC}	1°C/W
Soldering Information	
TF Package (10 sec.)	260°C
Storage Temperature	–40°C to +150°C

Operating Ratings (Notes 4, 5)

Temperature Range	
$T_{MIN} \le T_A \le T_{MAX}$	$-20^{\circ}C \le T_A \le$
	+85°C
Supply Voltage $ V_{CC} + V_{EE} $ (Note	
1)	20V to 64V

Electrical Characteristics (Notes 4, 5)

The following specifications apply for V_{CC} = +22V, V_{EE} = -22V with R_L = 8 Ω unless otherwise specified. Limits apply for T_A = 25°C.

Symbol	Parameter	Conditions	LM1876		Units
			Typical	Limit	(Limits)
			(Note 9)	(Note 10)	
IV _{CC} I +	Power Supply Voltage	$GND - V_{EE} \ge 9V$		20	V (min)
$ V_{EE} $	(Note 11)			64	V (max)
Po	Output Power	THD + N = 0.1% (max),			
(Note 3)	(Continuous Average)	f = 1 kHz			
		$ V_{CC} = V_{EE} = 22V, R_L = 8\Omega$	20	15	W/ch (min)
		$ V_{CC} = V_{EE} = 20V, R_L = 4\Omega$ (Note 13)	22	15	W/ch (min)
THD + N	Total Harmonic Distortion	15 W/ch, $R_L = 8\Omega$	0.08		%
	Plus Noise	15 W/ch, $R_L = 4\Omega$, $ V_{CC} = V_{EE} = 20V$	0.1		%
		20 Hz \leq f \leq 20 kHz, A_V = 26 dB			
X _{talk}	Channel Separation	$f = 1 \text{ kHz}, V_{O} = 10.9 \text{ Vrms}$	80		dB
SR	Slew Rate	V_{IN} = 1.414 Vrms, t_{rise} = 2 ns	18	12	V/µs (min)
(Note 3)					
I _{total}	Total Quiescent Power	Both Amplifiers $V_{CM} = 0V$,			
(Note 2)	Supply Current	$V_{O} = 0V, I_{O} = 0 mA$			
		Standby: Off	50	80	mA (max)
		Standby: On	4.2	6	mA (max)
V _{OS}	Input Offset Voltage	$V_{CM} = 0V, I_O = 0 \text{ mA}$	2.0	15	mV (max)
(Note 2)					
Ι _Β	Input Bias Current	$V_{CM} = 0V, I_O = 0 \text{ mA}$	0.2	0.5	μA (max)
l _{os}	Input Offset Current	$V_{CM} = 0V, I_O = 0 \text{ mA}$	0.002	0.2	μA (max)
lo	Output Current Limit	$ V_{CC} = V_{EE} = 10V, t_{ON} = 10 \text{ ms},$	3.5	2.9	Apk (min)
		$V_{O} = 0V$			
V _{OD}	Output Dropout Voltage	$ V_{CC}-V_{O} , V_{CC} = 20V, I_{O} = +100 \text{ mA}$	1.8	2.3	V (max)
(Note 2)	(Note 12)	$ V_{O}-V_{EE} , V_{EE} = -20V, I_{O} = -100 \text{ mA}$	2.5	3.2	V (max)
PSRR	Power Supply Rejection Ratio	$V_{CC} = 25V$ to 10V, $V_{EE} = -25V$,	115	85	dB (min)
(Note 2)		$V_{CM} = 0V, I_O = 0 \text{ mA}$			
		$V_{CC} = 25V, V_{EE} = -25V \text{ to } -10V$	110	85	dB (min)
		$V_{CM} = 0V, I_O = 0 \text{ mA}$			

Electrical Characteristics (Notes 4, 5) (Continued)

The following specifications apply for V_{CC} = +22V, V_{EE} = -22V with R_L = 8 Ω unless otherwise specified. Limits apply for T_A = 25°C.

Symbol	Parameter	Conditions	LN	Units	
			Typical	Limit	(Limits)
			(Note 9)	(Note 10)	
CMRR	Common Mode Rejection Ratio	$V_{\rm CC} = 35V$ to 10V, $V_{\rm EE} = -10V$ to $-35V$,	110	80	dB (min)
(Note 2)		V_{CM} = 10V to -10V, I_O = 0 mA			
A _{VOL} (Note 2)	Open Loop Voltage Gain	$R_L = 2 k\Omega, \Delta V_O = 20 V$	110	90	dB (min)
GBWP	Gain Bandwidth Product	$f_{O} = 100 \text{ kHz}, V_{IN} = 50 \text{ mVrms}$	7.5	5	MHz (min)
e _{IN}	Input Noise	IHF—A Weighting Filter	2.0	8	μV (max)
(Note 3)		$R_{IN} = 600\Omega$ (Input Referred)			
SNR	Signal-to-Noise Ratio	$P_{O} = 1W, A$ —Weighted,	98		dB
		Measured at 1 kHz, $R_S = 25\Omega$			
		$P_{O} = 15W, A - Weighted$	108		dB
		Measured at 1 kHz, $R_S = 25\Omega$			
A _M	Mute Attenuation	Pin 6,11 at 2.5V	115	80	dB (min)
Standby Pin					
VIL	Standby Low Input Voltage	Not in Standby Mode		0.8	V (max)
V _{IH}	Standby High Input Voltage	In Standby Mode	2.0	2.5	V (min)
Mute pin					
VIL	Mute Low Input Voltage	Outputs Not Muted		0.8	V (max)
V _{IH}	Mute High Input Voltage	Outputs Muted	2.0	2.5	V (min)

Note 1: Operation is guaranteed up to 64V, however, distortion may be introduced from SPiKe Protection Circuitry if proper thermal considerations are not taken into account. Refer to the Application Information section for a complete explanation.

Note 2: DC Electrical Test; Refer to Test Circuit #1.

Note 3: AC Electrical Test; Refer to Test Circuit #2.

Note 4: All voltages are measured with respect to the GND pins (5, 10), unless otherwise specified.

Note 5: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

Note 6: For operating at case temperatures above 25°C, the device must be derated based on a 150°C maximum junction temperature and a thermal resistance of $\theta_{JC} = 2$ °C/W (junction to case) for the TF package and $\theta_{JC} = 1$ °C/W for the T package. Refer to the section Determining the Correct Heat Sink in the Application Information section.

Note 7: Human body model, 100 pF discharged through a 1.5 $k\Omega$ resistor.

Note 8: The operating junction temperature maximum is 150°C, however, the instantaneous Safe Operating Area temperature is 250°C.

Note 9: Typicals are measured at 25°C and represent the parametric norm.

Note 10: Limits are guarantees that all parts are tested in production to meet the stated values.

Note 11: V_{EE} must have at least -9V at its pin with reference to ground in order for the under-voltage protection circuitry to be disabled. In addition, the voltage differential between V_{CC} and V_{EE} must be greater than 14V.

Note 12: The output dropout voltage, V_{OD}, is the supply voltage minus the clipping voltage. Refer to the Clipping Voltage vs. Supply Voltage graph in the Typical Performance Characteristics section.

Note 13: For a 4Ω load, and with $\pm 20V$ supplies, the LM1876 can deliver typically 22W of continuous average output power with less than 0.1% (THD + N). With supplies above $\pm 20V$, the LM1876 cannot deliver more than 22W into a 4Ω due to current limiting of the output transistors. Thus, increasing the power supply above $\pm 20V$ will only increase the internal power dissipation, not the possible output power. Increased power dissipation will require a larger heat sink as explained in the Application Information section.



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