

LM111 - Voltage Comparator



PowerWisi



Features

- Operates from single 5V supply
- Input current: 150 nA max. over temperature
- Offset current: 20 nA max. over temperature
- Differential input voltage range: ±30V
- Power consumption: 135 mW at ±15V

Typical Application



Connection Diagram



Parametric Table

0.2, 0.1 us
Open Drain
5 Volt
36 Volt
1 Channels
3 mV
50 mA
Not R-R
5.1 mA
1020, 510 uA x us
150 nA
Offset Adjust, Strobe
-55 deg C
125 deg C
Comparator
0

Typical Performance





Package Availability, Models

			Packa	ge			Factory	Factory Lead				Std	Package				
Part Number	Туре	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report		Weeks	Qty	Models				Pack Size	Marking Format		
LM111J-8	CERDIP	8	STD	1	NA	RoHS		Full produ	uction 500	LM111.MOD				rail of	NSUZXYTT LM111J		
і м111н	TO 00	8	STD	1	NA	RoHS		Full produ	uction					box	-o		
	10-99	0	NOPB	1	NA	KUIIS		6 weeks	2000					500	NSZATTTE# LIWITTIT		
LM111 MD8			Unj	package	ed Die			Full produ N/A	uction 5000	LM111.MOD				tray of N/A	-		
LM111 MW8	Wafer					Full prod	uction	LM111.MOD				wafer jar	-				
			1					N/A	10000					N/A	NEZEEVVVA		
5962-8687701Q2A (LM111E-SMD)	LCC	20				RoHS		Obsole 10 weeks	ete 100	LM111.MOD				of 50	Q 5962- 8687701Q2A		
LM111E/883	LCC	20	STD	1	NA	RoHS		Obsole 8 weeks	ete 500	LM111.MOD				rail of 50	NSZSSXXYYA LM111E /883 Q		
5962-8687701QGA (LM111H-SMD)	TO-99	8				RoHS		Obsole 8 weeks	ete 100	LM111.MOD				tray of 20	NSZSSXXYYA Q 5962-8687701QGA		
LM111H/883	TO-99	8	STD	1	NA	RoHS		Full produ	uction	LM111.MOD				tray of	NSZSSXXYYA LM111H/883 Q		
5962-8687701QPA (LM111J-8-SMD)	CERDIP	8				RoHS		Obsole 8 weeks	ete	LM111.MOD				rail of	NSZSSXXYYA Q 5962		
LM111J-8/883	CERDIP	8	STD	1	NA	RoHS		Full produ	uction	LM111.MOD				40 rail of	NSZSSXXYYA LM111J-8		
LM111J/883	CERDIP	14	STD	1	NA	RoHS		7 weeks Full produ	uction	LM111.MOD				40 rail of	/883 Q NSZSSXXYYA		
		-						6 weeks	1000					25	NS		
LM111W/883	CERPACK	10	STD	1	NA	RoHS		10 weeks	100	LM111.MOD				raii of 19	LM111W /883 Q ZSSXXYYA		
5962-8687701QZA	CERDACK	10				Dolle		Obsole	ete					tray	NSZSS XXYYA		
(LM111WG-SMD)	CERPACK	10				KUHS		10 weeks	500					54	868770 1QZA Q		
LM111WG/883	CERPACK	10	STD	1	NA	RoHS		Full produ	Locion	LM111.MOD				tray of	NS LM111WG /883 Q		
JM38510/10304BGA	TO 99	0				PollS		Full produ	uction	N/A				54 tray	ZSSXXYYA NS ZSSXXYYA 27014 Q		
(JL111BGA)	10-99	°				КОПЗ		8 weeks	500	N/A				20 20	JM38510/10304BGA NS JM38510		
JM38510/10304BPA (JL111BPA)	CERDIP	8				RoHS		8 weeks	100	N/A				of 40	/10304BPA 27014 Q ZSSXXYYA		
JM38510/10304BCA (JL111BCA)	CERDIP	14				RoHS		Obsole 6 weeks	ete 500	N/A				rail of 25	NS ZSSXXYYA JM38510/10304BCA 27014 O		
IM38510/1030/BHA								Obsole	ete					rail	NS JM38510/		
(JL111BHA)	CERPACK	10				RoHS		13 weeks	500	N/A				of 19	10304BHA 27014 Q ZSSXXYYA		
JL111SGA	TO-99	8	STD	1	NA	RoHSo		Obsole N/A	ete 100	N/A				tray of N/A	NS ZSSXXYYA JM38510/10304SGA		
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LM111HPQMLV	TO-99	8	STD	1	NA	RoHS		Obsol	ete	LM111.MOD				of N/A	LM111HPQV Q 5962P0052401VGA							
		┢──						Full prod	uction					tray	NSZSSXXYYA							
5962L0052401VGA (LM111HLQMLV)	TO-99	TO-99 8				RoHS		N/A	500	LM111.MOD				of 20	LM111HLQV Q 5962L0052401VGA							
5962R0052402VGA	TO 00		1			Dalle		Full prod	uction					tray	NSZSSXXYYA							
(LM111HRLQMLV)	10-99	8				ROHS		6 weeks	100					20	5962R0052402VGA							
								Obsol	ete				ĺ	rail	NSZSSXXYYA							
JL111SPA	CERDIP	8	STD	1	NA	RoHS				N/A				of	JM38510 /10304SPA							
			<u> </u>			ļ		N/A	N/A					N/A	27014 Q							
5962L0052401VPA								Full prod	uction					rail	NSZSSXXYYA LM111J-8L							
(LM111J-8LQMLV)	CERDIP	8				RoHS		N/A	100	LM111.MOD				of 40	QV Q 5962L							
		-	-												NSZSSXXYYA							
5962R0052402VPA	CERDIP	8				RoHS	D	Full prod	uction	LM111.MOD				of	LM111J-8RL							
(LIVITTJ-8RLQIVILV)								N/A	N/A					40	0052402VPA							
	<u> </u>		1					Obsol	ete					an il	NS							
JL111SHA	CERPACK	10	STD	1	NA	RoHS				N/A				of	/10304SHA							
								N/A	50					N/A	27014 Q							
			-			ļ									NSLM111W							
5962L0052401VHA	огранск	10				Dalle		Full prod	uction					rail	LQV Q							
(LM111WLQMLV)	CERPACK	10				ROHS		N/A	50				19	5962L005								
		<u> </u>	-			<u> </u>				ļ ļ					2401VHA							
5962R0052402VHA (LM111WRLQMLV)								Full prod	uction					rail	RLQV Q							
	CERPACK	10				RoHS		<u> </u>	_	LM111.MOD				of 10	ZSSXXYYA							
								N/A	N/A					19	2402VHA							
								Obsol	ete					trov	NS IM28510							
JL111SZA	CERPACK	10	STD	1	NA	RoHS				N/A				of	\10304SZA							
										N/A	N/A					N/A	27014 Q 755XXXXA					
		┢──													NSLM111W							
	огранск	10	OTD	4	NIA	Dalle		Obsol	ete					tray	GPQV Q							
LIVITTIVGPQIVILV	CERPACK	1 701 10		10	510	1	NA	ROHS	ſ	N/A	N/A					N/A	2401VZA					
		<u> </u>	I			ļ									ZSSXXYYA							
		PACK 10						Full prod	uction					tray	GLQV Q							
(LM111WGLQMLV)	CERPACK			RPACK 10		PACK 10		PACK 10				RoHS				LM111.MOD				of	5962L005	
								N/A	50					54	ZSSXXYYA							
]					Full prod	uction					trov	NSLM111W							
5962R0052402VZA	CERPACK	ACK 10	PACK 10		PACK 10	PACK 10		ACK 10	СК 10				RoHS				LM111.MOD				of	5962R005
								N/A	100					54	2402VZA							
Obsolete Versid	ons							I		II					200///11//							
Obsolete Part	Alte	ernat	te Part	or Sup	plier			Sourc	е		Last Tim	e Bu	y Date									
LM111E-MLS	JL111S	ZA				Nation	nal Semico	nductor			12/06/2005											
LM111E-SMD	LM111\	/VG/8	383			NATIC	DNAL SEM		FOR CC	RP	12/03/2008											
LM111E-SMD	UA111	NO	202				DNAL SEM				12/03/2008											
LIVIIIIE/003		/VG/0	000				JNAL SEM				12/03/2008											
LIVITTE/003							JNAL SEM			JRP	12/03/2008											
	TH-MLS UL111SGA				NONE	JNAL SEIV	ICONDUC	IUK		03/06/99												
									02/21/2006													
		None				/1 \1	09/06/2005															
I M111.I						NATIO			FOR		12/07/02											
LM111,I-8-SMD	M111	J_8/8	83			NATIO	NAL SEM)RP	12/03/2008											
LM111,I-8PQMLV	JI 1119	PA				NATIONAL SEMICONDUCTOR CORP					12/06/2005											
LM111W-MLS	JL111S	HA				NSC					03/05/2008											
LM111WG-SMD	UA111	1				NATIC	DNAL SEM		FOR CC	RP	11/16/2008											
LM111WGPQMI V	A111					NATIC	DNAL SEM		FOR		09/06/2005											
LM111WPQMLV	None					None					09/06/2005											
LM111W/883 UA111				NATIC	NATIONAL SEMICONDUCTOR CORP																	

General Description

The LM111, LM211 and LM311 are voltage comparators that have input currents nearly a thousand times lower than devices like the LM106 or LM710. They are also designed to operate over a wider range of supply voltages: from standard ±15V op amp supplies down to the single 5V supply used for IC logic. Their output is compatible with RTL, DTL and TTL as well as MOS circuits. Further, they can drive lamps or relays, switching voltages up to 50V at currents as high as 50 mA.

Both the inputs and the outputs of the LM111, LM211 or the LM311 can be isolated from system ground, and the output can drive loads referred to ground, the positive supply or the negative supply. Offset balancing and strobe capability are provided and outputs can be wire OR'ed. Although slower than the LM106 and LM710 (200 ns response time vs 40 ns) the devices are also much less prone to spurious oscillations. The LM111 has the same pin configuration as the LM106 and LM710. Downloaded from Elcodis.com



LM111/LM211/LM311 **Voltage Comparator 1.0 General Description**

The LM111, LM211 and LM311 are voltage comparators that have input currents nearly a thousand times lower than devices like the LM106 or LM710. They are also designed to operate over a wider range of supply voltages: from standard ±15V op amp supplies down to the single 5V supply used for IC logic. Their output is compatible with RTL, DTL and TTL as well as MOS circuits. Further, they can drive lamps or relays, switching voltages up to 50V at currents as high as 50 mA.

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3.0 Typical Applications (Note 3)







The LM211 is identical to the LM111, except that its performance is specified over a -25°C to +85°C temperature range instead of -55°C to +125°C. The LM311 has a temperature range of 0°C to +70°C.

2.0 Features

- Operates from single 5V supply
- Input current: 150 nA max. over temperature
- Offset current: 20 nA max. over temperature
- Differential input voltage range: ±30V
- Power consumption: 135 mW at ±15V



Note: Do Not Ground Strobe Pin. Output is turned off when current is pulled from Strobe Pin.

Detector for Magnetic Transducer



Increasing Input Stage Current (Note 1)



Note 1: Increases typical common mode slew from 7.0V/µs to 18V/µs.

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4.0 Absolute Maximum Ratings for the LM111/LM211(Note 10)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage (V ₈₄)	36V
Output to Negative Supply Voltage	
(V ₇₄)	50V
Ground to Negative Supply Voltage	
(V ₁₄)	30V
Differential Input Voltage	±30V
Input Voltage (Note 4)	±15V
Output Short Circuit Duration	10 sec
Operating Temperature Range	

LM111	–55°C to 125°C
LM211	–25°C to 85°C
Lead Temperature (Soldering, 10 sec)	260°C
Voltage at Strobe Pin	V+-5V
Soldering Information	
Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
See AN-450 "Surface Mounting Methods	and Their Effect
on Product Reliability" for other methods	of soldering
surface mount devices.	

ESD Rating (Note 11)

Electrical Characteristics (Note 6) for the LM111 and LM211

Parameter	Conditions	Min	Тур	Max	Units
Input Offset Voltage (Note 7)	T _A =25°C, R _S ≤50k		0.7	3.0	mV
Input Offset Current	T _A =25°C		4.0	10	nA
Input Bias Current	T _A =25°C		60	100	nA
Voltage Gain	T _A =25°C	40	200		V/mV
Response Time (Note 8)	T _A =25°C		200		ns
Saturation Voltage	V _{IN} ≤–5 mV, I _{OUT} =50 mA		0.75	1.5	V
	T _A =25°C				
Strobe ON Current (Note 9)	T _A =25°C		2.0	5.0	mA
Output Leakage Current	V _{IN} ≥5 mV, V _{OUT} =35V		0.2	10	nA
	T _A =25°C, I _{STROBE} =3 mA				
Input Offset Voltage (Note 7)	R _S ≤50 k			4.0	mV
Input Offset Current (Note 7)				20	nA
Input Bias Current				150	nA
Input Voltage Range	V ⁺ =15V, V ⁻ =-15V, Pin 7	-14.5	13.8,-14.7	13.0	V
	Pull-Up May Go To 5V				
Saturation Voltage	V ⁺ ≥4.5V, V [−] =0		0.23	0.4	V
	V _{IN} ≤–6 mV, I _{OUT} ≤8 mA				
Output Leakage Current	V _{IN} ≥5 mV, V _{OUT} =35V		0.1	0.5	μA
Positive Supply Current	T _A =25°C		5.1	6.0	mA
Negative Supply Current	T _A =25°C		4.1	5.0	mA

Note 4: This rating applies for ±15 supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.

Note 5: The maximum junction temperature of the LM111 is 150°C, while that of the LM211 is 110°C. For operating at elevated temperatures, devices in the H08 package must be derated based on a thermal resistance of 165°C/W, junction to ambient, or 20°C/W, junction to case. The thermal resistance of the dual-in-line package is 110°C/W, junction to ambient.

Note 6: These specifications apply for $V_S=\pm 15V$ and Ground pin at ground, and $-55^{\circ}C \leq T_A \leq +125^{\circ}C$, unless otherwise stated. With the LM211, however, all temperature specifications are limited to $-25^{\circ}C \leq T_A \leq +85^{\circ}C$. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to $\pm 15V$ supplies.

Note 7: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1 mA load. Thus, these parameters define an error band and take into account the worst-case effects of voltage gain and R_S.

Note 8: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

Note 9: This specification gives the range of current which must be drawn from the strobe pin to ensure the output is properly disabled. Do not short the strobe pin to ground; it should be current driven at 3 to 5 mA.

Note 10: Refer to RETS111X for the LM111H, LM111J and LM111J-8 military specifications.

Note 11: Human body model, 1.5 k Ω in series with 100 pF.

3

300V

5.0 Absolute Maximum Ratings for

the LM311 (Note 12)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage (V ₈₄)	36V
Output to Negative Supply Voltage	
(V ₇₄)	40V
Ground to Negative Supply Voltage	
(V ₁₄)	30V
Differential Input Voltage	±30V
Input Voltage (Note 13)	±15V
Power Dissipation (Note 14)	500 mW
ESD Rating (Note 19)	300V

Output Short Circuit Duration	10 sec					
Operating Temperature Range	0° to 70°C					
Storage Temperature Range	–65°C to 150°C					
Lead Temperature (soldering, 10 sec)	260°C					
Voltage at Strobe Pin	V+-5V					
Soldering Information						
Dual-In-Line Package						
Soldering (10 seconds)	260°C					
Small Outline Package						
Vapor Phase (60 seconds)	215°C					
Infrared (15 seconds)	220°C					
See AN-450 "Surface Mounting Methods and Their Effect						
on Product Reliability" for other methods of soldering						

surface mount devices.

Electrical Characteristics (Note 15) for the LM311

Parameter	Conditions	Min	Тур	Max	Units
Input Offset Voltage (Note 16)	T _A =25°C, R _S ≤50k		2.0	7.5	mV
Input Offset Current(Note 16)	T _A =25°C		6.0	50	nA
Input Bias Current	T _A =25°C		100	250	nA
Voltage Gain	T _A =25°C	40	200		V/mV
Response Time (Note 17)	T _A =25°C		200		ns
Saturation Voltage	V _{IN} ≤−10 mV, I _{OUT} =50 mA		0.75	1.5	V
	T _A =25°C				
Strobe ON Current (Note 18)	T _A =25°C		2.0	5.0	mA
Output Leakage Current	V _{IN} ≥10 mV, V _{OUT} =35V				
	T _A =25°C, I _{STROBE} =3 mA		0.2	50	nA
	$V^{-} = Pin \ 1 = -5V$				
Input Offset Voltage (Note 16)	R _s ≤50K			10	mV
Input Offset Current (Note 16)				70	nA
Input Bias Current				300	nA
Input Voltage Range		-14.5	13.8,–14.7	13.0	V
Saturation Voltage	V+≥4.5V, V ⁻ =0		0.23	0.4	V
	V _{IN} ≤−10 mV, I _{OUT} ≤8 mA				
Positive Supply Current	T _A =25°C		5.1	7.5	mA
Negative Supply Current	T _A =25°C		4.1	5.0	mA

Note 12: "Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits."

Note 13: This rating applies for ±15V supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.

Note 14: The maximum junction temperature of the LM311 is 110°C. For operating at elevated temperature, devices in the H08 package must be derated based on a thermal resistance of 165°C/W, junction to ambient, or 20°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

Note 15: These specifications apply for $V_S=\pm 15V$ and Pin 1 at ground, and 0°C < T_A < +70°C, unless otherwise specified. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to $\pm 15V$ supplies.

Note 16: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with 1 mA load. Thus, these parameters define an error band and take into account the worst-case effects of voltage gain and R_S.

Note 17: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

Note 18: This specification gives the range of current which must be drawn from the strobe pin to ensure the output is properly disabled. Do not short the strobe pin to ground; it should be current driven at 3 to 5 mA.

Note 19: Human body model, 1.5 k Ω in series with 100 pF.





